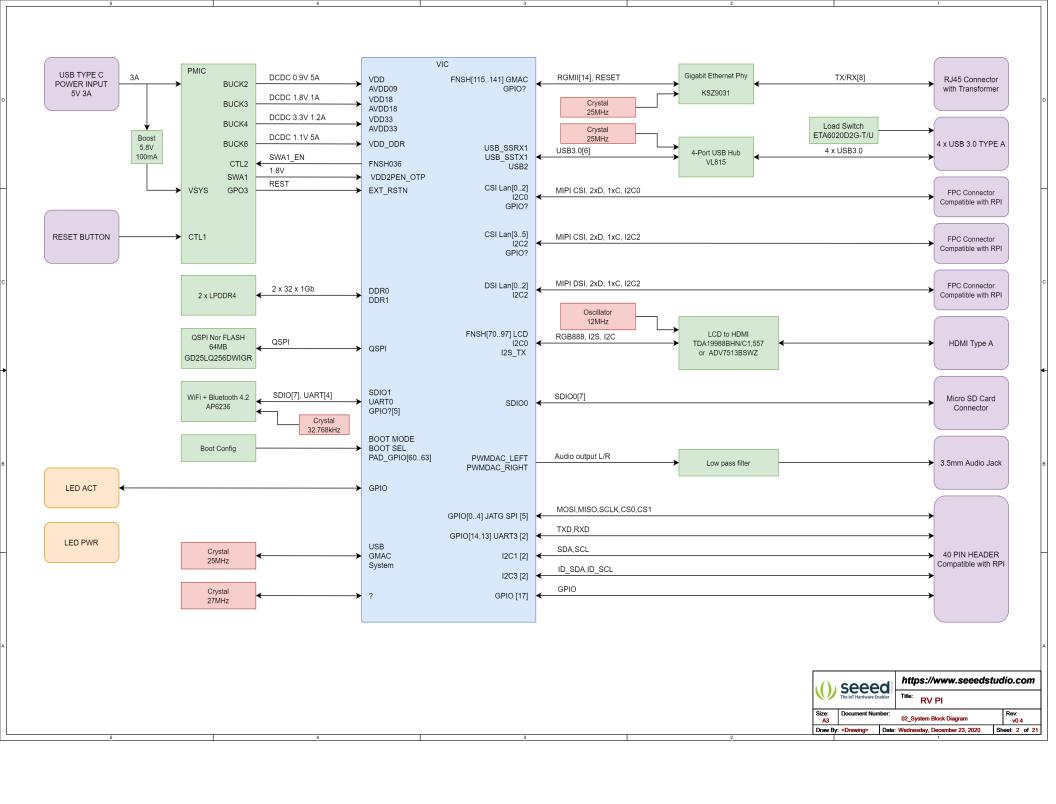
# Schematic: Expansion Accessory

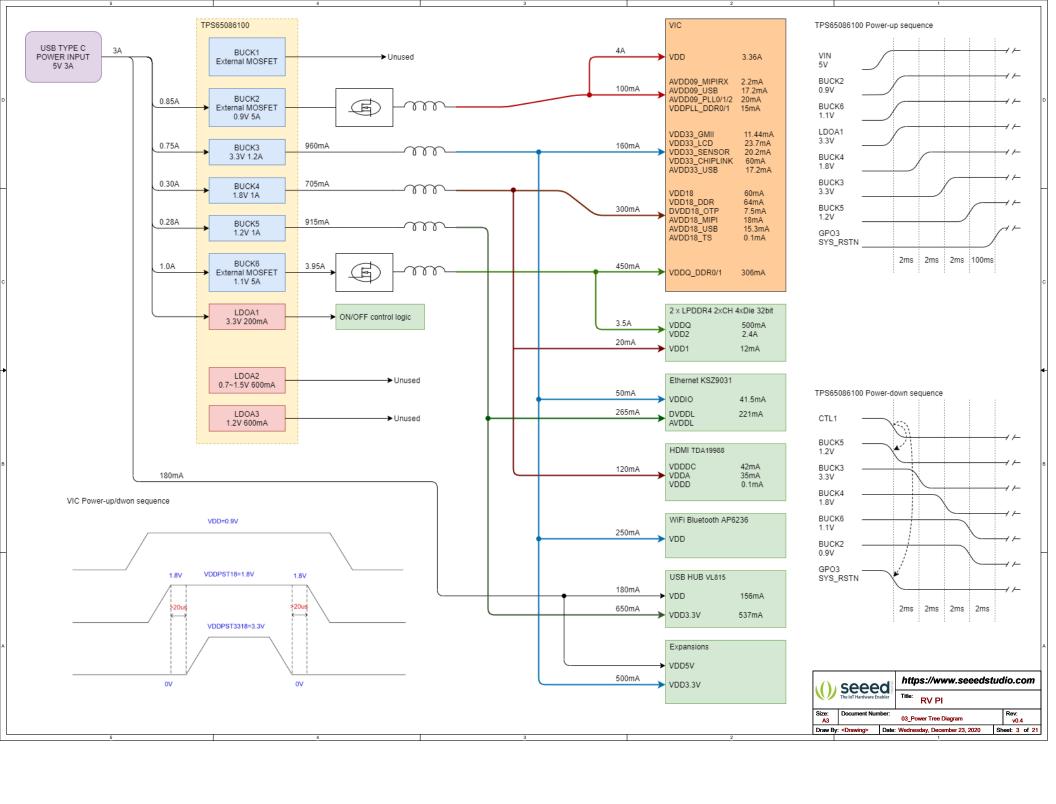
SHEET	SHEET NAME						
01	Title/Revision History						
02	System Block Diagram						
03	Power Tree Diagram						
04	PMIC						
05	VIC Power						
06	VIC DDR Ctrl						
07	VIC ChipLink & CM						
08	VIC LCD & GMII & GPIOs						
09	VIC HighIF & Ctrl Other						
10	LPDDR (A)						
11	LPDDR (B)						
12	Type C,uSD, QSIP Flash						
13	USB 3.0 HUB						
14	2 x USB TYPE A (A)						
15	2 x USB TYPE A (B)						
16	2 x CSI CONN						
17	DSI, AUDIO JACK						
18	HDMI FRAMER						
19	WiFi, Bluetooth						
20	10/100/1000 ETHERNET						
21	RPI EXP CONN, LED, BUTTON						

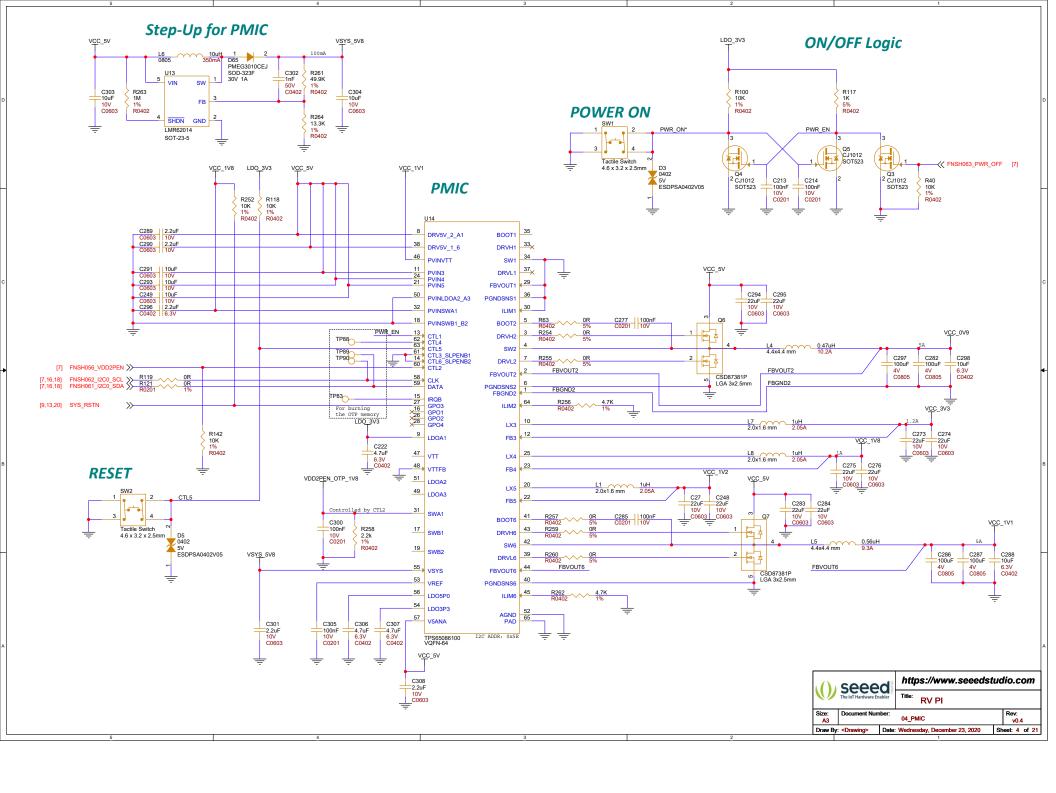
## Revision History

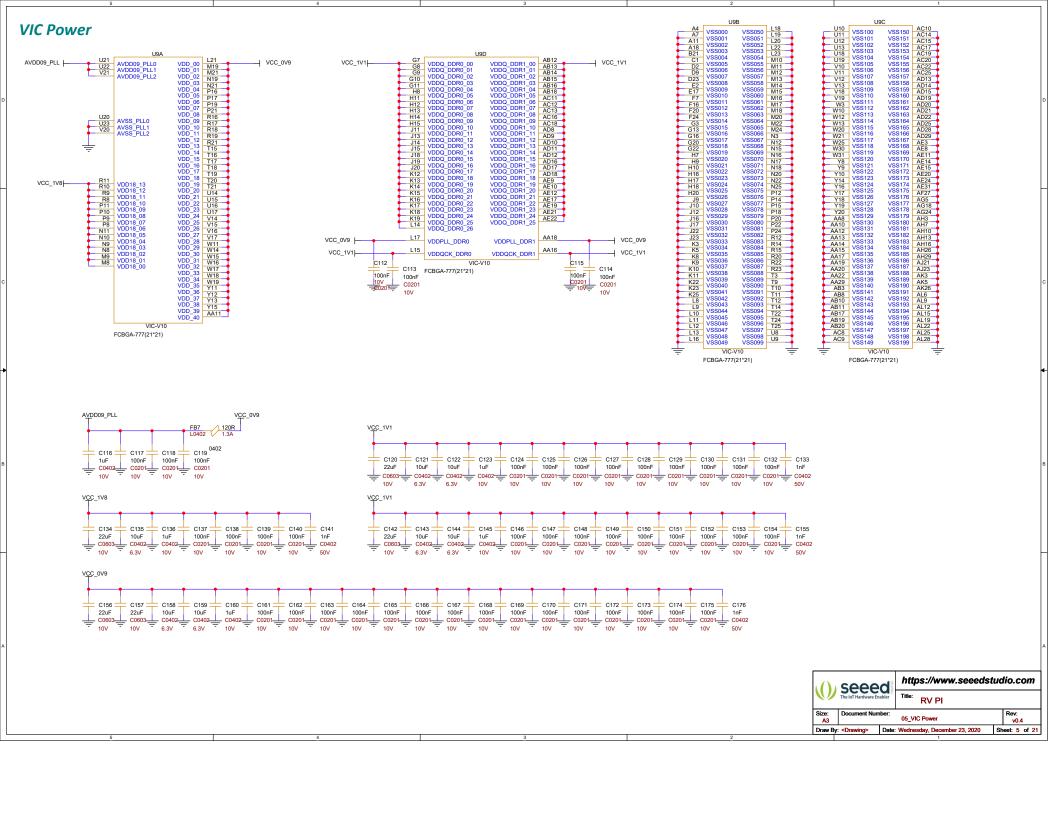
DATE	REVISION	DESCRIPTION				
Nov. 23 2020	v0.1	1. Initial release				
Nov. 25 2020	v0.2	1. Add USB Hub circuit 2. Modify reset circuit of KSZ9031, removed interrupt function 3. Change 1V2 output form LDO3 to BUCK5				
Nov. 25 2020	v0.3	1. Add Power down control and power on button				
Dec. 1 2020	v0.4	1. Add reset button				
Dec. 23 2020	v0.5	1. Add U74 JTAG connector for Tag-Connect cable 2. Modified GPIO assignment for better layout				



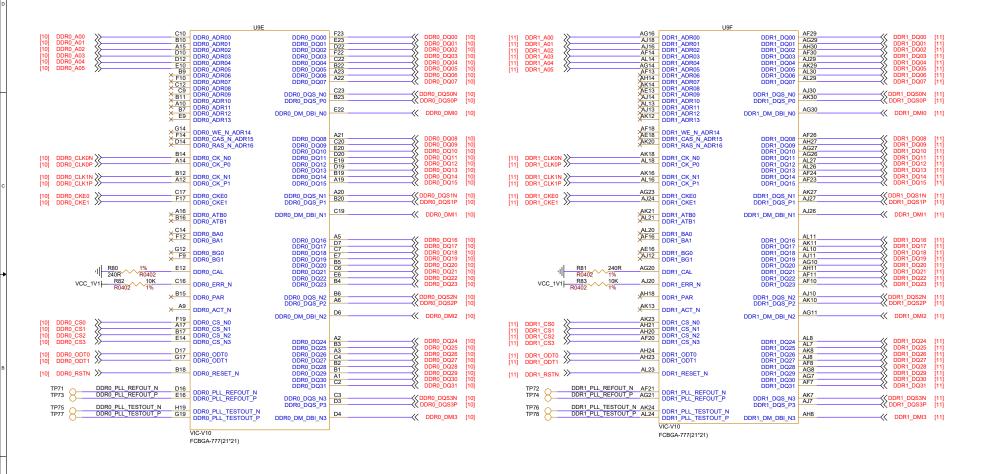




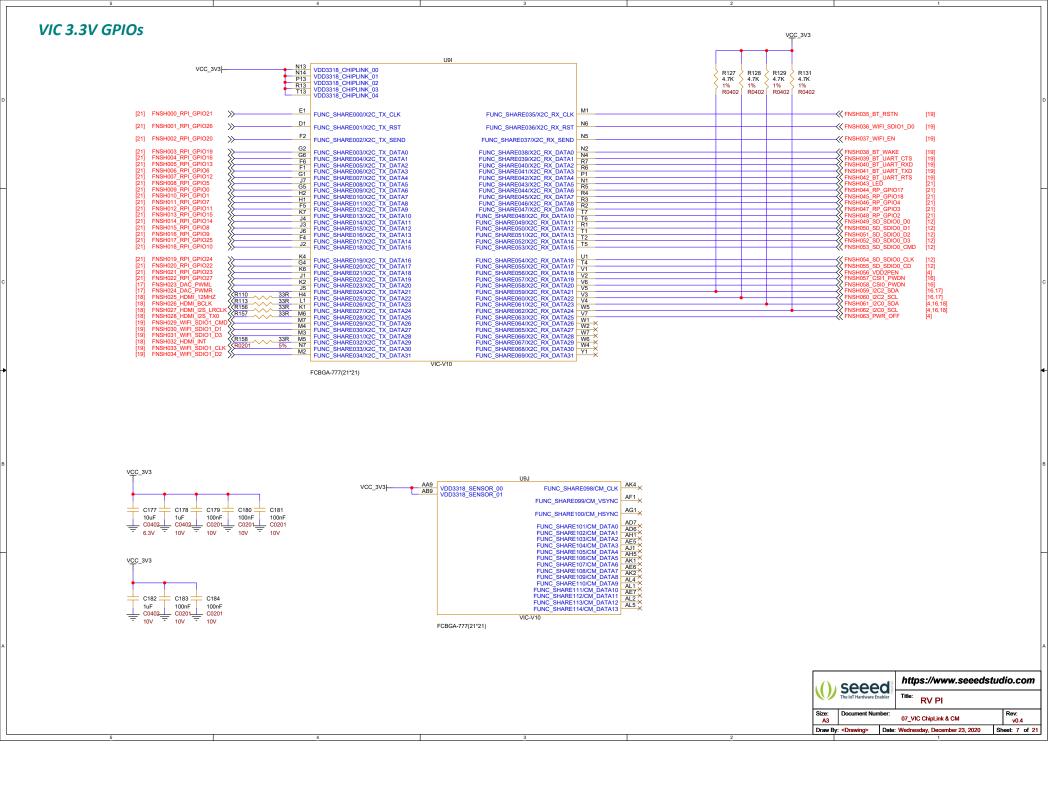


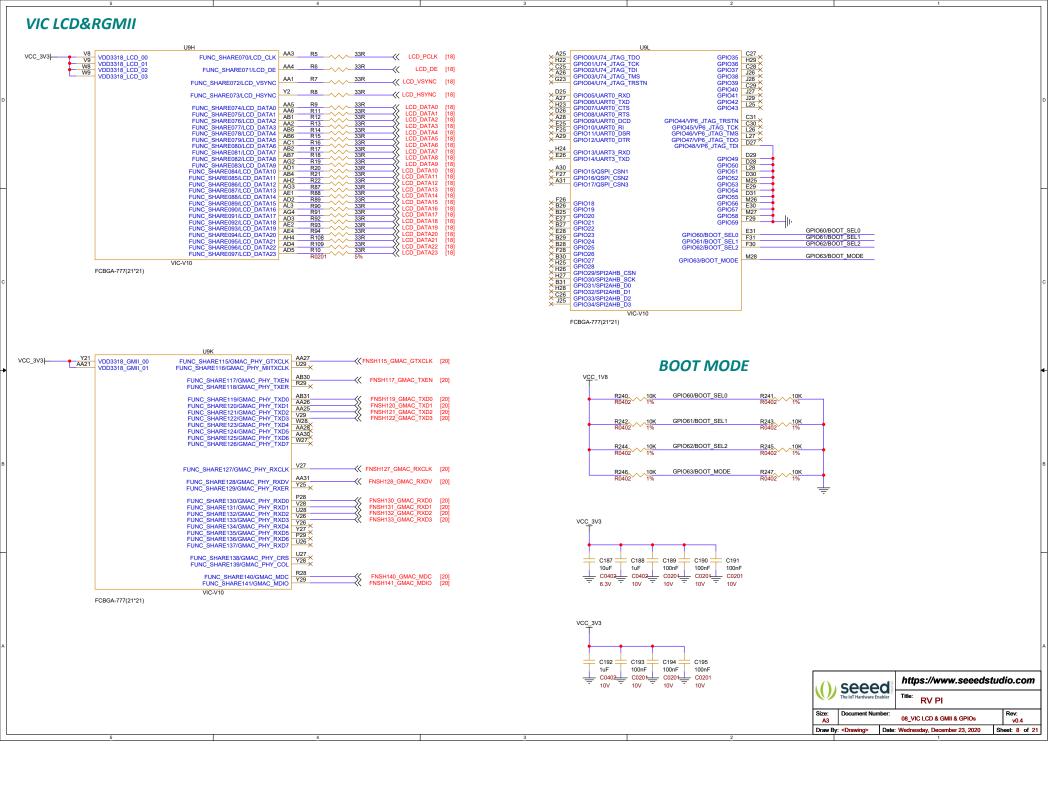


#### VIC DDR controller



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			nber:	06_VIC DDR Ctrl	Rev: v0.4		
	Draw By	: <drawing></drawing>	Date:	Wednesday, December 23, 2020	Sheet: 6 of 21		
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#### **VIC MIPI&USB**

VCC\_1V8

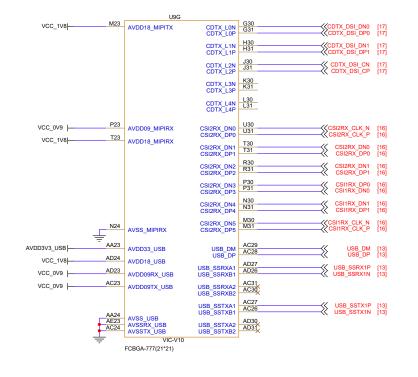
C202

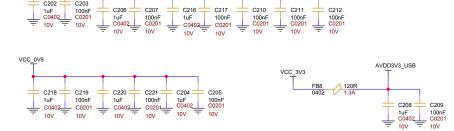
C203

C206 =

C207

C216 =





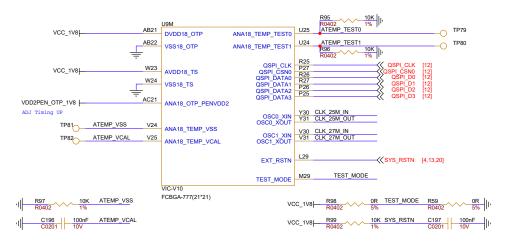
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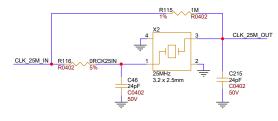
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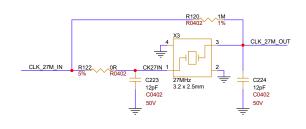
C211

C212

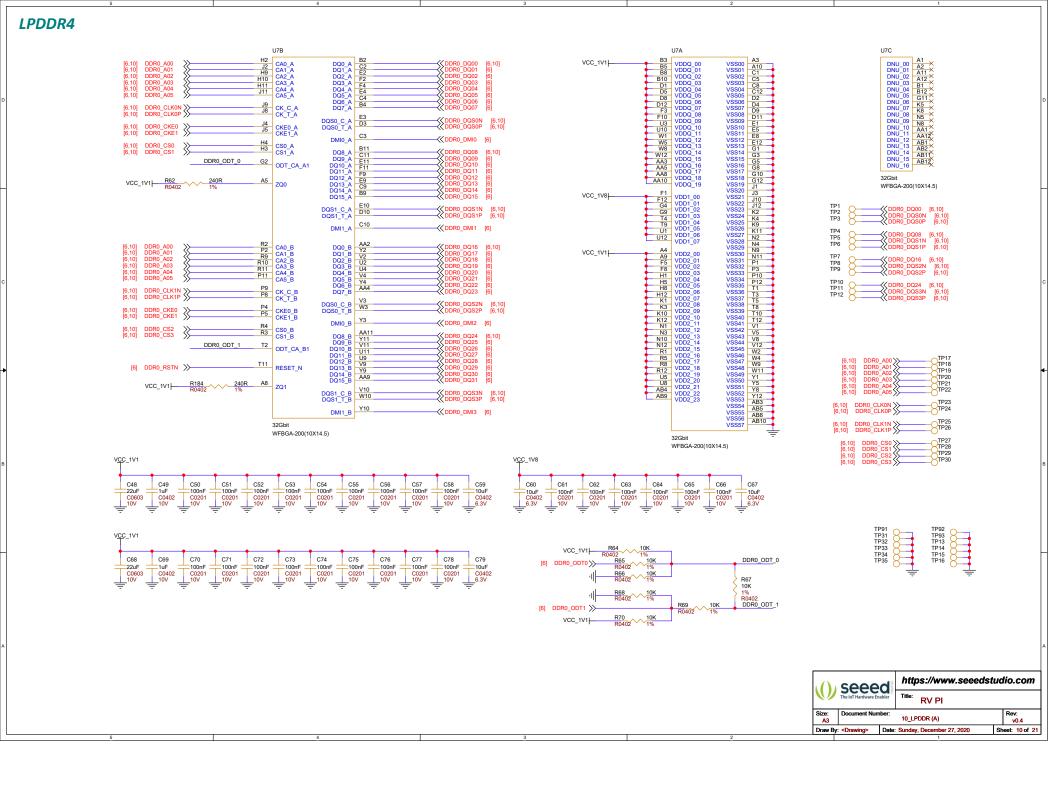
## VIC Clock&

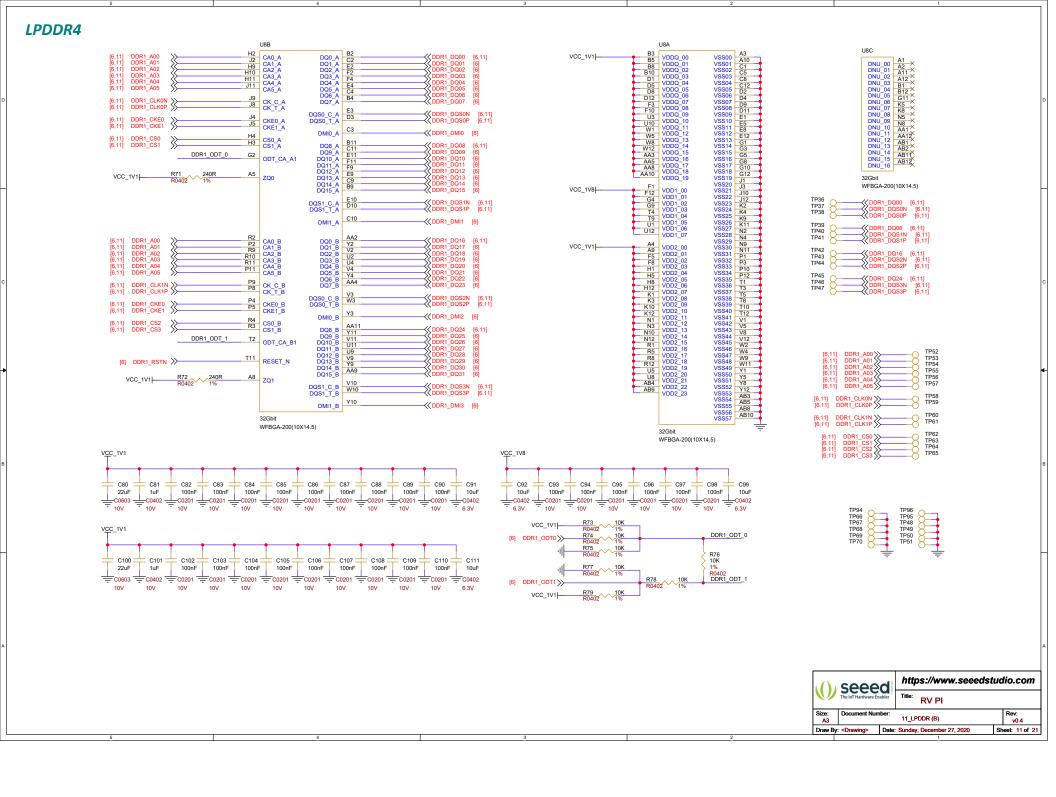


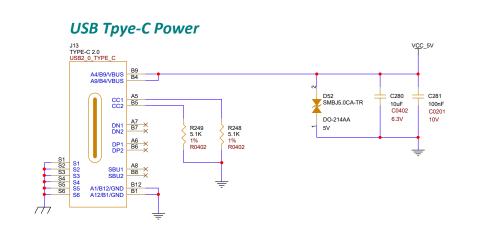


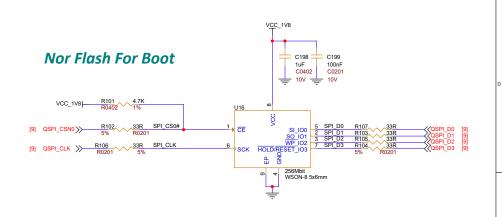


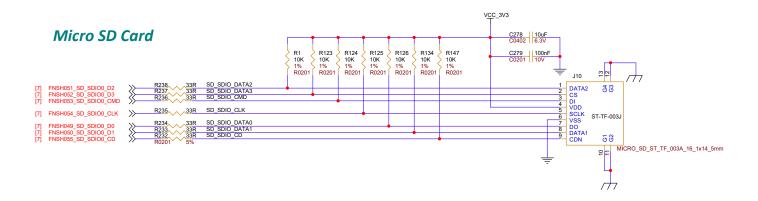






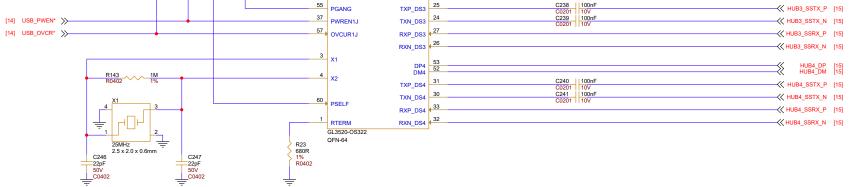


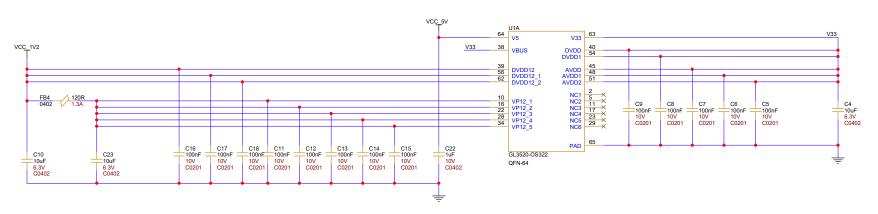






#### **USB 3.0 HUB TBD** 42 DP0 DM0 [9] USB\_DP >>-C242 100nF C0201 10V C243 100nF C0201 10V C244 100nF C0201 10V C245 100nF C0201 10V C24 | 100nF C0201 | 10V C185 | 100nF C0201 | 10V TX\_UP\_P TXP\_UP [9] USB\_SSRX1P >> TXP\_DS1 TX\_UP\_N 6 TXN\_UP TXN\_DS1 [9] USB\_SSRX1N >> RX\_UP\_P 9 RXP\_UP RXP\_DS1 15 [9] USB\_SSTX1P >>-RX\_UP\_N RXN\_DS1 14 8 RXN\_UP [9] USB\_SSTX1N >>-DP2 DM2 61 RESETJ [4,9,20] SYS\_RSTN >>-TXP DS2 TP84 O 35 P\_SPI\_CK TXN\_DS2 TP85 O 36 P\_SPI\_CZ V33 RXP\_DS2 21 TP86 \_\_\_\_\_59 P\_SPI\_DI RXN\_DS2 20 R144 100K 5% R0402 R146 > 100K - 5% R0402 R26 100K 5% R0402 R25 100K 5% R0402 58 P\_SPI\_DO DP3 DM3





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HUB1\_DP [14] HUB1\_DM [14]

HUB1\_SSTX\_P [14]

HUB1\_SSTX\_N [14]

HUB1\_SSRX\_P [14]

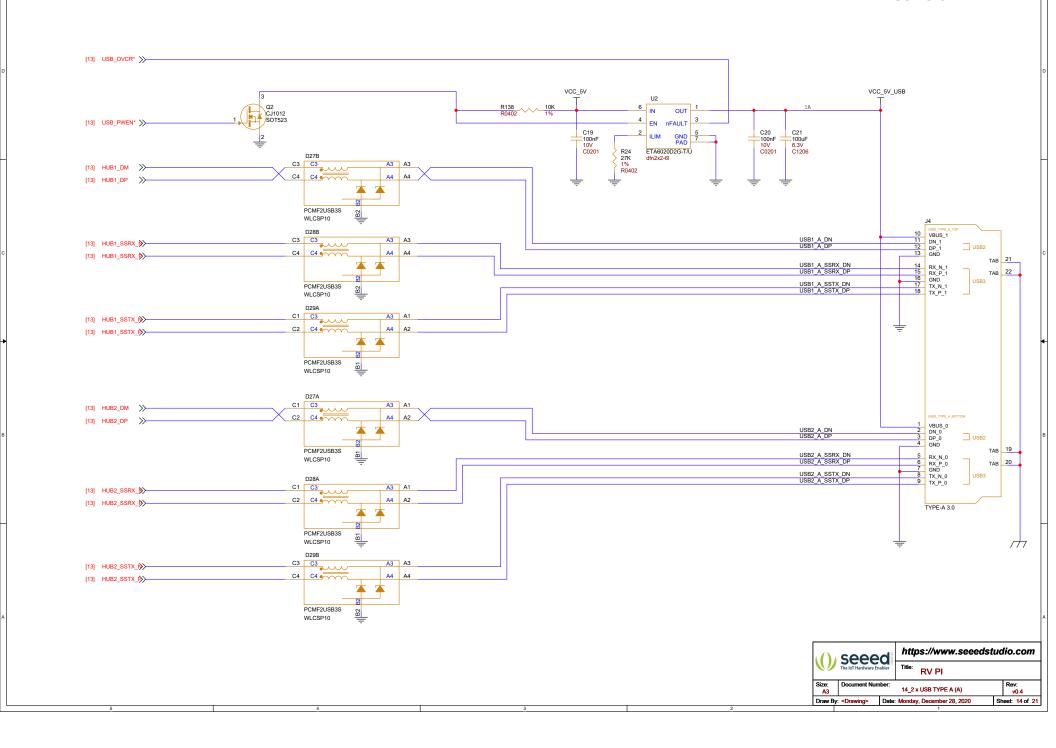
HUB2\_SSTX\_P [14]

HUB2\_SSTX\_N [14]

HUB2\_SSRX\_P [14]

HUB3\_DP [15] HUB3\_DM [15]

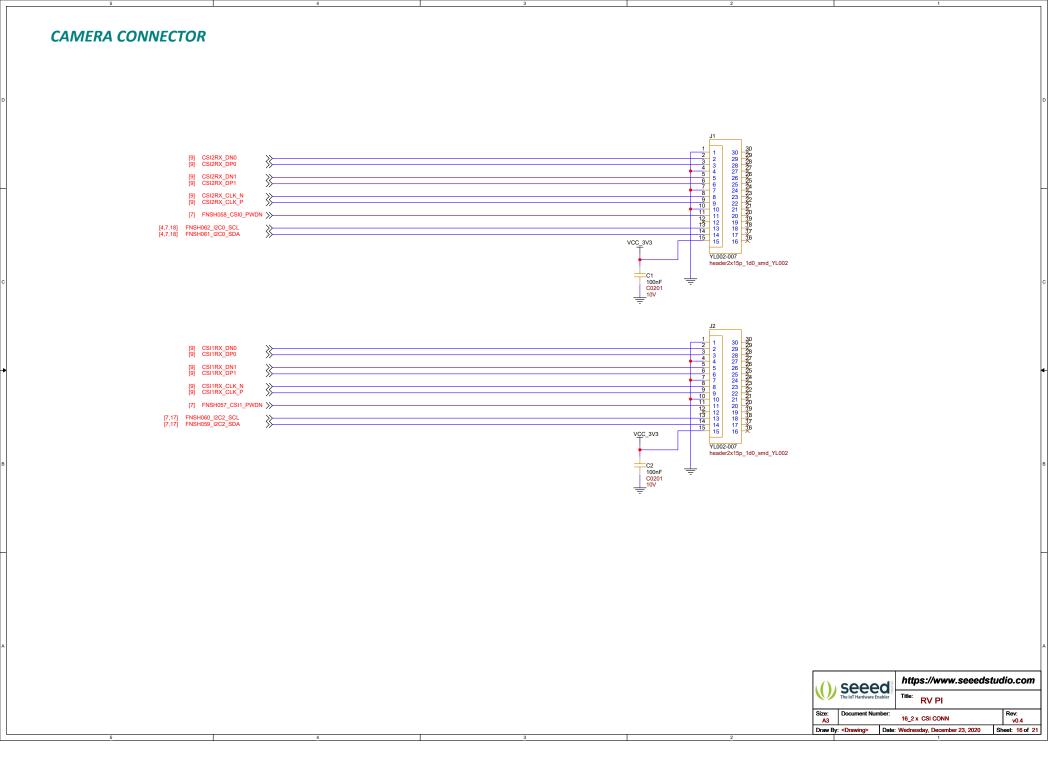
HUB2\_DP [14] HUB2\_DM [14]

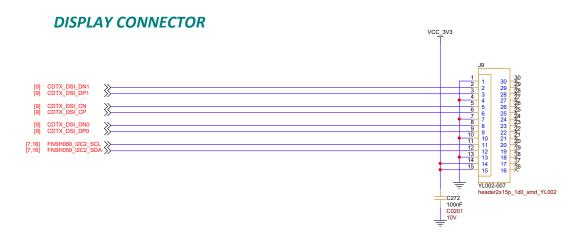


15\_2 x USB TYPE A (B)

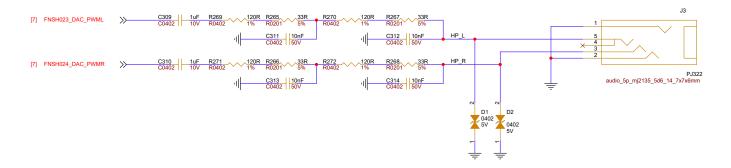
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Draw By: <Drawing> Date: Monday, December 28, 2020

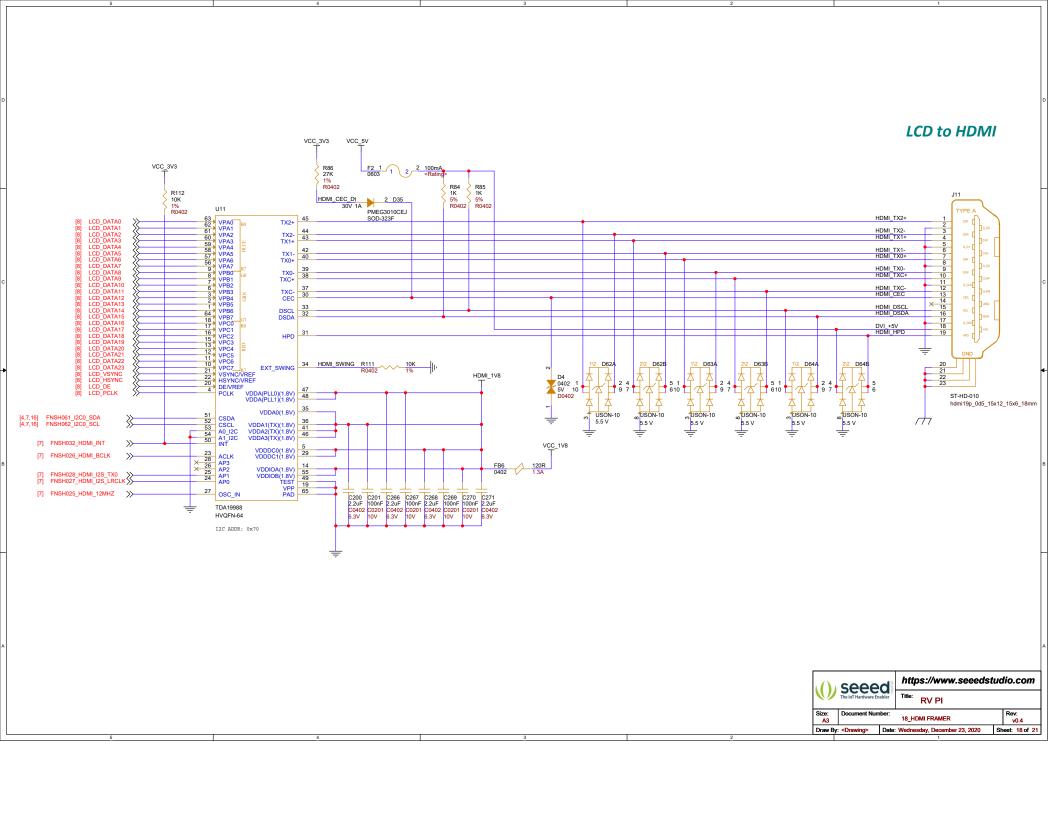


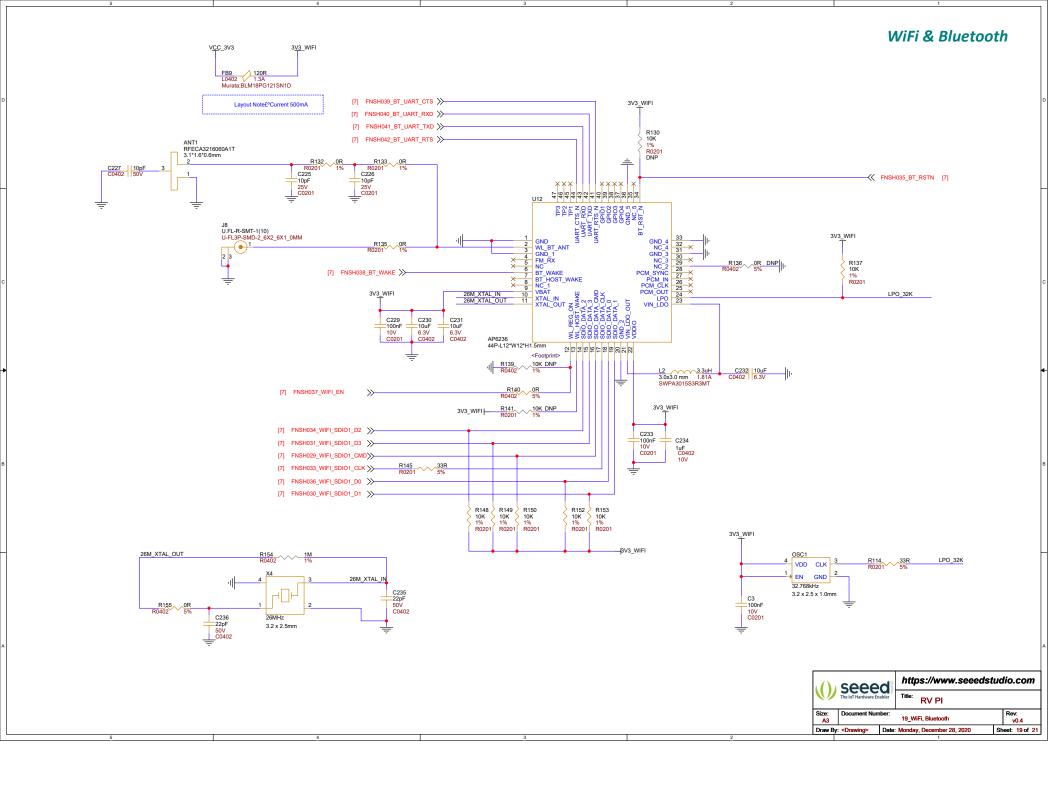


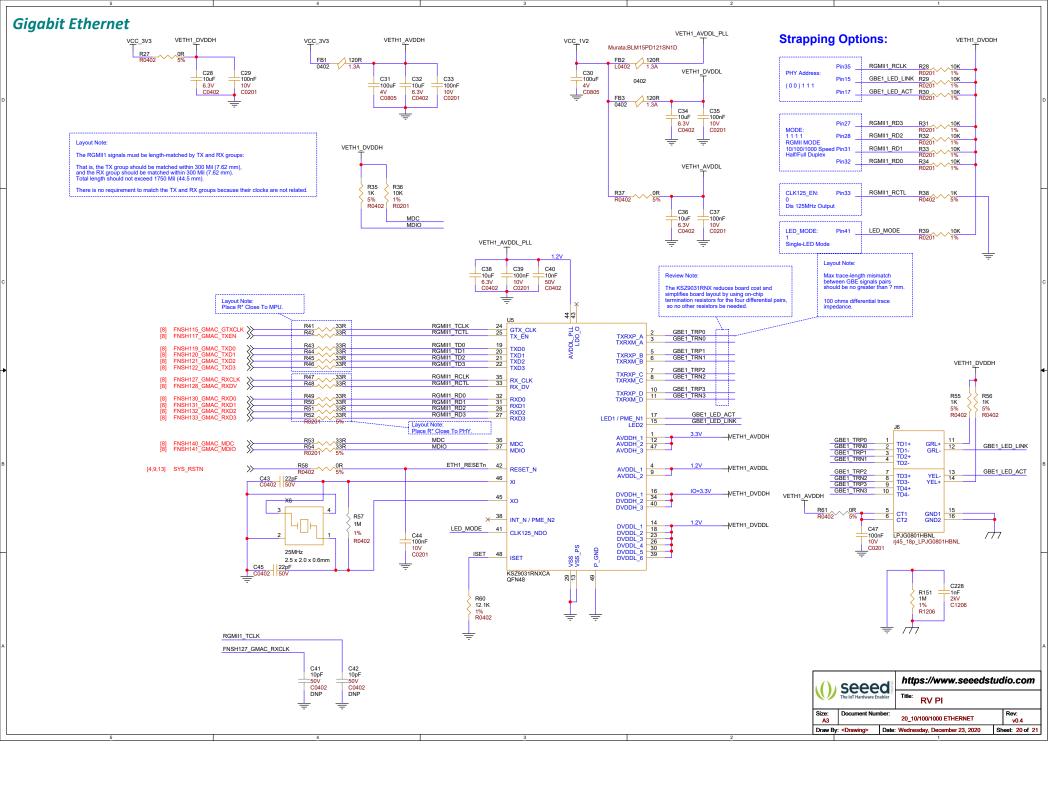
### **AUDIO**

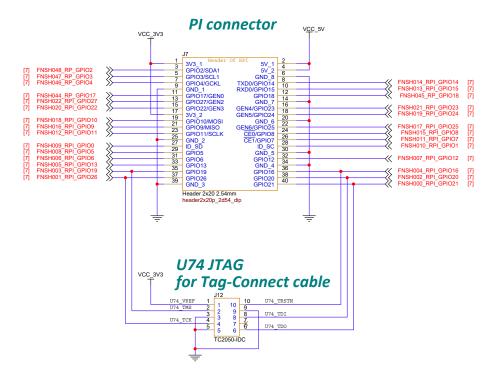


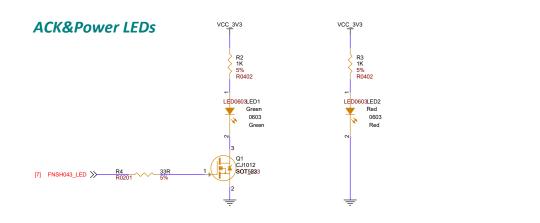












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				Title:	RV PI		
	Size: A3	Document Num	nber:	21_RF	PI EXP CONN, LED, BUTTON		Rev: v0.4
	Draw By: <drawing> Date:</drawing>			Wedne	sday, December 23, 2020	S	eet: 21 of 21