

PCI Express Host Interface

D
D
R

SMX

SMX

SMX

SMX

D
D
R

L2 Cache

D
D
R

SMX

SMX

SMX

SMX

SMX

Instruction Cache

Warp Scheduler

Warp Scheduler

Dispatch Unit

Dispatch Unit

Register File

D
D
R

SMX

CUDA Core

Dispatch Port

FP Unit

INT Unit

Result Queue

D
R

Shared Memory