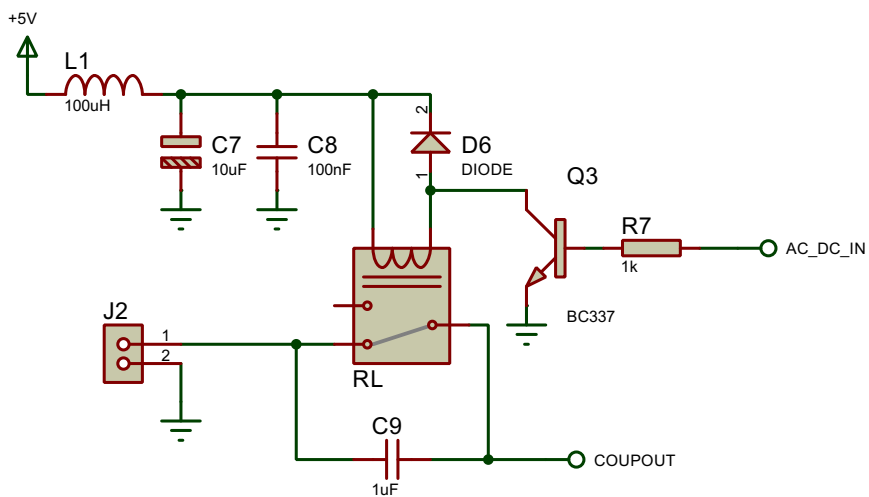


POWER SUPPLY

FILE NAME: **OscilloscopeProject.pdsprj**
 DESIGN TITLE: **OscilloscopeProject.pdsprj**
 PATH: **C:\Users\jamie\UniversityProjects\OscilloscopeProject\OscilloscopeProject.pdsprj**
 BY: **@AUTHOR**
 BOARD: **@BOARD**

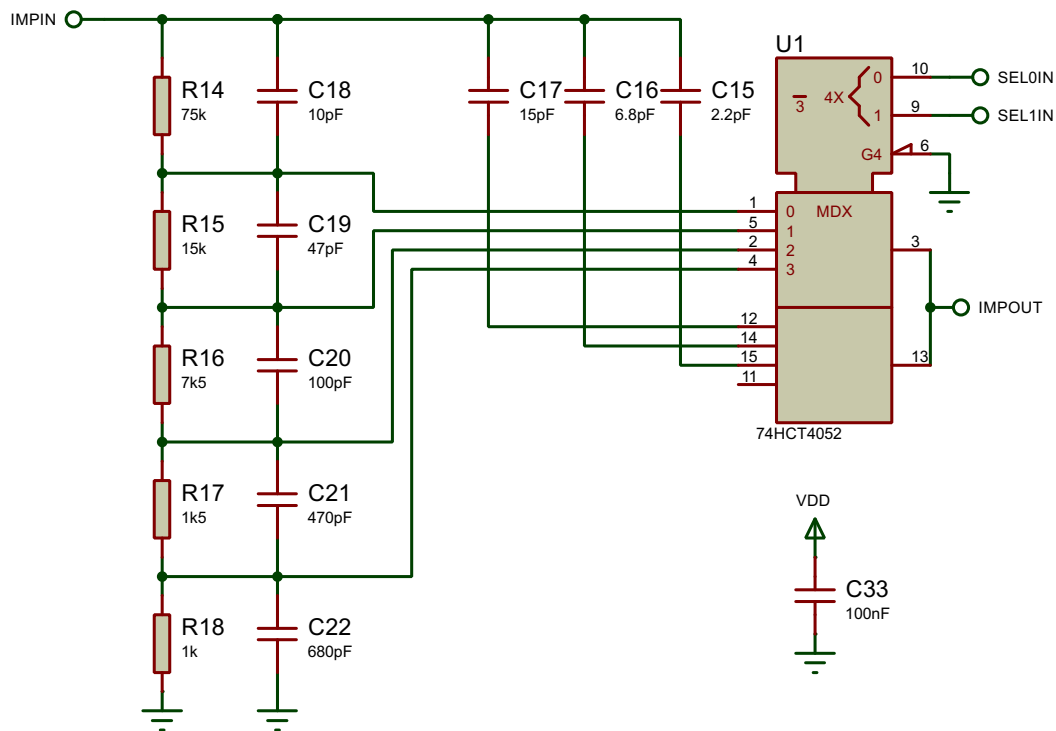
DATE: **15/03/2025**
 2 of 8
 TIME: 21:10:06



AC/DC COUPLING

FILE NAME: **OscilloscopeProject.pdsprj**
DESIGN TITLE: **OscilloscopeProject.pdsprj**
PATH: C:\Users\jamie\UniversityProjects\OscilloscopeProject\OscilloscopeProject.pdsprj
BY: @AUTHOR
BOARD: @BOARD

DATE: **15/03/2025**
3 of 8
TIME: 21:10:06



IMPEDANCE RANGE

FILE NAME: **OscilloscopeProject.pdsprj**

DESIGN TITLE: **OscilloscopeProject.pdsprj**

PATH: **C:\Users\jamie\UniversityProjects\OscilloscopeProject\OscilloscopeProject.pdsprj**

BY: **@AUTHOR**

BOARD: **@BOARD**

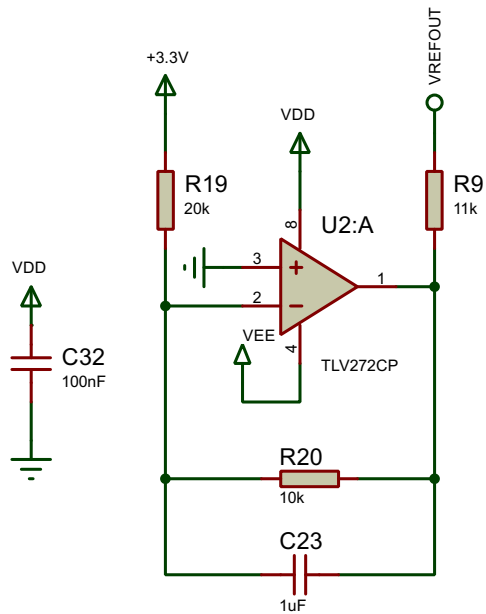
REV: **@REV**

DATE:

15/03/2025

4 of 8

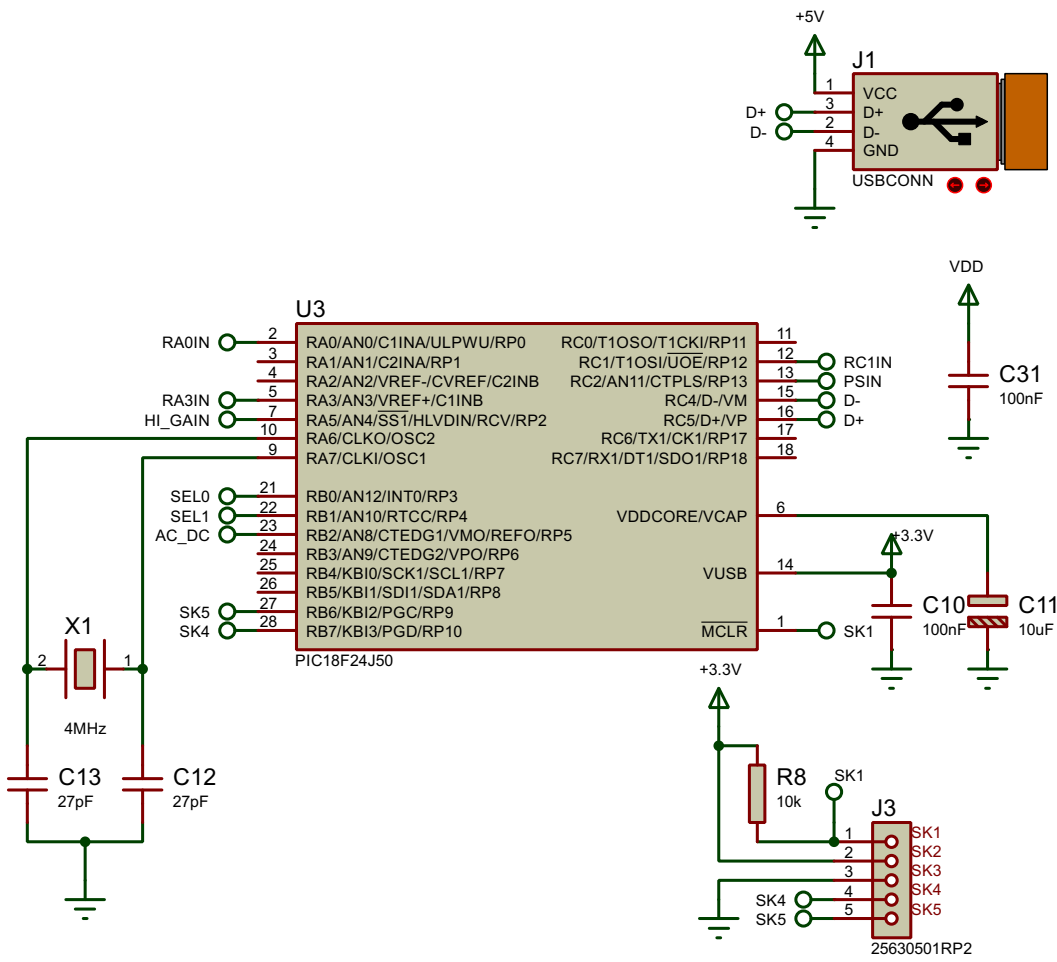
TIME: **21:10:06**



VOLTAGE REFERENCE

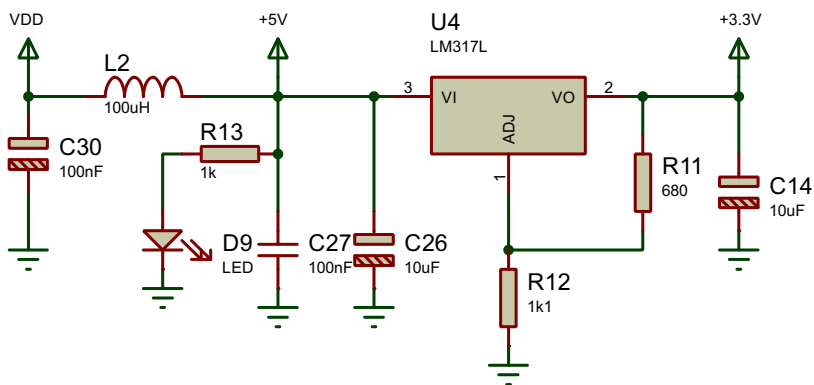
FILE NAME: **OscilloscopeProject.pdsprj**
DESIGN TITLE: **OscilloscopeProject.pdsprj**
PATH: **C:\Users\jamie\UniversityProjects\OscilloscopeProject\OscilloscopeProject**
BY: **@AUTHOR**
BOARD: **@BOARD**

DATE: **15/03/2025**
5 of 8
TIME: 21:10:06



PIC18F24J50

FILE NAME: OscilloscopeProject.pdsprj	DATE: 15/03/2025
DESIGN TITLE: OscilloscopeProject.pdsprj	7 of 8
PATH: C:\Users\jamie\UniversityProjects\OscilloscopeProject\OscilloscopeProject.pdsprj	TIME: 21:10:06
BY: @AUTHOR	REV: @REV
BOARD: @BOARD	



POWER REGULATOR

FILE NAME: **OscilloscopeProject.pdsprj**
DESIGN TITLE: **OscilloscopeProject.pdsprj**
PATH: **C:\Users\jamie\UniversityProjects\OscilloscopeProject\OscilloscopeProject.pdsprj**
BY: **@AUTHOR**
BOARD: **@BOARD**

DATE: **15/03/2025**
8 of 8
TIME: 21:10:06