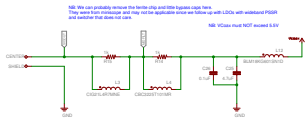
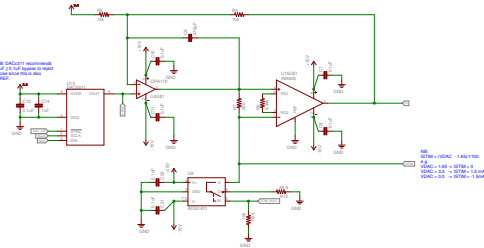


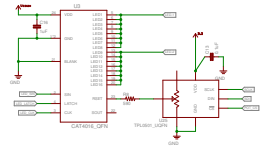
## INPUT



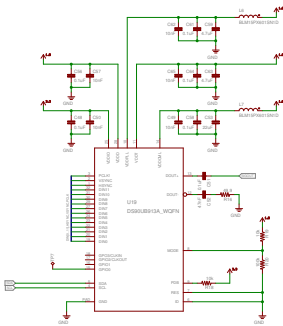
## ESTIM



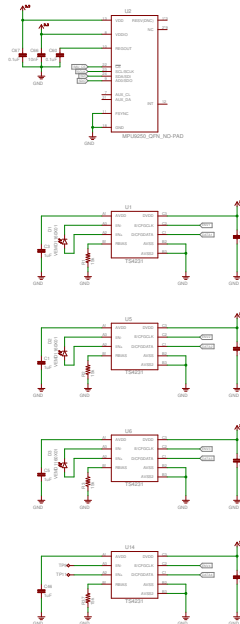
## OPTOSTIM



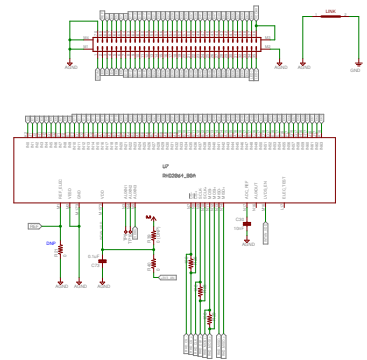
## SERIALIZER



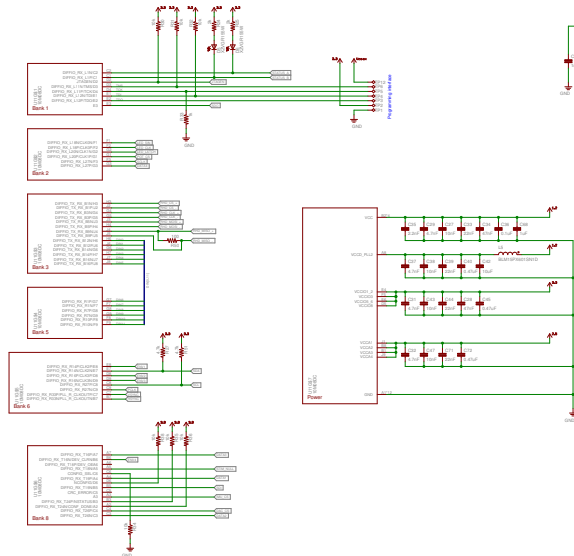
## TRACKING



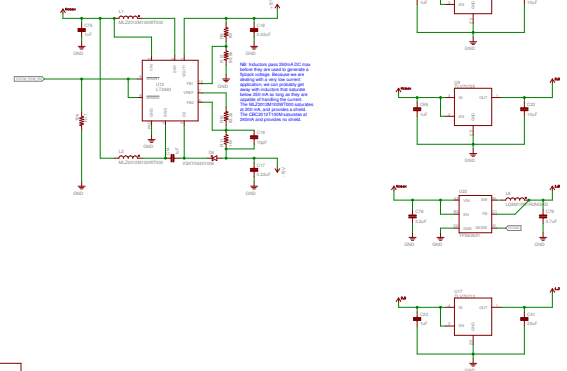
## ADC



## FPGA



## POWER



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