progress report 11/24/14
3 messages

Jacob Mickiewicz <jmickiew@pdx.edu> Mon, Nov 24, 2014 at 8:47 PM
To: Kris Gibbs <kris.gibbs888@gmail.com>
Cc: Brandon Towell <brandon.towell@gmail.com>, Ryan O'Connor <roconnor@pdx.edu>

Last week I:
Helped with board assembly, and reviewed the test plan.

Next week I will:
Build cardboard models of the enclosures.

Brandon Towell <brandon.towell@gmail.com> Mon, Nov 24, 2014 at 10:04 PM
To: Jacob Mickiewicz <jmickiew@pdx.edu>
Cc: Kris Gibbs <kris.gibbs888@gmail.com>, Ryan O'Connor <roconnor@pdx.edu>

Last Week:
Board Assembly
Conductivity Testing
Board rework for connectivity issues

This Week:
Construct wiring harness for buttons
Run test cases on prototype
Check button operation with harness
Button Assembly

Sent from my Gold iPhone 6+
[Quoted text hidden]

Ryan O'Connor <roconnor@pdx.edu> Wed, Nov 26, 2014 at 10:39 AM
To: Brandon Towell <brandon.towell@gmail.com>
Cc: Jacob Mickiewicz <jmickiew@pdx.edu>, Kris Gibbs <kris.gibbs888@gmail.com>

Developed first version of mainline code
Assembled board

To do:
Revise mainline code to operate on board
Get button interface and debounce nailed down
Integrate display code architecture into mainline code for future integration

Note: mainline code will take a few iterations to get to workable level. Lack of ICSP on board makes debug more tricky if you do not have an Arduino to program the chip. I can provide code that can be used but without an Arduino you will have no means to verify it. I will go over the code in the next meeting to show just how I have approached it based on specifications. The software test cases will need to reflect the code. I work retail so my cycles on Thur/Fri are non-existent but I do hope to have at minimum some button interface functions that will work: I did not grab a switch for software reset so if you can bring those components it would be great. Buttons have arrived and I will bring those on Saturday as well.

Best Regards,
Ryan
[Quoted text hidden]

--

Ryan O'Connor | Bachelors of Science in Electrical Engineering, Analog IC Design/Layout | Mobile: 503.830.5818 |
roconnor@pdx.edu