15-213

"The course that gives CMU its Zip!"

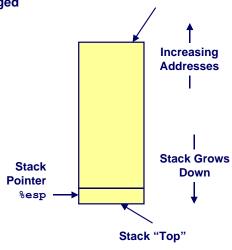
Machine-Level Programming III: Procedures Sept. 17, 2002

Topics

- IA32 stack discipline
- Register saving conventions
- Creating pointers to local variables

IA32 Stack

- Region of memory managed with stack discipline
- Grows toward lower addresses
- Register %esp indicates lowest stack address
 - address of top element



Stack "Bottom"

- 2 - 15-213, F'02

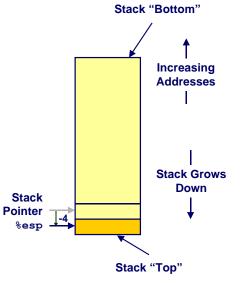
IA32 Stack Pushing

Pushing

■ pushl *Src*

class07.ppt

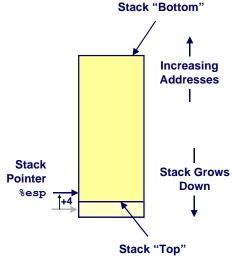
- Fetch operand at *Src*
- Decrement %esp by 4
- Write operand at address given by %esp



IA32 Stack Popping

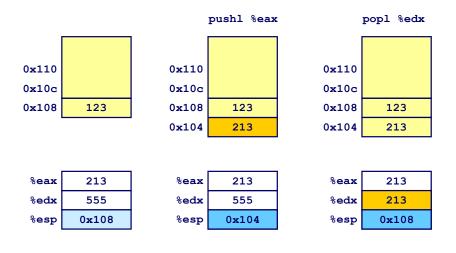
Popping

- popl Dest
- Read operand at address given by %esp
- Increment %esp by 4
- Write to *Dest*



- 3 - 15-213, F'02 - 4 - 15-213, F'02

Stack Operation Examples



Procedure Control Flow

Use stack to support procedure call and return

Procedure call:

call label Push return address on stack; Jump to label

Return address value

- Address of instruction beyond call
- Example from disassembly

804854e: e8 3d 06 00 00 8048b90 <main> call

8048553: 50 pushl %eax

• Return address = 0x8048553

Procedure return:

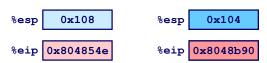
Pop address from stack; Jump to address ■ ret

-5-15-213, F'02 -6-15-213, F'02

Procedure Call Example

804854e: e8 3d 06 00 00 8048b90 <main> call 8048553: 50

pushl %eax call 8048ь90 0x110 0x10c 0x108123 0x104 0x8048553



0x110

0x10c

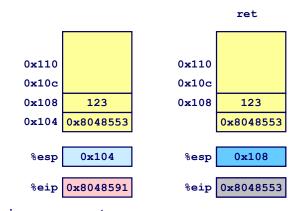
0x108

123

%eip is program counter

Procedure Return Example

8048591: c3 ret



%eip is program counter

Stack-Based Languages

Languages that Support Recursion

- e.g., C, Pascal, Java
- Code must be "Reentrant"
 - Multiple simultaneous instantiations of single procedure
- Need some place to store state of each instantiation
 - Arguments
 - Local variables
 - Return pointer

Stack Discipline

- State for given procedure needed for limited time
 - From when called to when return
- Callee returns before caller does

Stack Allocated in Frames

state for single procedure instantiation

-9-15-213, F'02 - 10 -

Stack Frames

Contents

- Local variables
- Return information
- Temporary space

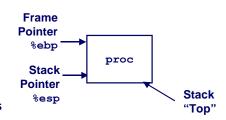
Management

- Space allocated when enter procedure
 - "Set-up" code
- Deallocated when return
 - "Finish" code

Pointers

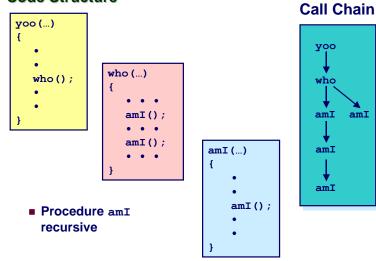
- Stack pointer %esp indicates stack top
- Frame pointer %ebp indicates start of current frame

yoo who amI



Call Chain Example

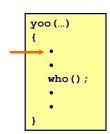
Code Structure



15-213, F'02

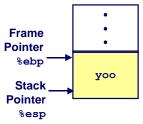
amI

Stack Operation



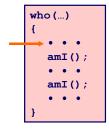


yoo

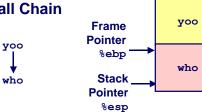


15-213, F'02 - 12 -15-213, F'02

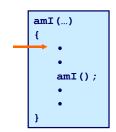
Stack Operation





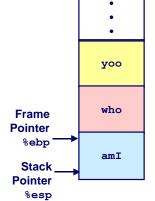


Stack Operation



Call Chain





- 13 -15-213, F'02 - 14 -15-213, F'02

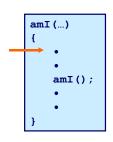
yoo

who

amI

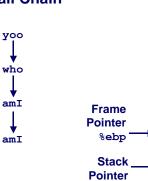
amI

Stack Operation



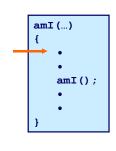
- 15 -

Call Chain



%esp

Stack Operation



- 16 -

Call Chain

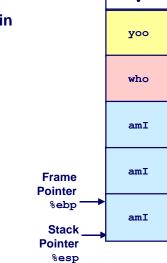
yoo

who

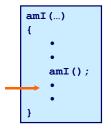
amI

 \mathtt{amI}

amI



Stack Operation



Call Chain

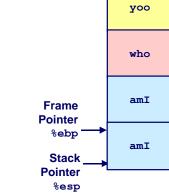
yoo

who

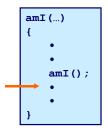
amI

amI

amI

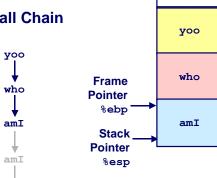


Stack Operation



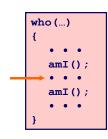
Call Chain

amI



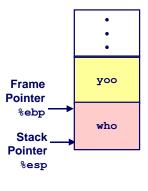
- 17 -15-213, F'02 - 18 -

Stack Operation

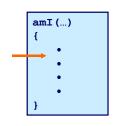




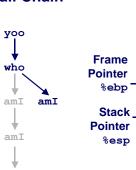
Call Chain

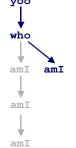


Stack Operation



Call Chain





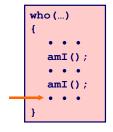
15-213, F'02

yoo

who

amI

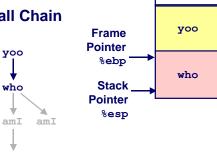
Stack Operation



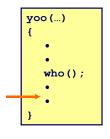
Call Chain

amI

amI

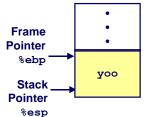


Stack Operation



Call Chain





-21-15-213, F'02 - 22 -15-213, F'02

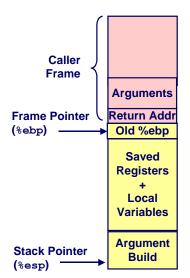
IA32/Linux Stack Frame

Current Stack Frame ("Top" to Bottom)

- Parameters for function about to call
 - "Argument build"
- Local variables
 - If can't keep in registers
- Saved register context
- Old frame pointer

Caller Stack Frame

- Return address
 - Pushed by call instruction
- Arguments for this call



Revisiting swap

```
int zip1 = 15213;
int zip2 = 91125;
void call_swap()
  swap(&zip1, &zip2);
```

```
void swap(int *xp, int *yp)
  int t0 = *xp;
  int t1 = *yp;
  *xp = t1;
  *yp = t0;
```

Calling swap from call swap

call swap:

```
pushl $zip2
              # Global Var
pushl $zip1
              # Global Var
call swap
. . .
                 Resulting
                  Stack
        &zip2
        &zip1
```

Rtn adr

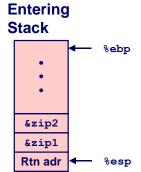
%esp

Revisiting swap

void swap(int *xp, int *yp) { int t0 = *xp; int t1 = *yp; *xp = t1; *yp = t0; }

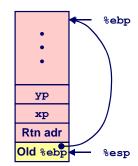
```
swap:
   pushl %ebp
                         Set
   movl %esp,%ebp
   pushl %ebx
   movl 12(%ebp),%ecx
   movl 8(%ebp),%edx
   movl (%ecx),%eax
                         Body
   movl (%edx),%ebx
   movl %eax,(%edx)
   movl %ebx,(%ecx)
   movl -4(%ebp),%ebx
   movl %ebp,%esp
                         Finish
   popl %ebp
   ret
```

swap Setup #1



swap:
 pushl %ebp
 movl %esp,%ebp
 pushl %ebx

Resulting Stack

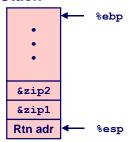


15-213, F'02

swap Setup #2

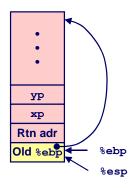
Entering Stack

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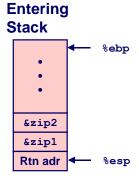


swap:
 pushl %ebp
 movl %esp,%ebp
 pushl %ebx

Resulting Stack

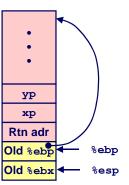


swap Setup #3



swap:
 pushl %ebp
 movl %esp,%ebp
 pushl %ebx

Resulting Stack



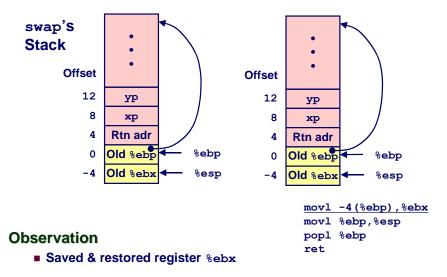
15-213, F'02

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Effect of swap Setup

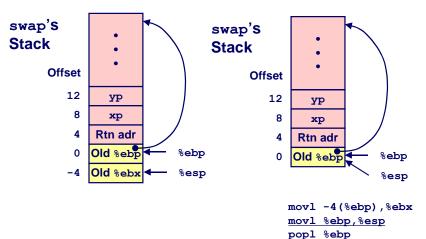
Entering Resulting **Stack Stack** %ebp Offset (relative to %ebp) &zip2 yр &zip1 хp Rtn adr ← %esp 4 Rtn adr Old %ebp %ebp Old %ebx %esp movl 12(%ebp), %ecx # get yp **▶** Body movl 8(%ebp),%edx # get xp

swap Finish #1



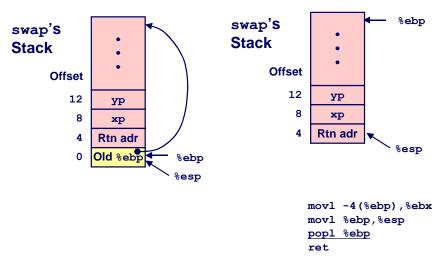
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swap Finish #2

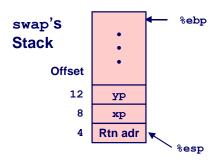


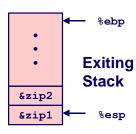
ret

swap Finish #3



swap Finish #4





Observation

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- Saved & restored register %ebx
- Didn't do so for %eax, %ecx, or %edx

movl %ebp,%esp popl %ebp <u>ret</u>

movl -4(%ebp),%ebx

15-213, F'02

Register Saving Conventions

When procedure yoo calls who:

■ yoo is the caller, who is the callee

Can Register be Used for Temporary Storage?

```
yoo:

movl $15213, %edx
call who
addl %edx, %eax

ret
```

15-213, F'02

■ Contents of register %edx overwritten by who

Register Saving Conventions

When procedure yoo calls who:

■ yoo is the caller, who is the callee

Can Register be Used for Temporary Storage?

Conventions

- "Caller Save"
 - Caller saves temporary in its frame before calling
- "Callee Save"
 - Callee saves temporary in its frame before using

IA32/Linux Register Usage

Integer Registers

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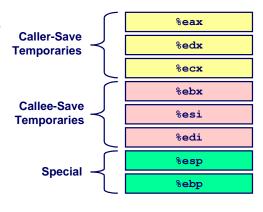
- Two have special uses %ebp, %esp
- Three managed as callee-save

%ebx, %esi, %edi

- Old values saved on stack prior to using
- Three managed as caller-save

%eax, %edx, %ecx

- Do what you please, but expect any callee to do so, as well
- Register %eax also stores returned value



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Recursive Factorial

```
int rfact(int x)
  int rval;
  if (x \le 1)
    return 1;
  rval = rfact(x-1);
  return rval * x;
```

Registers

- %eax used without first saving
- %ebx used, but save at beginning & restore at end

```
.globl rfact
    . type
rfact,@function
rfact:
   pushl %ebp
   movl %esp,%ebp
   pushl %ebx
    mov1 8 (%ebp), %ebx
    cmpl $1,%ebx
    ile .L78
    leal -1(%ebx), %eax
   pushl %eax
    call rfact
   imull %ebx, %eax
    jmp .L79
    .align 4
.L78:
    movl $1, %eax
.L79:
    movl -4(%ebp),%ebx
   movl %ebp,%esp
   popl %ebp
    ret
                 15-213, F'02
```

Rfact Stack Setup

```
pre %ebp
                       %ebp
         pre %ebx
                       Entering Stack
Caller
          Rtn adr
                       %esp
                                   rfact:
                                       pushl %ebp
                                       movl %esp,%ebp
                                       pushl %ebx
             pre %ebp
   Caller
             pre %ebx
          8
              Rtn adr
             Old %ebr
                           %ebp
   Callee
             Old %ebx
```

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Rfact Body

```
\# ebx = x
                movl 8(%ebp),%ebx
                cmpl $1,%ebx
                                    # Compare x : 1
                jle .L78
                                    # If <= goto Term
                leal -1 (%ebx), %eax # eax = x-1
                pushl %eax
                                    # Push x-1
Recursion
                                    # rfact(x-1)
                call rfact
                imull %ebx,%eax
                                    # rval * x
                jmp .L79
                                    # Goto done
               .L78:
                movl $1,%eax
                                    # return val = 1
                                  # Done:
               .L79:
```

```
int rfact(int x)
 int rval;
 if (x \le 1)
    return 1;
 rval = rfact(x-1);
  return rval * x;
```

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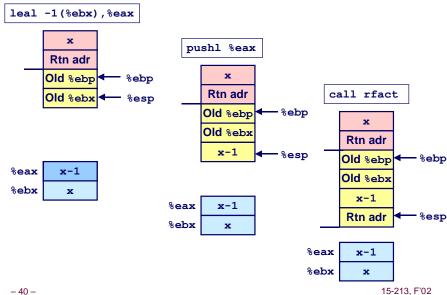
Registers

%ebx Stored value of x %eax

- Temporary value of x-1
- Returned value from rfact(x-1)
- Returned value from this call

Rfact Recursion

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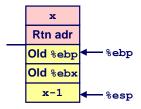
15-213, F'02

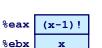
15-213, F'02

15-213, F'02

Rfact Result

Return from Call



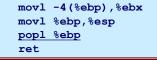


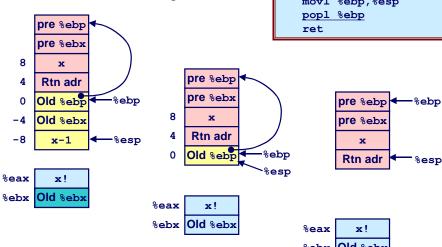
Assume that rfact (x-1) returns (x-1)! in register %eax

imull %ebx,%eax x Rtn adr Old %ebp - %ebp Old %ebx x-1 -%esp

%eax	x!
%ebx	х

Rfact Completion





%ebx Old %ebx

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Pointer Code

Recursive Procedure

```
void s helper
  (int x, int *accum)
 if (x \le 1)
    return:
 else {
   int z = *accum * x;
    *accum = z;
    s helper (x-1,accum);
```

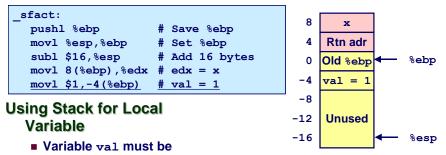
Top-Level Call

```
int sfact(int x)
  int val = 1;
  s helper(x, &val);
  return val;
```

Pass pointer to update location

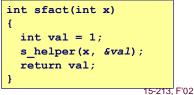
Creating & Initializing Pointer

Initial part of sfact



stored on stack Need to create pointer to it

- Compute pointer as -4 (%ebp)
- Push on stack as second argument



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Passing Pointer

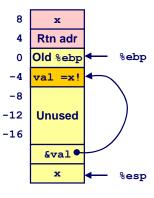
Calling s helper from sfact

```
leal -4(%ebp),%eax # Compute &val
pushl %eax # Push on stack
pushl %edx # Push x
call s_helper # call
movl -4(%ebp),%eax # Return val
• • • # Finish
```

```
int sfact(int x)
{
  int val = 1;
  s_helper(x, &val);
  return val;
}
```

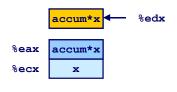
- 45 -

Stack at time of call



15-213, F'02

Using Pointer



```
movl %ecx,%eax # z = x
imull (%edx),%eax # z *= *accum
movl %eax,(%edx) # *accum = z
. . .
```

- Register %ecx holds x
- Register %edx holds pointer to accum
 - Use access (%edx) to reference memory

Summary

The Stack Makes Recursion Work

- Private storage for each *instance* of procedure call
 - Instantiations don't clobber each other
 - Addressing of locals + arguments can be relative to stack positions
- Can be managed by stack discipline
 - Procedures return in inverse order of calls

IA32 Procedures Combination of Instructions + Conventions

- Call / Ret instructions
- Register usage conventions
 - Caller / Callee save
 - %ebp and %esp
- Stack frame organization conventions

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