

CS202 Computer Organization and Architecture

Minor 2

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5 = 3
6 = 6
7 = 17

- Total duration: 55 mins
- Total marks: 20 marks (Q1, Q3, Q4, Q6 carry 3 marks Q2, Q5, Q7 and Q8 carry 2 marks)
- 1. Compare static RAM with dynamic RAM 3
- 2. Do you agree that hierarchical organization of computer memory is inevitable? Justify your answer 2
- 3. There is a need to design 4MB SRAM chip. The word size of 8 bits. What is the suitable cell array size? 3
- 4. For a cache with capacity 32KB, how many blocks does the cache hold for block sizes of 128, 32 and 64? 3
- 5. What is virtual memory concept? Briefly explain 2
- 6. Briefly explain the role played by PC 3
- 7. Give control sequence for instruction fetch 2
- 8. Compare and contrast hard wired control unit and microprogrammed control unit 2

102
204
408