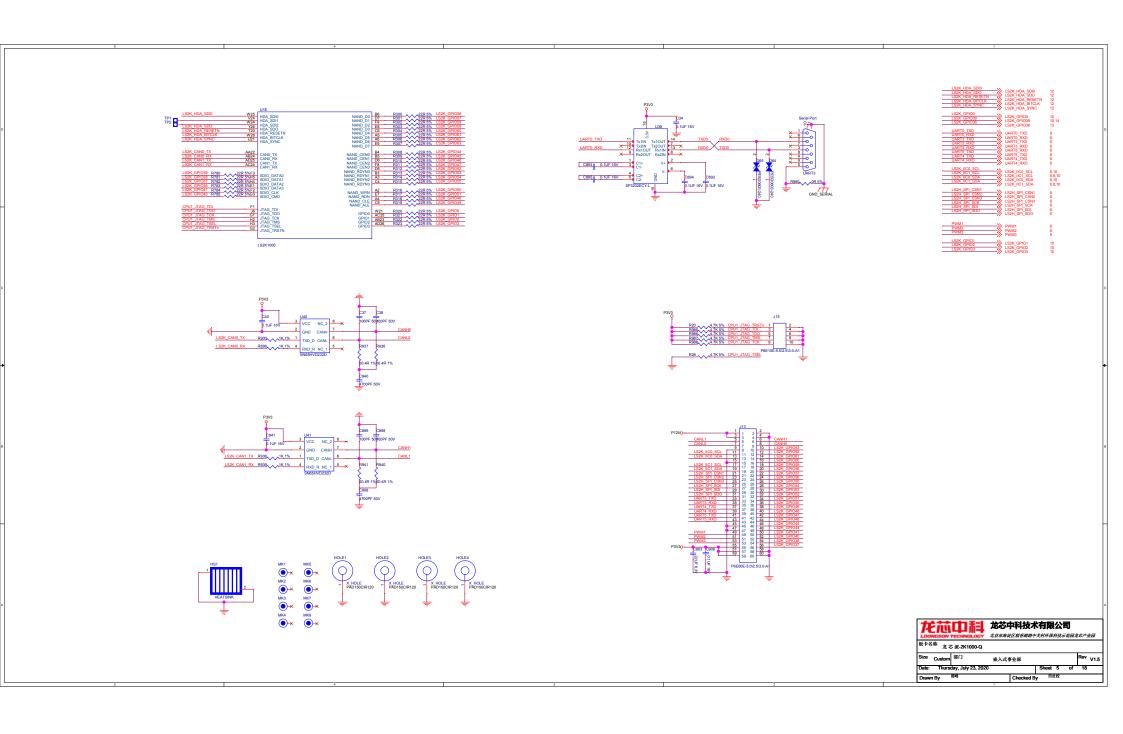
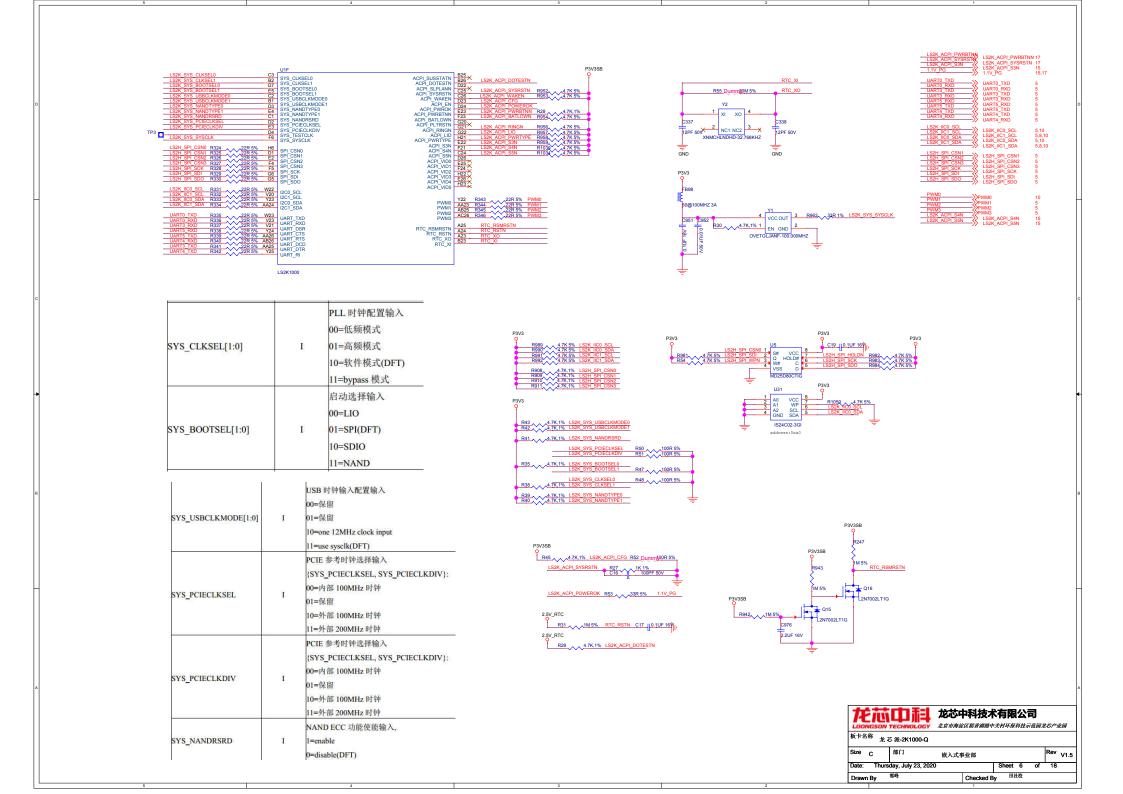


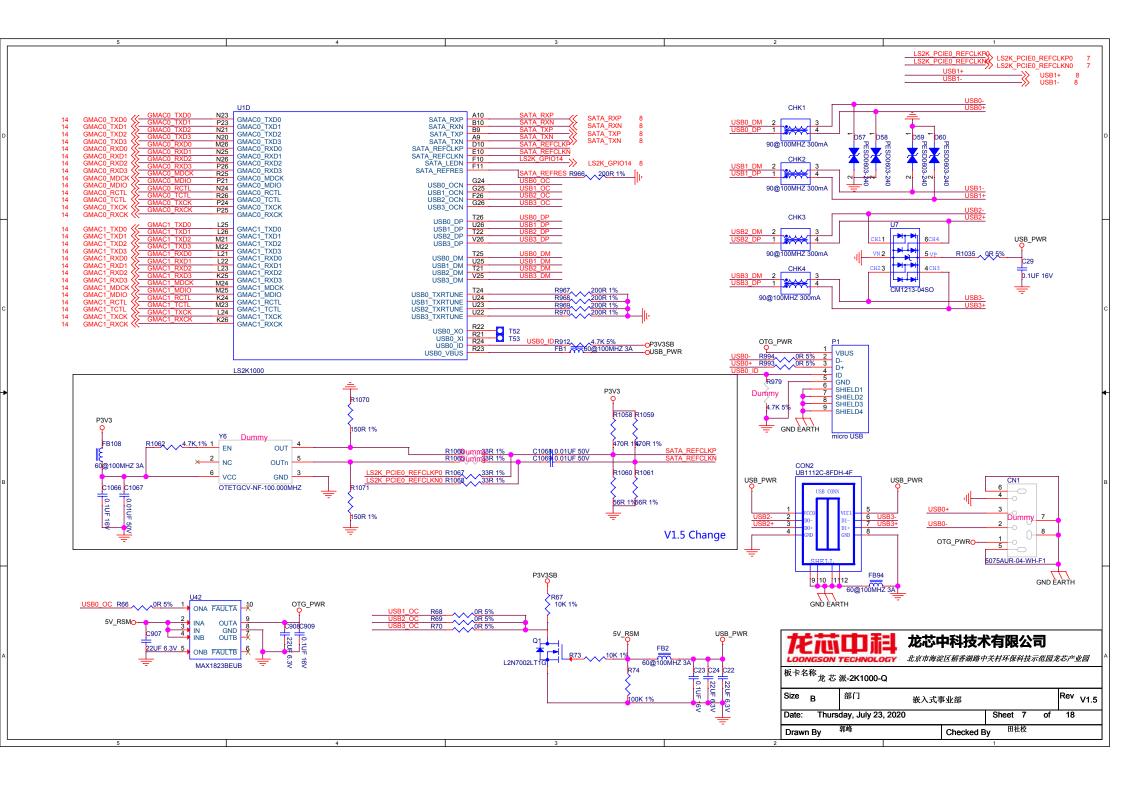
龙芯派-2K1000-Q

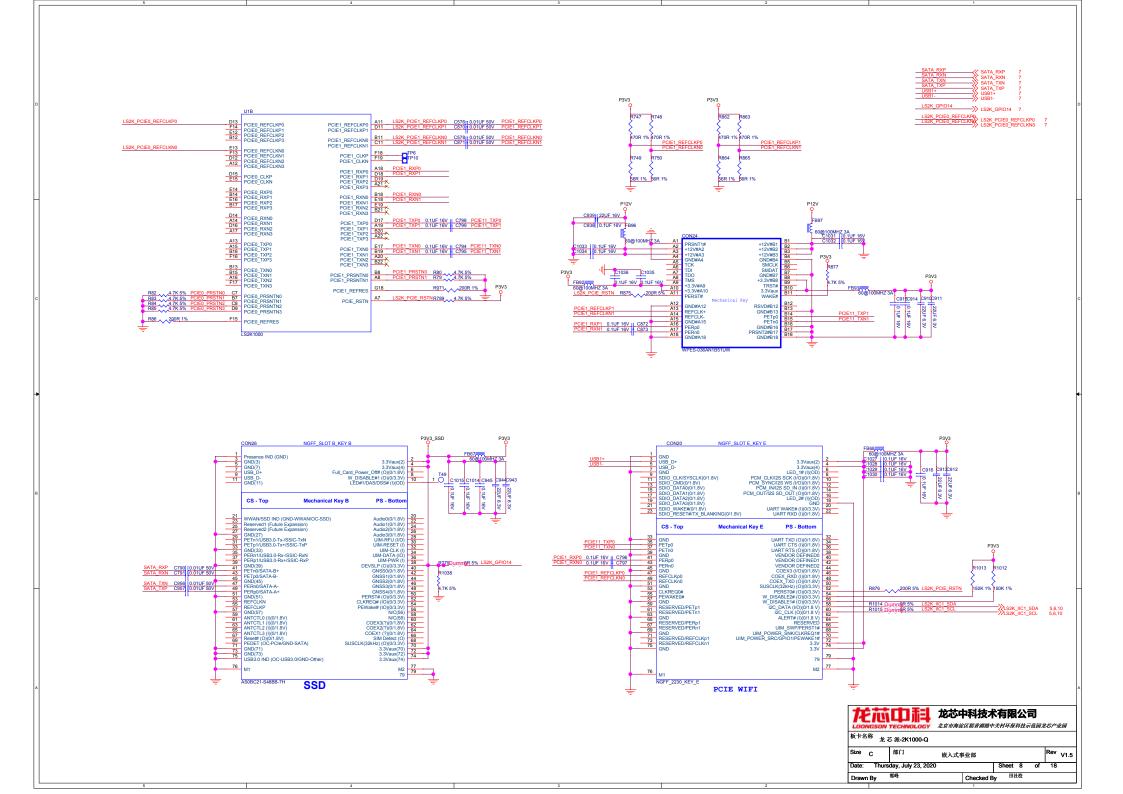
TITLE	PAGE
sysrem block	1
clk/reset# block	2
power block	3
resource spread	4
GPIO/I2S/EJTAG/SDIO/CAN	5
ACPI/I2C/SPI/PWM/URT/CFG	6
GMAC0/GPIO/USB	7
SSD/PCIE	8
DDR	9
DVO/HDMI/LIO	10
2K_POWER	11
AUDIO CODEC&CONN	12
BEEP	13
LAN0/1	14
1V1/1V2/1V5/3V3	15
RSM1V1/3V3	16
RESET#/PWR_BTN	17
BLANK	18

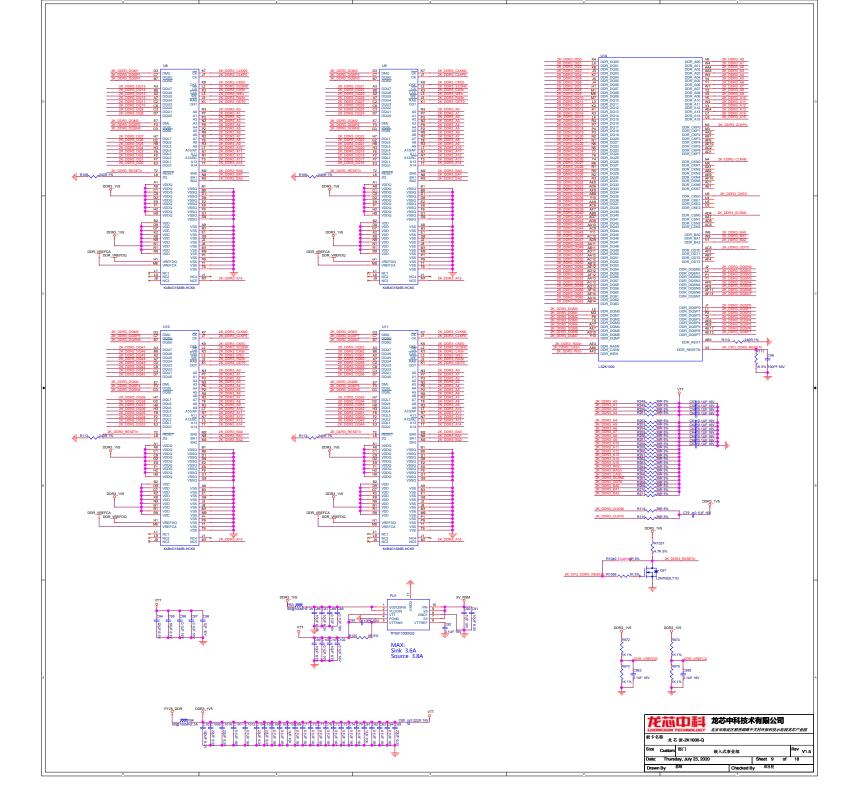


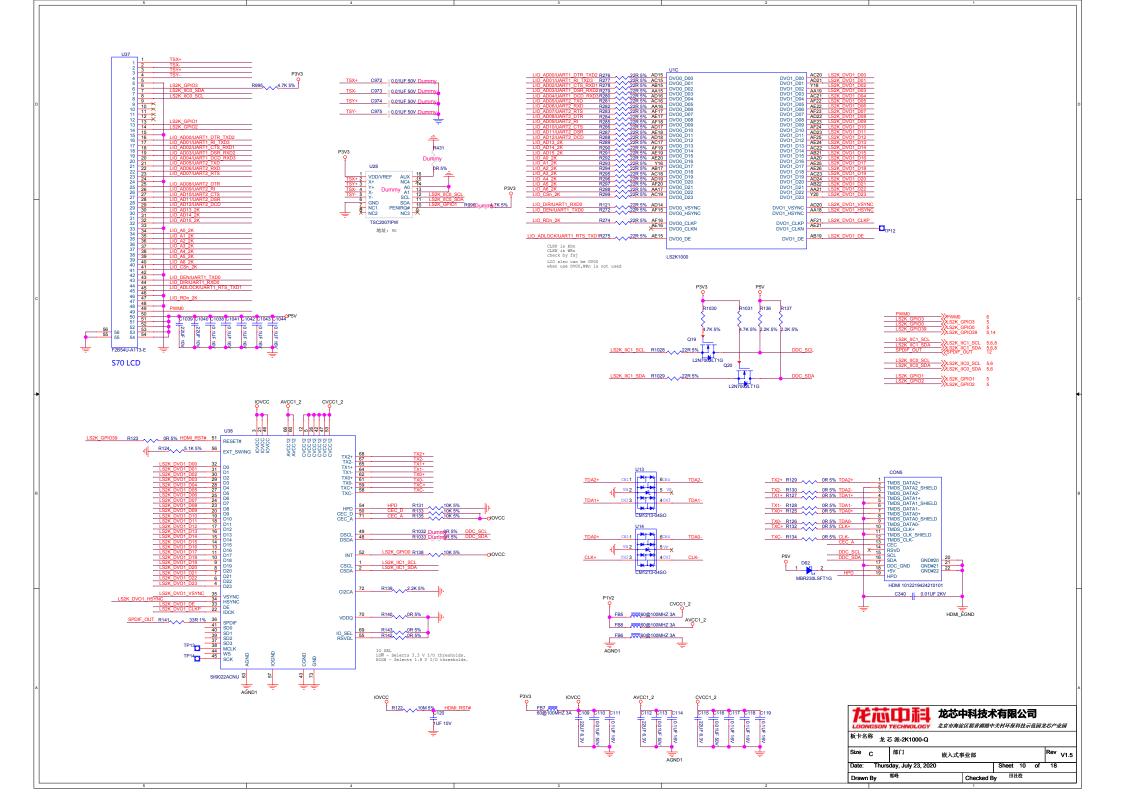


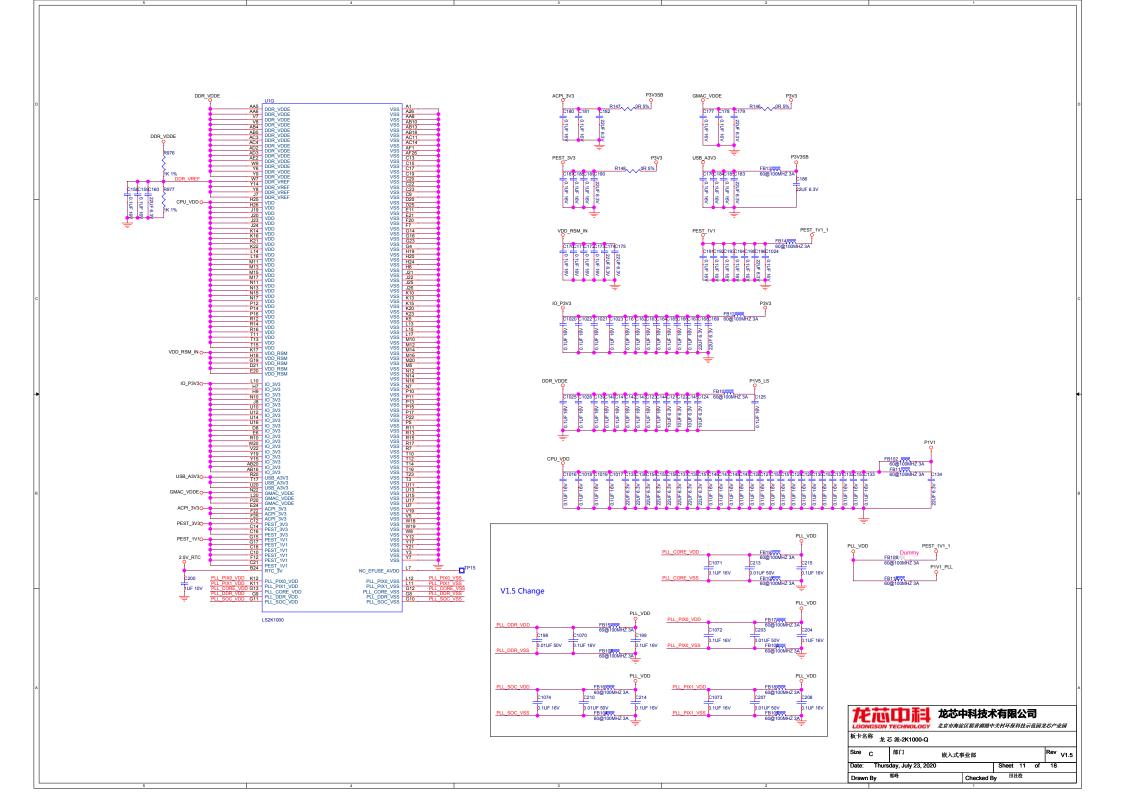


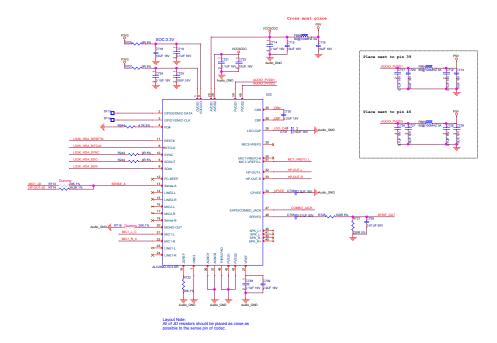






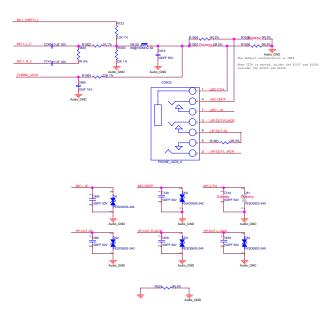




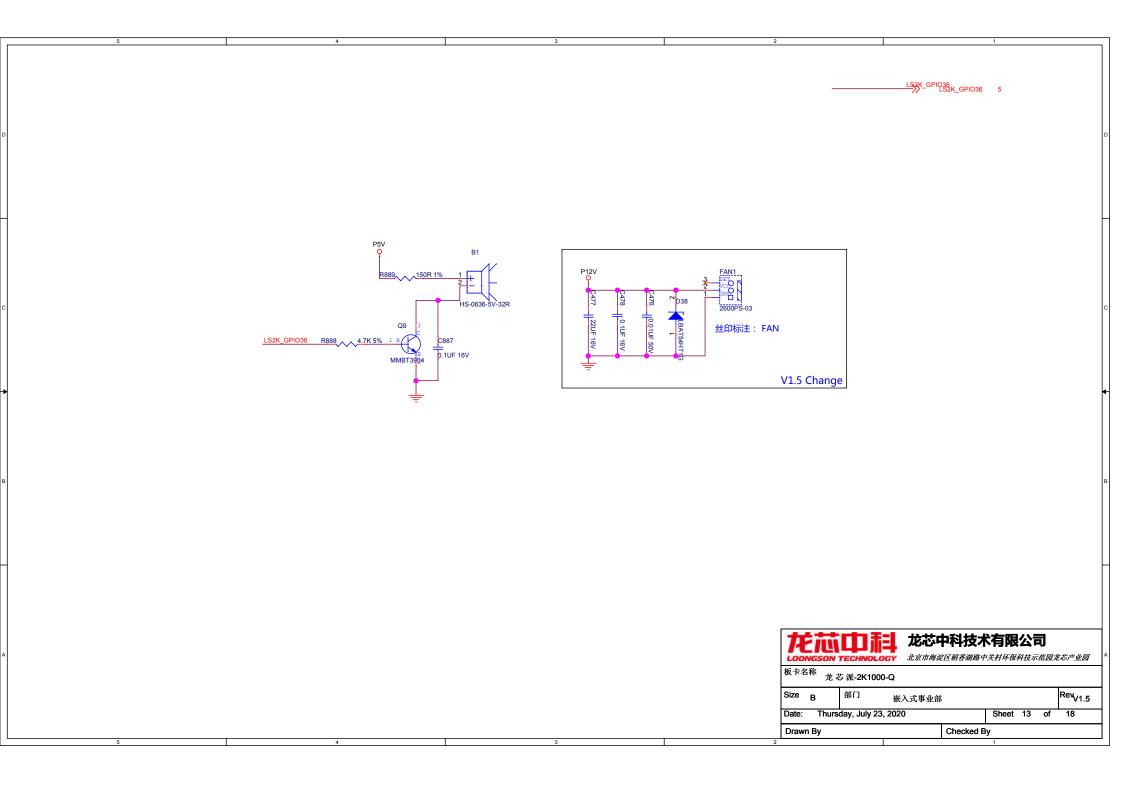


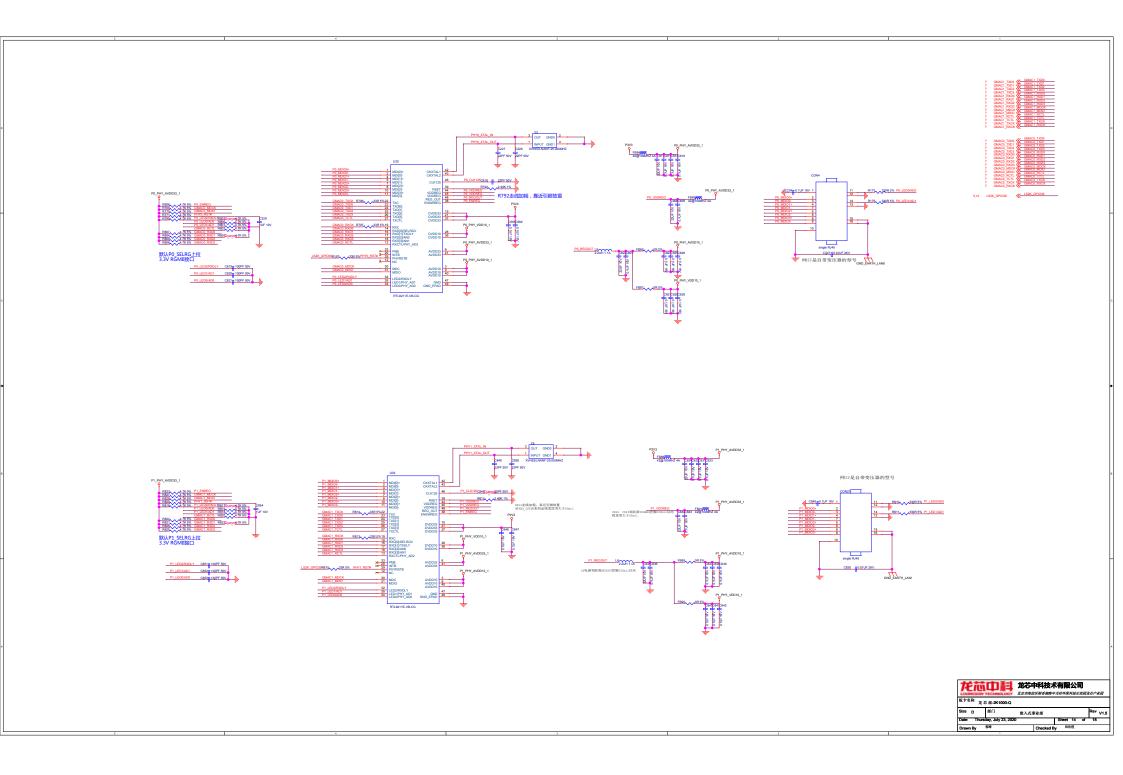


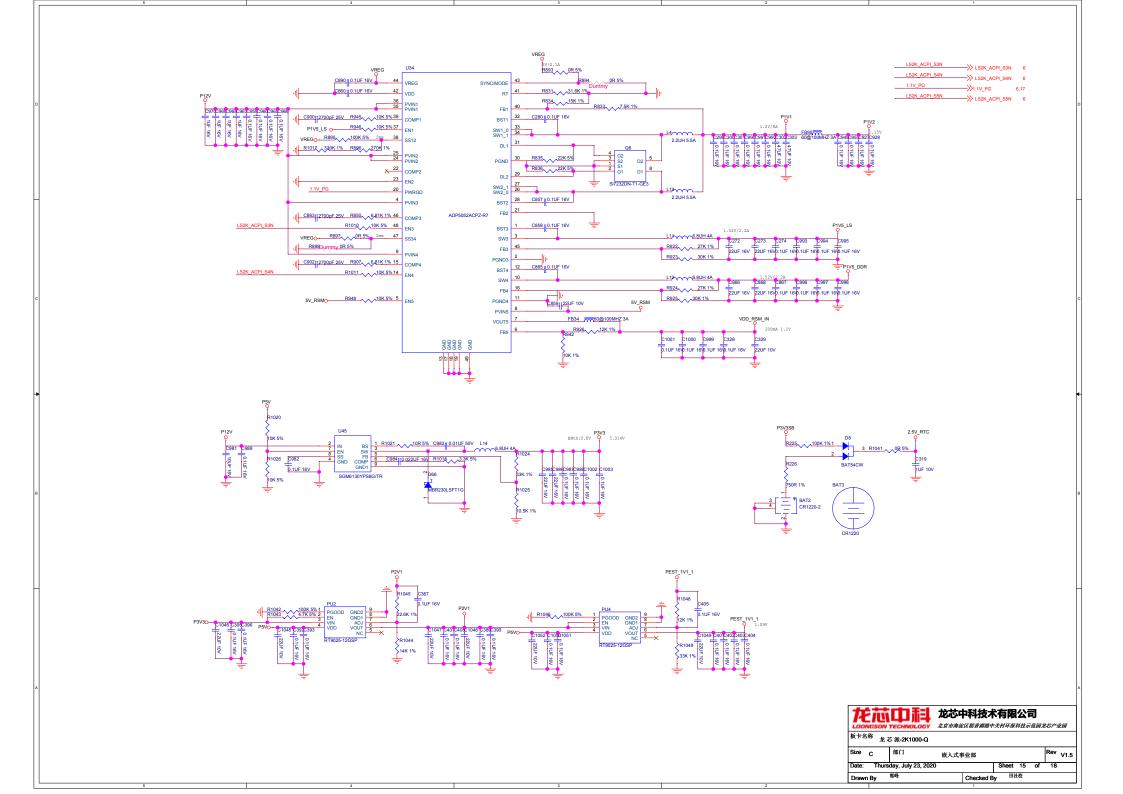
Change the ALC269's outout to HPOUT Change the MIC's circuit Dummy the MIC_JD's resistor Change the HPOUT_JD's resistor to 39.2K

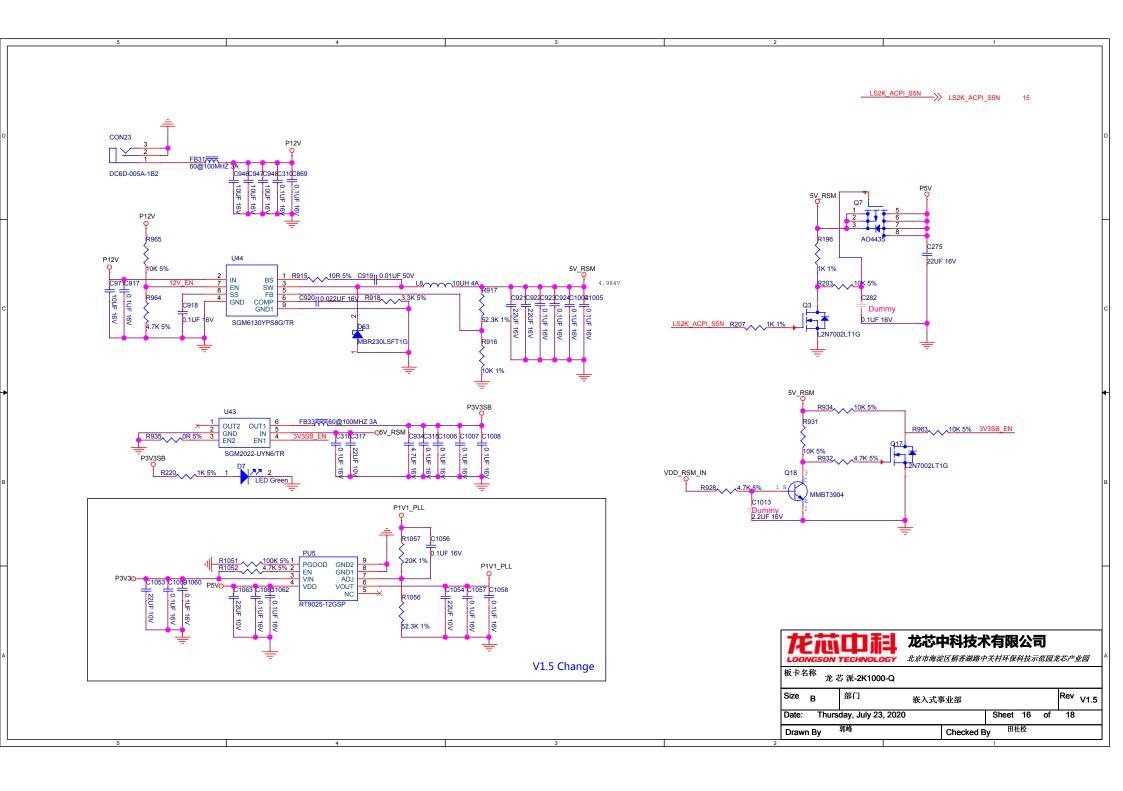


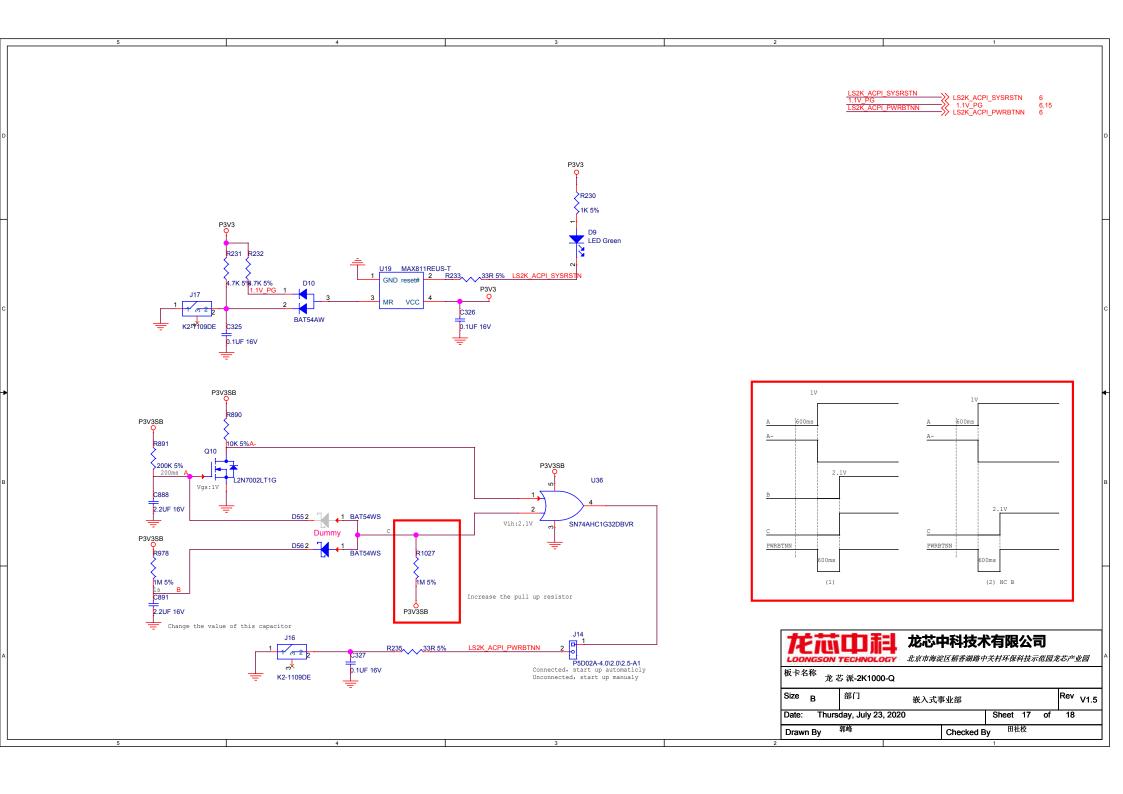












Change history

- 1.del F1 20170812
- 2.LCD change 3.3V to 5V
- 3. Delete the SD card
- 4. Change the EJTAG pin to the standard one
- 5. Change the OTG USB connector to the micro one
- 6. Change the HDMI connector to the standard one
- 7. Change the AUDIO Codec IC, and change the audio jack from two to one and delet its delay circuit
- 8. Increase the Beeper circuit and the power on start up circuit
- 9. Change the PHY chip and the internet connector to the standard one
- 10.CHange the 2.0,1.0,1.25pin to 2.54pin
- 11. Delete the touch IC ZT2083, and change the touch mode to 1-wire mode;
- 12. Increase the M.2 SATA, M.2 PCIE, and PCIE X1
- 13. The power scheme is changed, .Using the PMU of ADP5052 and the RT9018B-25GQW to generate the used voltage, delete the former used power IC
- 14. Change the power entry port from micro USB to the 5V adapter port
- 15.Increase one RS232 connector
- 16.Delete the wifi module
- 17. The PCB size is changed into 120mm*120mm
- 18. Increase the EEPROM.
- 19. Increase the heatsink package and 8 mark points
- 20.Connect the USB1 to PCIE WIFI connector
- 21. Delete the resistors of 15K on the USB circuit
- 22. Changed the 40pin into a 54pin one

2018.10.23

- 1. Change the value of R942, and increase the capicator of C976
- 2. Change U5 from W25X40BVSNIG to MD25D80CTIG
- 3.Change the order of SATA_TXP and SATA_TXN to the right order
- 5. Change the ALC 269's output pin to audiojack, and modify the mic circuit
- 6. Change the resistors that are conneted to the EN pins of ADP 5052
- 7.R225 is changed from 100K to 100R
- 8. Change the channel 1 and channel 2 to parallel mode of ADP5052
- 9.P3V3 is generated by other circuit, the chip is SGM6130
- 10.Increase the pull up resistors to the ${\tt W_DISABLE}$ signals of PCIE WIFI
- 11. Increase the pull up resistor to the auto startup resistor
- 12.Change the ADP5052's inductance's footprint to small ones.

2019.06.18

- 1、R31改为1M
- 2、增加R1028/R1029\R1031\R1030\Q19\Q20\C1013
- 3、RTCRSMRSTN上拉改为P3V3SB
- 4、USB处的供电源该为5VRSM
- 5、增加P3V3 SSD处的滤波电容
- 6、I2C1的信号接到HDMI接口处的I2C总线上,增加电平转换电路
- 7、去电U13、U14两个TVS管5脚上的电源
- 8、S5信号控制P5V的通断,S3信号控制DDR电源的使能。

2019.12.31 V1.4版本

- 1、新增PEST1V1 1的电源产生电路,该部分电路用于给2K的PEST1V1和PLL部分供电
- 2、修改给CPU VDD供电的磁珠,同时该位置增加1个磁珠

2020.06.10 V1.5版本

- 1、新增PLL1V1 1的电源产生电路,该部分电路用于给2K的PLL部分供电(16页)
- 2、新增SATA 100M差分外部参考晶振(选焊,解决SATA概率性找不到问题)7页
- 3、修改多路电源的使能电阻,阻值10K,解决概率性不能上电问题(15页)
- 4、选焊掉C282防止上电5V有瞬间通电现象(16页)

