Рекуррентные нейронные сети







Outline

- 1. RNN intuitions
- 2. Language models
- 3. Memory concept: LSTM
- 4. RNN as encoder for sequential data
- 5. Vanishing gradient
- 6. Names generation from scratch
- 7. Q & A

RNNs generating...

Shakespeare

PANDARUS:

Alas, I think he shall be come approached and the day When little srain would be attain'd into being never fed, And who is but a chain and subjects of his death, I should not sleep.

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They are away this miseries, produced upon my soul, Breaking and strongly should be buried, when I perish The earth and thoughts of many states.

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Well, your wit is in the care of side and that.

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Clown:

Come, sir, I will make did behold your worship.

VIOLA:

I'll drink it.

Algebraic Geometry (Latex)

```
Proof. Omitted.
Lemma 0.1. Let C be a set of the construction.
   Let C be a gerber covering. Let F be a quasi-coherent sheaves of O-modules. We
have to show that
                                   \mathcal{O}_{\mathcal{O}_X} = \mathcal{O}_X(\mathcal{L})
Proof. This is an algebraic space with the composition of sheaves \mathcal{F} on X_{Oute} we
have
                          O_X(F) = \{morph_1 \times_{O_X} (G, F)\}
where G defines an isomorphism F \to F of O-modules.
Lemma 0.2. This is an integer Z is injective.
Proof. See Spaces, Lemma ??.
Lemma 0.3. Let S be a scheme. Let X be a scheme and X is an affine open
covering. Let U \subset X be a canonical and locally of finite type. Let X be a scheme.
Let X be a scheme which is equal to the formal complex.
The following to the construction of the lemma follows.
Let X be a scheme. Let X be a scheme covering. Let
                      b: X \rightarrow Y' \rightarrow Y \rightarrow Y \rightarrow Y' \times_X Y \rightarrow X.
be a morphism of algebraic spaces over S and Y.
Proof. Let X be a nonzero scheme of X. Let X be an algebraic space. Let F be a
quasi-coherent sheaf of O_X-modules. The following are equivalent

 F is an algebraic space over S.

   (2) If X is an affine open covering.
Consider a common structure on X and X the functor O_X(U) which is locally of
finite type.
```

Linux kernel (source code)

```
* If this error is set, we will need anything right after that BSD.
static void action new function(struct s stat info *wb)
 unsigned long flags;
 int lel idx bit = e->edd, *sys & -((unsigned long) *FIRST COMPAT);
 buf[0] = 0xFFFFFFFF & (bit << 4);
 min(inc, slist->bytes);
 printk(KERN WARNING "Memory allocated %02x/%02x, "
   "original MLL instead\n"),
   min(min(multi run - s->len, max) * num data in),
   frame pos, sz + first seg);
 div u64 w(val, inb p);
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 mutex unlock(&func->mutex);
 return disassemble(info->pending bh);
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$$\mathcal{O}_{\mathcal{O}_X} = \mathcal{O}_X(\mathcal{L})$$

.

Proof. This is an algebraic space with the composition of sheaves F on $X_{\acute{e}tale}$ we have

$$\mathcal{O}_X(\mathcal{F}) = \{morph_1 \times_{\mathcal{O}_X} (\mathcal{G}, \mathcal{F})\}$$

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$$b: X \to Y' \to Y \to Y \to Y' \times_X Y \to X.$$

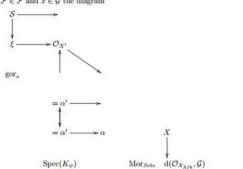
be a morphism of algebraic spaces over S and Y.

Proof. Let X be a nonzero scheme of X. Let X be an algebraic space. Let \mathcal{F} be a quasi-coherent sheaf of \mathcal{O}_X -modules. The following are equivalent

- F is an algebraic space over S.
- (2) If X is an affine open covering.

Consider a common structure on X and X the functor $\mathcal{O}_X(U)$ which is locally of finite type.

This since $F \in F$ and $x \in G$ the diagram



is a limit. Then G is a finite type and assume S is a flat and F and G is a finite type f_* . This is of finite type diagrams, and

- the composition of G is a regular sequence,
- O_{X'} is a sheaf of rings.

Proof. We have see that $X = \operatorname{Spec}(R)$ and $\mathcal F$ is a finite type representable by algebraic space. The property $\mathcal F$ is a finite morphism of algebraic stacks. Then the cohomology of X is an open neighbourhood of U.

Proof. This is clear that G is a finite presentation, see Lemmas ??. A reduced above we conclude that U is an open covering of C. The functor F is a "field

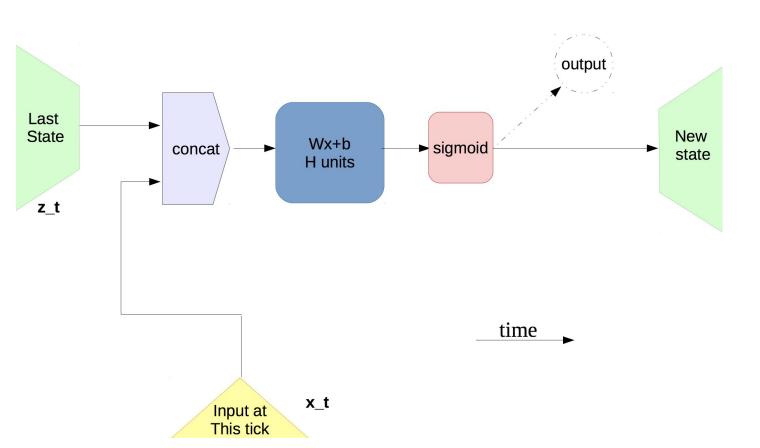
$$\mathcal{O}_{X,x} \longrightarrow \mathcal{F}_{\overline{x}} -1(\mathcal{O}_{X_{\text{étale}}}) \longrightarrow \mathcal{O}_{X_{\epsilon}}^{-1}\mathcal{O}_{X_{\lambda}}(\mathcal{O}_{X_{n}}^{\overline{v}})$$

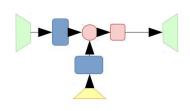
is an isomorphism of covering of \mathcal{O}_{X_i} . If \mathcal{F} is the unique element of \mathcal{F} such that X is an isomorphism.

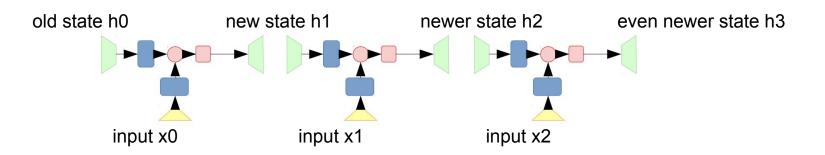
The property \mathcal{F} is a disjoint union of Proposition ?? and we can filtered set of presentations of a scheme \mathcal{O}_{X} -algebra with \mathcal{F} are opens of finite type over S. If \mathcal{F} is a scheme theoretic image points.

If $\mathcal F$ is a finite direct sum $\mathcal O_{X_\lambda}$ is a closed immersion, see Lemma \ref{Lemma} . This is a sequence of $\mathcal F$ is a similar morphism.

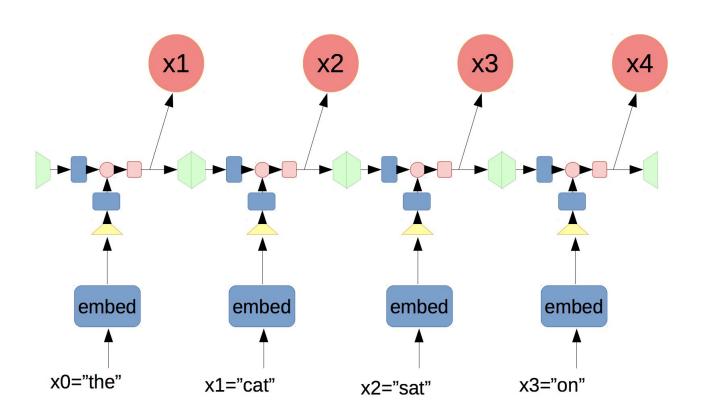
```
#include <asm/io.h>
#include <asm/prom.h>
#include <asm/e820.h>
#include <asm/system info.h>
#include <asm/setew.h>
#include <asm/pgproto.h>
#define REG_PG vesa slot addr pack
#define PFM NOCOMP AFSR(0, load)
#define STACK DDR(type)
                         (func)
#define SWAP ALLOCATE(nr)
                             (e)
#define emulate sigs() arch get unaligned child()
#define access rw(TST) asm volatile("movd %%esp, %0, %3" :: "r" (0)); \
 if ( type & DO READ)
static void stat PC SEC read mostly offsetof(struct seg argsqueue, \
          pC>[1]);
static void
os prefix(unsigned long sys)
#ifdef CONFIG PREEMPT
 PUT PARAM RAID(2, sel) = get state state();
  set pid sum((unsigned long)state, current state str(),
           (unsigned long)-1->lr full; low;
}
```







We use same weight matrices for all steps



Recurrent neural network: with formulas

$$h_{0} = \bar{0}$$

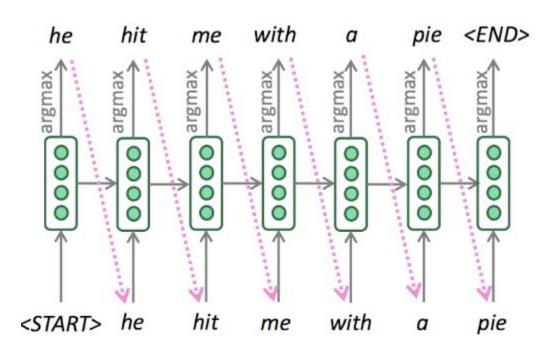
$$h_{1} = \sigma(\langle W_{\text{hid}}[h_{0}, x_{0}] \rangle + b)$$

$$h_{2} = \sigma(\langle W_{\text{hid}}[h_{1}, x_{1}] \rangle + b) = \sigma(\langle W_{\text{hid}}[\sigma(\langle W_{\text{hid}}[h_{0}, x_{0}] \rangle + b, x_{1}] \rangle + b)$$

$$h_{i+1} = \sigma(\langle W_{\text{hid}}[h_{i}, x_{i}] \rangle + b)$$

$$P(x_{i+1}) = \operatorname{softmax}(\langle W_{\text{out}}, h_{i} \rangle + b_{\text{out}})$$

Recurrent neural network: text generation



RNNs generating...

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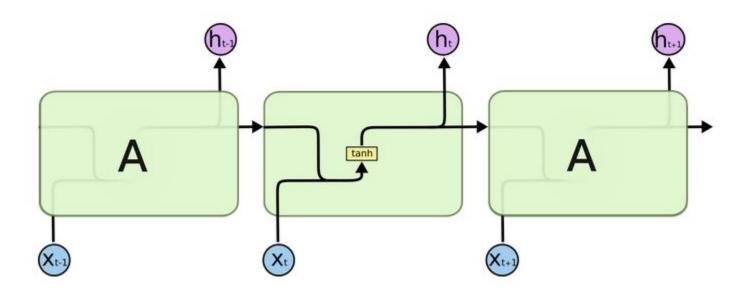
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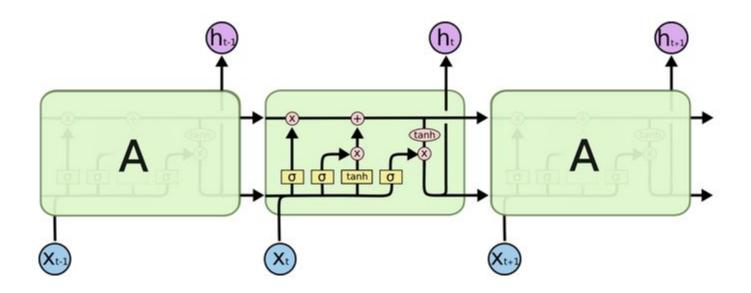
Linux kernel (source code)

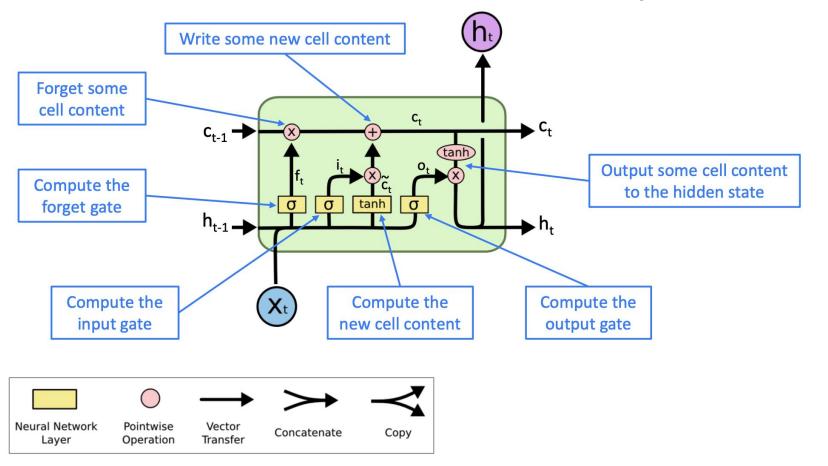
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 int lel idx bit = e->edd, *sys & -((unsigned long) *FIRST COMPAT);
 buf[0] = 0xFFFFFFFF & (bit << 4);
 min(inc, slist->bytes);
 printk(KERN WARNING "Memory allocated %02x/%02x, "
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   min(min(multi run - s->len, max) * num data in),
   frame pos, sz + first seg);
 div u64 w(val, inb p);
 spin unlock(&disk->queue lock);
 mutex unlock(&s->sock->mutex);
 mutex unlock(&func->mutex);
 return disassemble(info->pending bh);
```

Vanilla RNN



LSTM





Forget gate: controls what is kept vs forgotten, from previous cell state

Input gate: controls what parts of the new cell content are written to cell

Output gate: controls what parts of cell are output to hidden state

New cell content: this is the new content to be written to the cell

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ight) \ oldsymbol{i}^{(t)} &= \sigma \left(oldsymbol{W}_i oldsymbol{h}^{(t-1)} + oldsymbol{U}_i oldsymbol{x}^{(t)} + oldsymbol{b}_i
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ight) \end{aligned}$$

Sigmoid function: all gate

values are between 0 and 1

$$oldsymbol{i}^{(t)} = \sigma \left[oldsymbol{W}_i oldsymbol{h}^{(t-1)} + oldsymbol{U}_i oldsymbol{x}^{(t)} + oldsymbol{b}_i
ight]$$

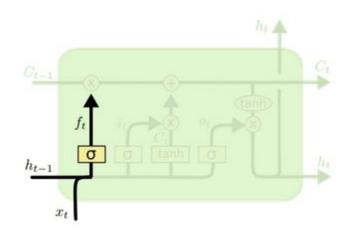
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$$ilde{oldsymbol{c}}(ilde{oldsymbol{c}}^{(t)} = anh\left(oldsymbol{W}_coldsymbol{h}^{(t-1)} + oldsymbol{U}_coldsymbol{x}^{(t)} + oldsymbol{b}_c
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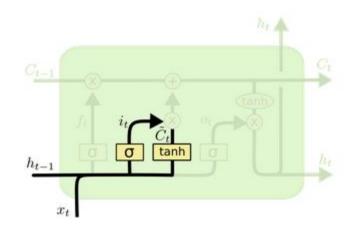
$$\boldsymbol{c}^{(t)} = \boldsymbol{f}^{(t)} \circ \boldsymbol{c}^{(t-1)} + \boldsymbol{i}^{(t)} \circ \tilde{\boldsymbol{c}}^{(t)}$$

$$m{ au} m{h}^{(t)} = m{o}^{(t)} \circ anh m{c}^{(t)}$$

Gates are applied using element-wise product

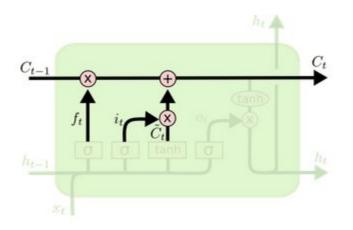


$$f_t = \sigma\left(W_f \cdot [h_{t-1}, x_t] + b_f\right)$$

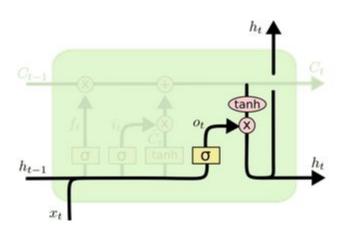


$$i_t = \sigma \left(W_i \cdot [h_{t-1}, x_t] + b_i \right)$$

$$\tilde{C}_t = \tanh(W_C \cdot [h_{t-1}, x_t] + b_C)$$



$$C_t = f_t * C_{t-1} + i_t * \tilde{C}_t$$



$$o_t = \sigma (W_o [h_{t-1}, x_t] + b_o)$$

$$h_t = o_t * \tanh (C_t)$$

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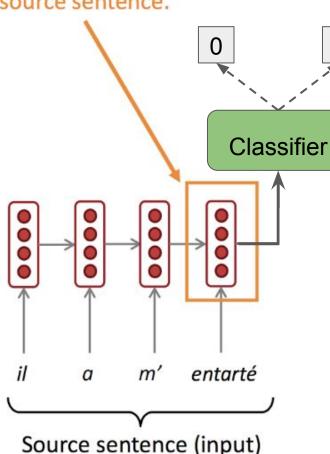
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Gates are applied using element-wise product

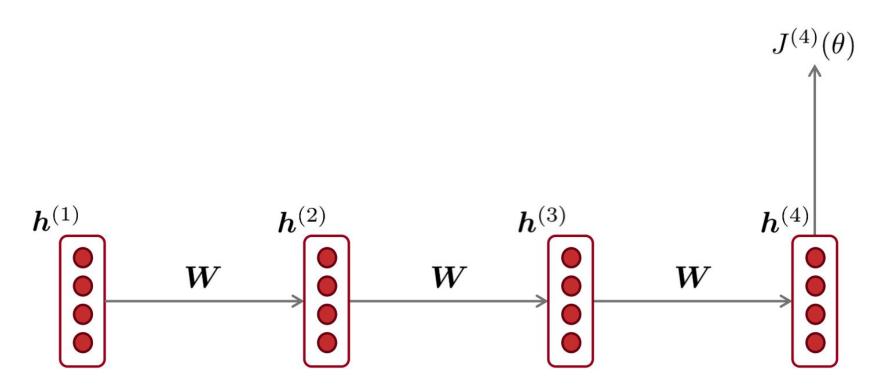
Encoding of the source sentence.

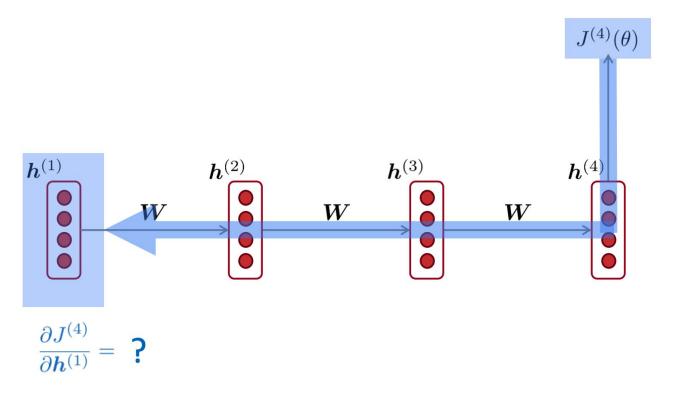
RNN as encoder for sequential data

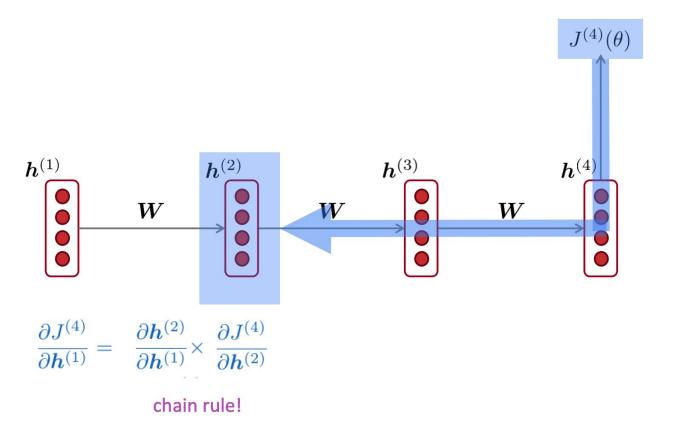


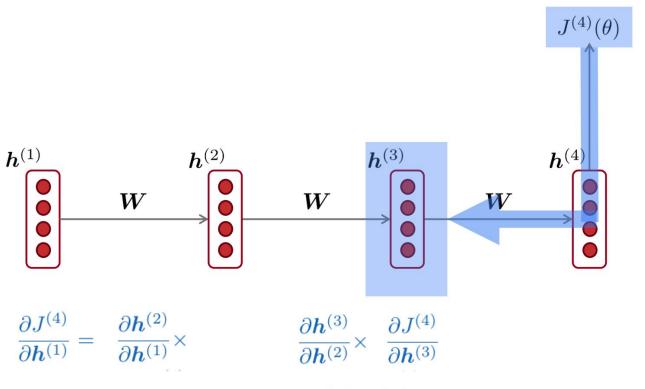
RNNs can be used to encode an input sequence in a fixed size vector.

This vector can be treated as a representation of input sequence.

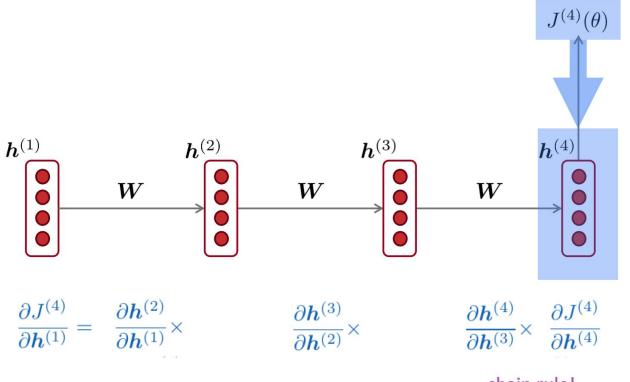








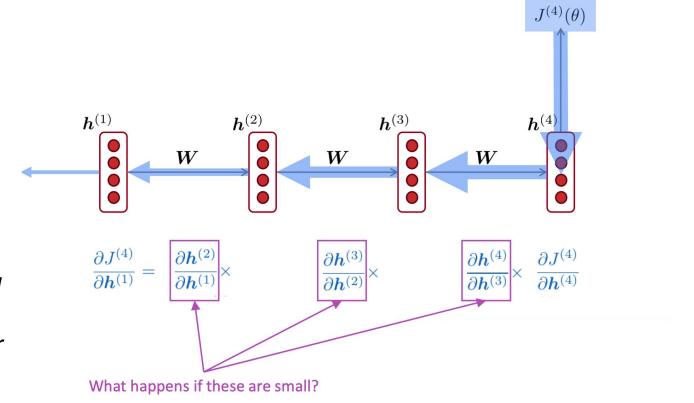
chain rule!



chain rule!

Vanishing gradient problem:

When the derivatives are small, the gradient signal gets smaller and smaller as it backpropagates further

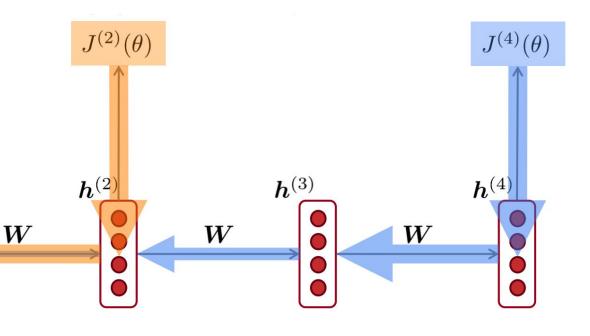


More info: "On the difficulty of training recurrent neural networks", Pascanu et al, 2013 http://proceedings.mlr.press/v28/pascanu13.pdf

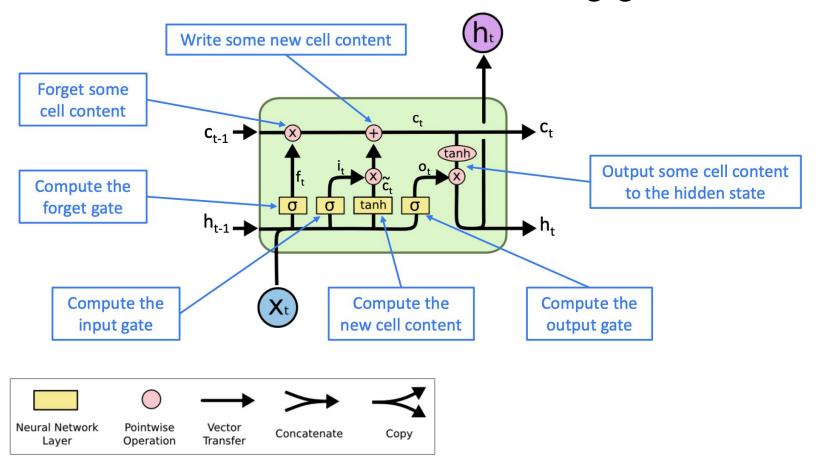
Gradient signal from far away is lost because it's much smaller than from close-by.

So model weights updates will be based only on short-term effects.

 $oldsymbol{h}^{(1)}$



Vanishing gradient: LSTM



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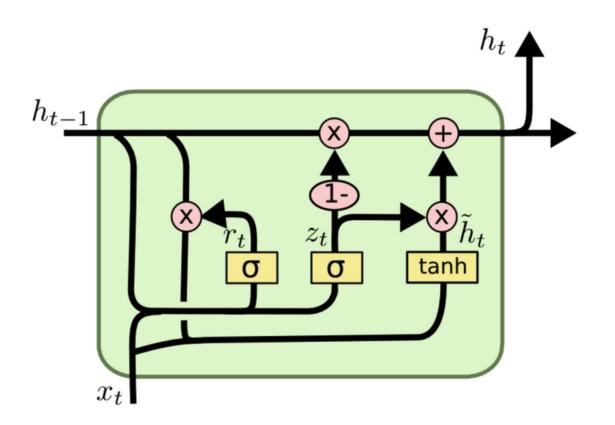
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Gates are applied using element-wise product

All these are vectors of same length *n*

Vanishing gradient: GRU



Vanishing gradient: GRU

<u>Update gate:</u> controls what parts of hidden state are updated vs preserved

Reset gate: controls what parts of previous hidden state are used to compute new content

New hidden state content: reset gate selects useful parts of prev hidden state. Use this and current input to compute new hidden content.

Hidden state: update gate simultaneously controls what is kept from previous hidden state, and what is updated to new hidden state content

$$m{u}^{(t)} = \sigma \left(m{W}_u m{h}^{(t-1)} + m{U}_u m{x}^{(t)} + m{b}_u
ight)$$
 $m{r}^{(t)} = \sigma \left(m{W}_r m{h}^{(t-1)} + m{U}_r m{x}^{(t)} + m{b}_r
ight)$

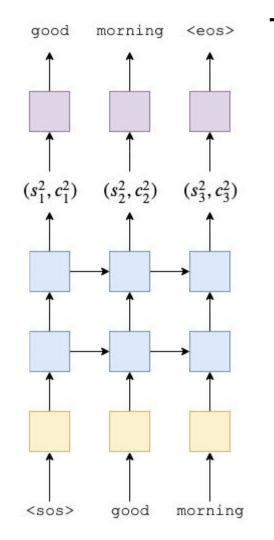
$$m{ ilde{h}}^{(t)} = anh\left(m{W}_h(m{r}^{(t)} \circ m{h}^{(t-1)}) + m{U}_hm{x}^{(t)} + m{b}_h
ight)$$
 $m{h}^{(t)} = (1 - m{u}^{(t)}) \circ m{h}^{(t-1)} + m{u}^{(t)} \circ m{ ilde{h}}^{(t)}$

How does this solve vanishing gradient?
Like LSTM, GRU makes it easier to retain info long-term (e.g. by setting update gate to 0)

Vanishing gradient: LSTM vs GRU

- LSTM and GRU are both great
 - GRU is quicker to compute and has fewer parameters than LSTM
 - There is no conclusive evidence that one consistently performs better than the other
 - LSTM is a good default choice (especially if your data has particularly long dependencies, or you have lots of training data)

- RNN is a great choice for data with sequential structure
- Multi-layer RNN can also be of great use
- Rule of thumb: start with LSTM, but switch to GRU if you want something more efficient



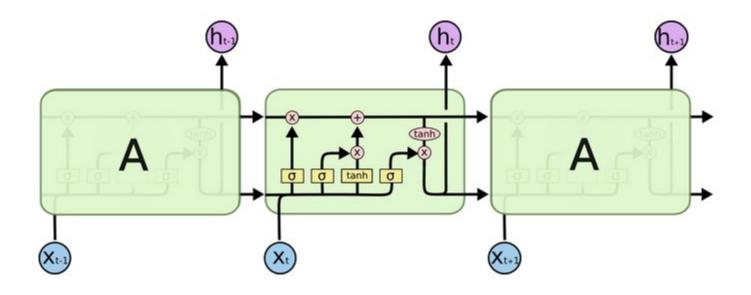
Q & A

Backlog

That's all. Feel free to ask any questions.

RNNs, we are coming. Time to generate some names!

Recap: LSTM



Exploding gradient problem

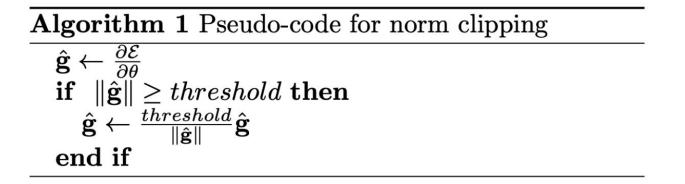
 If the gradient becomes too big, then the SGD update step becomes too big:

$$heta^{new} = heta^{old} - \overbrace{lpha}^{ ext{learning rate}} \int_{ ext{gradient}}^{ ext{learning rate}} \int_{ ext{gradient}}^{ ext{gradient}} d\theta^{new}$$

- This can cause bad updates: we take too large a step and reach a bad parameter configuration (with large loss)
- In the worst case, this will result in Inf or NaN in your network (then you have to restart training from an earlier checkpoint)

Exploding gradient solution

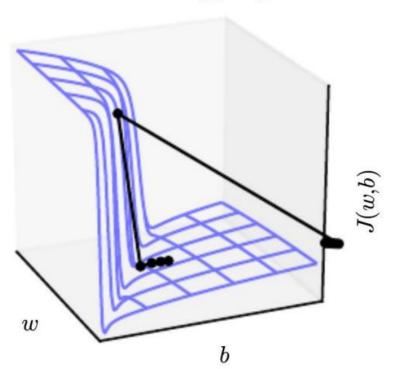
 Gradient clipping: if the norm of the gradient is greater than some threshold, scale it down before applying SGD update



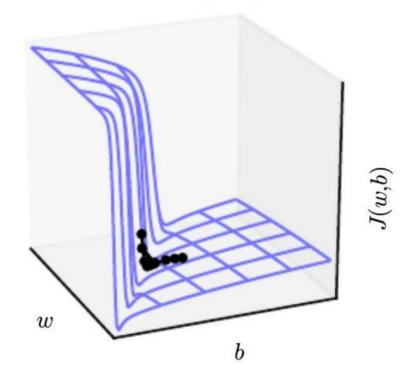
 Intuition: take a step in the same direction, but a smaller step

Exploding gradient solution

Without clipping



With clipping



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Vanishing gradient in non-RNN

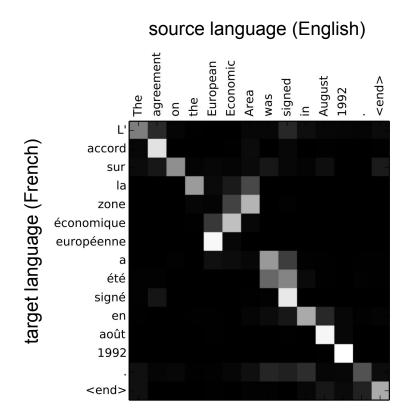
Vanishing gradient is present in all deep neural network architectures.

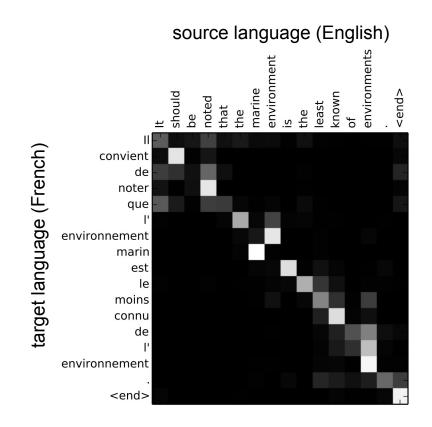
- Due to chain rule / choice of nonlinearity function, gradient can become vanishingly small during backpropagation
- Lower levels are hard to train and are trained slower
- Potential solution(but not actually for that problem): dense connections (just like in DenseNet)

Conclusion:

Though vanishing/exploding gradients are a general problem, RNNs are particularly unstable due to the repeated multiplication by the same weight matrix [Bengio et al, 1994]. Gradients magnitude drops exponentially with connection length.

Attention maps in translation





Very Deep Backlog

Vanishing gradient in non-RNN

Vanishing gradient is present in all deep neural network architectures.

- Due to chain rule / choice of nonlinearity function, gradient can become vanishingly small during backpropagation
- Lower levels are hard to train and are trained slower
- Potential solution: direct (or skip-) connections (just like in ResNet)

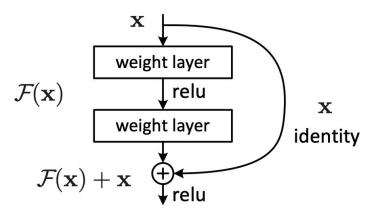


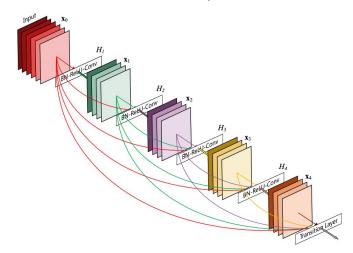
Figure 2. Residual learning: a building block.

Source: "Deep Residual Learning for Image Recognition", He et al, 2015. https://arxiv.org/pdf/1512.03385.pdf

Vanishing gradient in non-RNN

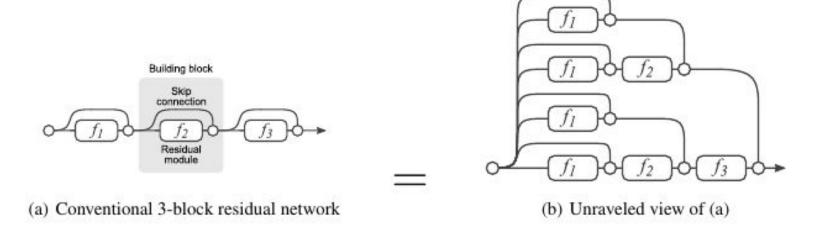
Vanishing gradient is present in all deep neural network architectures.

- Due to chain rule / choice of nonlinearity function, gradient can become vanishingly small during backpropagation
- Lower levels are hard to train and are trained slower
- Potential solution: dense connections (just like in DenseNet)



Another view on ResNets and vanishing gradient

"Residual Networks Behave Like Ensembles of Relatively Shallow Networks"



Source: https://arxiv.org/pdf/1605.06431.pdf