

1-4-2021

CS341 - Virtual Memory Assignment

Ans 1: Assume the page size is of x bits

Given, size of virtual memory is 2^{46}

$$\Rightarrow \boxed{\text{no. of pages in virtual memory is } \frac{2^{46}}{2^x} = 2^{46-x}}$$

Since, T_1 occupies exactly one page

$$\Rightarrow \boxed{\text{Size of } T_1 = 2^x \text{ bytes}}$$

$$\text{No. of entries in } T_1 = \frac{\text{Size of } T_1}{\text{Size of 1 entry}} = \frac{2^x}{4} = 2^{x-2}$$

$$\Rightarrow \text{No. of second level page tables } (T_2) = \text{no. of entries in } T_1 = 2^{x-2}$$

$$\begin{aligned} \therefore \text{Total size of second level page tables } (T_2) \\ &= \text{No. of page tables} \times \text{size of each table.} \\ &= 2^{x-2} \times 2^x = \underline{\underline{2^{2x-2}}} \end{aligned}$$

$$\begin{aligned} \text{Total entries in second level page tables } (T_2) \\ &= \frac{\text{Total Size}}{\text{Size of 1 entry}} = \frac{2^{2x-2}}{4} = 2^{2x-4} \end{aligned}$$

$$\begin{aligned} \text{Again, No. of third level page tables } (T_3) &= \text{no. of entries in } T_2 \\ &= 2^{2x-4} \end{aligned}$$

Now, total entries in level three page tables

$$= \frac{\text{total size of level three page tables}}{\text{size of 1 entry}}$$

$$= \frac{\text{total no. of level three page tables} \times 2^x}{4}$$

$$= 2^{2x-4} \times \frac{2^x}{4} = \underline{\underline{2^{3x-6}}}$$

We also know,

$$\left. \begin{array}{l} \text{total entries in level three} \\ \text{page tables} \end{array} \right\} = \left\{ \begin{array}{l} \text{no. of pages in} \\ \text{virtual memory} \end{array} \right.$$

$$\Rightarrow 2^{3x-6} = 2^{46-x}$$

$$\Rightarrow 2^{4x-6} = 2^{46}$$

$$\Rightarrow 2^{4x} = 2^{52}$$

$$\Rightarrow 2^x = 2^{\frac{52}{4}} = 2^{13}$$

$$\Rightarrow \boxed{x = 13}$$

\therefore Page size is of 13 bits \Rightarrow Page size is 2^{13} bytes

Hence page size is 8KB

Ans 2: Given,

memory access time = 20ns

page fault service time = 10ms

$$\text{miss ratio} = \frac{1}{10^6} \Rightarrow \text{hit ratio} = \left(1 - \frac{1}{10^6}\right)$$

effective memory access time (EAT)

$$= (\text{hit ratio})^* (\text{memory access time}) + (\text{miss ratio})^* (\text{page fault service time})$$

$$= \left(1 - \frac{1}{10^6}\right) \times 20 + \left(\frac{1}{10^6}\right) \times 10 \times 10^6$$

$$= 20 - (2 \times 10^{-5}) + 10 \text{ ns} = 30 - (2 \times 10^{-5}) \text{ ns}$$

$$\approx \underline{\underline{30 \text{ ns}}}$$

\therefore Effective memory access time is 30ns approximately,
To be precise it is 29.99998 ns

Ans 3: Given, physical address has 36-bit
 virtual address has 32-bit
 page frame size is 4KB
 page table entry size is 4 bytes

Address Translation involves three-level page table,

bits 30-31	→	index into level I page table (2 bits)
21-29	→	II (9 bits)
12-20	→	III (9 bits)
0-11	→	offset within the page. (12 bits)

Now, max no. of page frames = $\frac{\text{Size of physical address space}}{\text{Page Size}}$

$$= \frac{2^{36}}{4 \times 2^{10}} = \underline{\underline{2^{24}}}$$

∴ 24-bits are required to index the page numbers in 3rd level page table.

Now, to access second level page table entry, 9 bits are used.

$$\therefore \text{Size of second level page table} = \underset{\substack{\text{total no. of} \\ \text{entries}}}{2^9} \times \underset{\substack{\text{size of one} \\ \text{entry}}}{4} \text{ bytes}$$

\Rightarrow Size of second level page table is 2^{11} bytes

Hence, in a 2^{36} physical address space, we can have

$$\frac{2^{36}}{2^{11}} \text{ second level page tables } (= 2^{25})$$

\therefore 25-bits are required to index the second level page tables

Similarly, first level page table entry requires 9 bits to access.

$$\therefore \text{Size of first level page table} = 2^9 \times 4 \text{ bytes} = 2^{11} \text{ bytes}$$

\Rightarrow There will be 2^{25} first level page tables in a 2^{36} physical address space. Hence, we need 25-bits for indexing

\therefore The answer is 25, 25, 24 bits to address next level page table in first, second & third level page tables respectively

Ans 4

Given,

capacity = 3 page frames

initially, none of the pages are available in the memory

Reference string = 1, 2, 1, 3, 7, 4, 5, 6, 3, 1

Optimal replacement policy is used.

Optimal replacement policy: Replace the page which is not used in the longest dimension of time in future.

	*	1	2	1	3	7	4	5	6	3	1	
F1	X	①	1	1	1	1	1	1	1	1	1	
F2	X	X	②	2	2	⑦	④	⑤	⑥	6	6	
F3	X	X	X	X	③	3	3	3	3	3	3	
R	0	1	2	3	4	5	6	7	8	9	10	

no. of references

no pages available in memory

for the next reference, 2 will be replaced. because it is not used called in future

next, 7 will be replaced because it is not used in future

for next reference, 5 is replaced because it is not used in future.

for next reference, 4 is replaced because it is not used in future

7 page faults

Therefore, there will be 7 page faults

Ans 5

Given,

$$\text{TLB access time} = 10 \text{ ms}$$

$$\text{Memory access time} = 80 \text{ ms}$$

$$\text{TLB hit ratio} = 0.6 \Rightarrow \text{TLB miss ratio} = 1 - 0.6 = \underline{0.4}$$

effective

memory access time }
(EMAT)

$$= (\text{TLB hit ratio}) \times (\text{TLB access time} + \text{memory access time}) \\ + (\text{TLB miss ratio}) \times (\text{TLB access time} + \text{page table access time} \\ + \text{memory access time})$$

$$= 0.6 \times (10 + 80) + 0.4 \times (10 + 80 + 80) \text{ ms}$$

[I assume page table lookup takes only one memory access]
 \Rightarrow page table access time = 80 ms

$$= 0.6 \times 90 + 0.4 \times 170 \text{ ms}$$

$$= 54 + 68 = \underline{122 \text{ ms}}$$

\Rightarrow

effective memory access time is 122 milliseconds