



Static Binary Rewriting

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Static binary rewriting is important

Applications

- ➊ Software fault isolation (SFI) [[WLAG93](#)]
- ➋ Control Flow Integrity (CFI) [[ABEL09](#)]
- ➌ Binary code hardening (e.g., STIR [[WMHL12a](#)])
- ➍ Binary code reuse (e.g., BCR [[CJMS10](#)])
- ➎ Platform-specific optimizations [[ASE⁺13](#)]

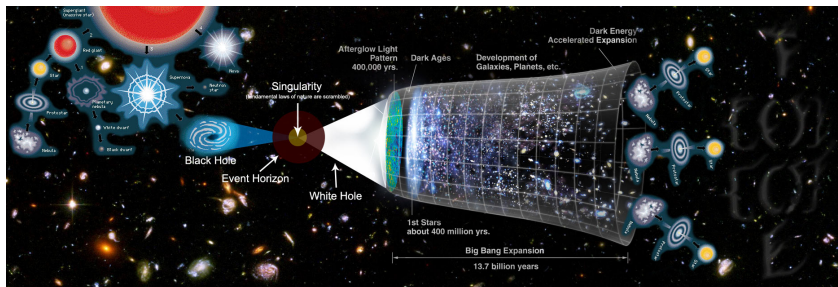
Challenges in disassembling

- ➊ Recognizing and relocating static memory addresses
- ➋ Handling dynamically computed memory addresses
- ➌ Differentiating code from data
- ➍ Handling function pointer arguments (e.g., callbacks)
- ➎ Handling Position Independent Code (PIC)

Existing static rewriters: w/ assumptions and heuristics

- ❶ Assume certain compiler (e.g., gcc) generated binaries
- ❷ Assume having debugging symbols
- ❸ Assume knowledge of APIs (call backs)
- ❹ Assume no code and data interleaving
- ❺ Assume relocation metadata
- ❻ Assume integer and pointer can be differentiated
- ❼ ...

MULTIVERSE: the first heuristic-free static binary rewriter



“Everything that can happen does happen.” [CF12]

A Running Example

```

1 // gcc -m32 -o sort cmp.o fstring.o sort.c
2 #include <stdio.h>
3 #include <unistd.h>
4
5 extern char *array[6];
6 int gt(void *, void *);
7 int lt(void *, void *);
8 char* get_fstring(int select);
9
10 void model(void){
11     qsort(array, 5, sizeof(char*), gt);
12 }
13 void mode2(void){
14     qsort(array, 5, sizeof(char*), lt);
15 }
16
17 void (*modes[2])() = {model, mode2};
18
19 void main(void){
20     int p = getpid() & 1;
21     printf("get_fstring(0),p);
22     (*modes[p])();
23     print_array();
24 }

```

(a) Source code of **sort.c**

```

1 // gcc -m32 -c -o cmp.o cmp.c -fPIC -O2
2 #include <stdio.h>
3 #include <stdlib.h>
4 #include <string.h>
5
6 char *array[6] = {"foo", "bar", "quus", "baz", "flux"};
7 char* get_fstring(int select);
8
9 void print_array(){
10     int i;
11     for (i = 0; i < 5; i++){
12         printf(stdout, get_fstring(i), array[i]);
13     }
14 }
15 int lt(void *a, void *b){
16     return strcmp(*(char **) a, *(char **)b);
17 }
18
19 int gt(void *a, void *b){
20     return strcmp(*(char **) b, *(char **)a);
21 }

```

(c) Source code of **cmp.c**

```

Hex dump of section '.rodata':
0x080487f8 03000000 01000200 66666f00 62617200 .....foo.bar.
0x08048778 7175757a 0062617a 00666c75 7800      quus.baz.flux.

```

(e) Hexdump of **.ro.data** section

```

Hex dump of section '.data':
0x0804a01c 00000000 00000000 70870408 74870408 .....p...t...
0x0804a02c 78870408 7d870408 81870408 00000000 x.....
0x0804a03c f4850408 20860408

```

(f) Hexdump of **.data** section

```

1 ;name -f elf fstring.asm
2 BITS 32
3 GLOBAL get_fstring
4 SECTION .text
5 get_fstring:
6     mov eax,[esp+4]
7     cmp eax,0
8     ja after
9     mov eax,msg2
10    ret
11 msg1:
12    db 'mode: %d', 10, 0
13 msg2:
14    db '%a', 10, 0
15 after:
16    mov eax,msg1
17    ret

```

(b) Source code of **fstring.asm**

```

8048510 <print_array:
...
8048515: 53                                push    %ebx
8048516: a8 b1 00 00 00                  call    80485cc <_i686.get_pc_thunk.bx>
804851b: 81 c3 d9 1a 00 00              add     $0x1ad9,%ebx
8048521: 83 ac 1c                        sub     $0x1c,%esp
8048524: 8b ab fc ff ff ff              mov     -0x4(%ebx),%ebp
...
80485a0 <gt;:
80485a0: 53                                push    %ebx
...
80485cc <_i686.get_pc_thunk.bx>:
80485cc: 8b 1c 24                        mov     (%esp),%ebx
80485cf: c3                                ret
...
80485d0 <get_fstring>:
80485d0: 8b 44 24 04                    mov     0x4(%esp),%eax
80485d4: 83 f8 00                       cmp     $0x0,%eax
80485d7: 74 14                          je      80485da <after>
80485d9: b8 a9 85 04 08                mov     $0x80485a9,%eax
80485da: c3                                ret
80485de: 6d                                insl    (%dx),%eax:(%edi)
80485e0: 6f                                outsl   (%eax),(%dx)
80485e1: 64 65 3a 20                    fs cmp  %fs:%gs:(%eax),%ah
...
80485f4 <mode1>:
...
80485fa: c7 44 24 0c a0 85 04          movl    $0x80485a0,0xc(%esp)
8048601: 08                                movl    0x0,%eax
8048602: c7 44 24 08 04 00 00          movl    $0x4,0x8(%esp)
8048609: 00                                movl    0x0,%eax
804860a: c7 44 24 04 05 00 00          movl    $0x5,0x4(%esp)
8048611: 00                                movl    0x0,%eax
8048612: c7 04 24 2a a0 04 08          movl    $0x804a024, (%esp)
8048619: a8 12 fa ff ff                call    8048430 <qsort@plt>
...
804864c <main>:
...
8048678: a8 73 fd ff ff                call    80483f0 <printf@plt>
804867d: 8b 44 24 1c                    mov     0x1c(%esp),%eax
8048681: 8b 04 85 3c a0 04 08          mov     0x804a03c(,%eax,4),%eax
8048688: ff d0                          call    *%eax
...

```

(d) Partial binary code of **sort**

A Running Example

```
1 // gcc -m32 -o sort cmp.o fstring.o sort.c
2 #include <stdio.h>
3 #include <unistd.h>
4
5 extern char *array[6];
6 int gt(void *, void *);
7 int lt(void *, void *);
8 char* get_fstring(int select);
9
10 void model(void){
11     qsort(array, 5, sizeof(char*), gt);
12 }
13 void mode2(void){
14     qsort(array, 5, sizeof(char*), lt);
15 }
16
17 void (*modes[2])() = {model, mode2};
18
19 void main(void){
20     int p = getpid() & 1;
21     printf(get_fstring(0),p);
22     (*modes[p])();
23     print_array();
24 }
```

C4

C4

C1

C2

(a) Source code of `sort.c`

A Running Example

```
1 ;nasm -f elf fstring.asm
2 BITS 32
3 GLOBAL get_fstring
4 SECTION .text
5 get_fstring:
6     mov eax,[esp+4]
7     cmp eax,0
8     jz after
9     mov eax,msg2
10    ret
11 msg1:
12    db 'mode: %d', 10, 0
13 msg2:
14    db '%s', 10, 0
15 after:
16    mov eax,msg1
17    ret
```

C3

C3

(b) Source code of **fstring.asm**

A Running Example

```
1 // gcc -m32 -c -o cmp.o cmp.c -fPIC -O2
2 #include <stdio.h>
3 #include <stdlib.h>
4 #include <string.h>
5
6 char *array[6] = {"foo", "bar", "quuz", "baz", "flux"};
7 char* get_fstring(int select);
8
9 void print_array(){
10     int i;
11     for (i = 0; i < 5; i++){
12         fprintf(stdout, get_fstring(1), array[i]);
13     }
14 }
15 int lt(void *a, void *b){
16     return strcmp(*(char **) a, *(char **)b);
17 }
18
19 int gt(void *a, void *b){
20     return strcmp(*(char **) b, *(char **)a);
21 }
```

C1

C5

(c) Source code of `cmp.c`

A Running Example

```

8048510 <print_array>:
...
8048515: 53                push    %ebx
8048516: e8 b1 00 00 00    call   80485cc <__i686.get_pc_thunk.bx>
804851b: 81 c3 d9 1a 00 00 add     $0x1ad9,%ebx
8048521: 83 ec 1c          sub     $0x1c,%esp
8048524: 8b ab fc ff ff    mov     -0x4(%ebx),%ebp
...
80485a0 <gt>:
80485a0: 53                push    %ebx
...
80485cc <__i686.get_pc_thunk.bx>:
80485cc: 8b 1c 24          mov     (%esp),%ebx
80485cf: c3                ret
...
80485d0 <get_fstring>:
80485d0: 8b 44 24 04       mov     0x4(%esp),%eax
80485d4: 83 f8 00          cmp     $0x0,%eax
80485d7: 74 14             je      80485ed <after>
80485d9: b8 e9 85 04 08    mov     $0x80485e9,%eax
80485de: c3                ret
80485df: 6d                insl    (%dx),%es:(%edi)
80485e0: 6f                outsl   %ds:(%esi),(%dx)
80485e1: 64 65 3a 20       fs cmp  %fs:%gs:(%eax),%ah
...
80485f4 <model>:
...
80485fa: c7 44 24 0c a0 85 04 movl    $0x80485a0,0xc(%esp)
8048601: 08
8048602: c7 44 24 08 04 00 00 movl    $0x4,0x8(%esp)
8048609: 00
804860a: c7 44 24 04 05 00 00 movl    $0x5,0x4(%esp)
8048611: 00
8048612: c7 04 24 24 a0 04 08 movl    $0x804a024, (%esp)
8048619: e8 12 fe ff ff    call   8048430 <qsort@plt>
...
804864c <main>:
...
8048678: e8 73 fd ff ff    call   80483f0 <printf@plt>
804867d: 8b 44 24 1c       mov     0x1c(%esp),%eax
8048681: 8b 04 85 3c a0 04 08 mov     0x804a03c(%eax,4),%eax
8048688: ff d0            call   *%eax
...

```

(d) Partial binary code of `sort`

A Running Example

Hex dump of section `'.rodata':`

```
0x08048768 03000000 01000200 666f6f00 62617200 .....foo.bar.  
0x08048778 7175757a 0062617a 00666c75 7800      quuz.baz.flux.
```

(e) Hexdump of `ro.data` section

Hex dump of section `'.data':`

```
0x0804a01c 00000000 00000000 70870408 74870408 .....p...t...  
0x0804a02c 78870408 7d870408 81870408 00000000 x...}.....  
0x0804a03c f4850408 20860408                .... ..
```

C1

(f) Hexdump of `.data` section

A Running Example

```

1 // gcc -m32 -o sort cmp.o fstring.o sort.c
2 #include <stdio.h>
3 #include <unistd.h>
4
5 extern char *array[6];
6 int gt(void *, void *);
7 int lt(void *, void *);
8 char* get_fstring(int select);
9
10 void model(void){
11     qsort(array, 5, sizeof(char*), gt);
12 }
13 void mode2(void){
14     qsort(array, 5, sizeof(char*), lt);
15 }
16
17 void (*modes[2])() = {model, mode2};
18
19 void main(void){
20     int p = getpid() & 1;
21     printf("get_fstring(0),p);
22     (*modes[p])();
23     print_array();
24 }

```

(a) Source code of **sort.c**

```

1 // gcc -m32 -c -o cmp.o cmp.c -fPIC -O2
2 #include <stdio.h>
3 #include <stdlib.h>
4 #include <string.h>
5
6 char *array[6] = {"foo", "bar", "quus", "baz", "flux"};
7 char* get_fstring(int select);
8
9 void print_array(){
10     int i;
11     for (i = 0; i < 5; i++){
12         printf(stdout, get_fstring(i), array[i]);
13     }
14 }
15 int lt(void *a, void *b){
16     return strcmp(*(char **) a, *(char **)b);
17 }
18
19 int gt(void *a, void *b){
20     return strcmp(*(char **) b, *(char **)a);
21 }

```

(c) Source code of **cmp.c**

```

Hex dump of section '.rodata':
0x080487f8 03000000 01000200 66666f00 62617200 .....foo.bar.
0x08048778 7175757a 0062617a 00666c75 7800      quus.baz.flux.

```

(e) Hexdump of **.ro.data** section

```

Hex dump of section '.data':
0x0804a01c 00000000 00000000 70870408 74870408 .....p...t...
0x0804a02c 78870408 7d870408 81870408 00000000 x.....
0x0804a03c f4850408 20860408

```

(f) Hexdump of **.data** section

```

1 ;name -f elf fstring.asm
2 BITS 32
3 GLOBAL get_fstring
4 SECTION .text
5 get_fstring:
6     mov eax,[esp+4]
7     cmp eax,0
8     ja after
9     mov eax,msg2
10    ret
11 msg1:
12    db 'mode: %d', 10, 0
13 msg2:
14    db '%a', 10, 0
15 after:
16    mov eax,msg1
17    ret

```

(b) Source code of **fstring.asm**

```

8048510 <print_array:
...
8048515: 53                                push    %ebx
8048516: a8 b1 00 00 00                  call    80485cc <_i686.get_pc_thunk.bx>
804851b: 81 c3 d9 1a 00 00              add     $0xad9,%ebx
8048521: 83 ac 1c                        sub     $0xc,%esp
8048524: 8b ab fc ff ff ff              mov     -0x4(%ebx),%ebp
...
80485a0 <gt;:
80485a0: 53                                push    %ebx
...
80485cc <_i686.get_pc_thunk.bx>:
80485cc: 8b 1c 24                        mov     (%esp),%ebx
80485cf: c3                                ret
...
80485d0 <get_fstring>:
80485d0: 8b 44 24 04                    mov     0x4(%esp),%eax
80485d4: 83 f8 00                        cmp     $0x0,%eax
80485d7: 74 14                           je      80485da <after>
80485d9: b8 a9 85 04 08                mov     $0x80485a9,%eax
80485da: c3                                ret
80485de: 6d                                insl    (%dx),%eax:(%edi)
80485df: 6f                                outsl   (%eax),(%dx)
80485e1: 64 65 3a 20                    fs cmp  %fs:%gs:(%eax),%ah
...
80485f4 <model>:
...
80485fa: c7 44 24 0c a0 85 04          movl    $0x80485a0,0xc(%esp)
8048601: 08                                <...>
8048602: c7 44 24 08 04 00 00          movl    $0x4,0x8(%esp)
8048609: 00                                <...>
804860a: c7 44 24 04 05 00 00          movl    $0x5,0x4(%esp)
8048611: 00                                <...>
8048612: c7 04 24 2a a0 04 08          movl    $0x804a024,(%esp)
8048619: a8 12 fa ff ff                call    8048430 <qsort@plt>
...
804864c <main>:
...
8048678: a8 73 fd ff ff                call    80483f0 <printf@plt>
804867d: 8b 44 24 1c                    mov     0x1c(%esp),%eax
8048681: 8b 04 85 3c a0 04 08          mov     0x804a03c(,%eax,4),%eax
8048688: ff d0                          call    *%eax
...

```

(d) Partial binary code of **sort**

A Running Example

```
zlin@zlin-desktop:~/rewriting-example$ ls -l
total 108
-rw-rw-r-- 1 zlin zlin  436 Apr 29  2016 cmp.c
-rw-rw-r-- 1 zlin zlin 1992 Apr 29  2016 cmp.o
-rw-rw-r-- 1 zlin zlin  219 Apr 29  2016 fstring.asm
-rw-rw-r-- 1 zlin zlin  576 Apr 29  2016 fstring.o
-rwxrwxr-x 1 zlin zlin 7682 Apr 29  2016 sort
-rw-rw-r-- 1 zlin zlin 17172 Apr 29  2016 sort.asm
-rw-rw-r-- 1 zlin zlin  443 Apr 29  2016 sort.c
-rw-rw-r-- 1 zlin zlin 5624 Apr 29  2016 ssort
-rw-rw-r-- 1 zlin zlin 15042 Apr 29  2016 ssort.asm
```

The above code can be downloaded at <http://http://web.cse.ohio-state.edu/~lin.3021/file/rewriting-example.zip>

Challenge (C)1: Recognizing and relocating static addresses

```
1 // gcc -m32 -o sort cmp.o fstring.o sort.c
2 #include <stdio.h>
3 #include <unistd.h>
4
5 extern char *array[6];
6 int gt(void *, void *);
7 int lt(void *, void *);
8 char* get_fstring(int select);
9
10 void model(void){
11     qsort(array, 5, sizeof(char*), gt);
12 }
13 void mode2(void){
14     qsort(array, 5, sizeof(char*), lt);
15 }
16
17 void (*modes[2])() = {model, mode2};
18
19 void main(void){
20     int p = getpid() & 1;
21     printf(get_fstring(0),p);
22     (*modes[p])();
23     print_array();
24 }
```

C4

C4

C1

C2

(a) Source code of `sort.c`

Challenge (C)1: Recognizing and relocating static addresses

Hex dump of section '.data':

```
0x0804a01c 00000000 00000000 70870408 74870408 .....p...t...
0x0804a02c 78870408 7d870408 81870408 00000000 x...} .....
0x0804a03c f4850408 20860408 ..... ..
```

C1

(f) Hexdump of .data section

C2: Handling dynamically computed memory addresses

```
1 // gcc -m32 -o sort cmp.o fstring.o sort.c
2 #include <stdio.h>
3 #include <unistd.h>
4
5 extern char *array[6];
6 int gt(void *, void *);
7 int lt(void *, void *);
8 char* get_fstring(int select);
9
10 void model(void){
11     qsort(array, 5, sizeof(char*), gt);
12 }
13 void mode2(void){
14     qsort(array, 5, sizeof(char*), lt);
15 }
16
17 void (*modes[2])() = {model, mode2};
18
19 void main(void){
20     int p = getpid() & 1;
21     printf(get_fstring(0),p);
22     (*modes[p])();
23     print_array();
24 }
```

C4

C4

C1

C2

(a) Source code of `sort.c`

C2: Handling dynamically computed memory addresses

```
804864c <main>:
...
8048678: e8 73 fd ff ff      call    80483f0 <printf@plt>
804867d: 8b 44 24 1c         mov     0x1c(%esp),%eax
8048681: 8b 04 85 3c a0 04 08 mov     0x804a03c(,%eax,4),%eax
8048688: ff d0              call    *%eax
...
```

C2

(d) Partial binary code of sort

C3: Differentiating code and data

```
1 ;nasm -f elf fstring.asm
2 BITS 32
3 GLOBAL get_fstring
4 SECTION .text
5 get_fstring:
6     mov eax,[esp+4]
7     cmp eax,0
8     jz after
9     mov eax,msg2
10    ret
11 msg1:
12     db 'mode: %d', 10, 0
13 msg2:
14     db '%s', 10, 0
15 after:
16     mov eax,msg1
17     ret
```

C3

C3

(b) Source code of `fstring.asm`

C3: Differentiating code and data

```
80485d0 <get_fstring>:  
80485d0: 8b 44 24 04      mov     0x4(%esp),%eax  
80485d4: 83 f8 00         cmp     $0x0,%eax  
80485d7: 74 14           je      80485ed <after>  
80485d9: b8 e9 85 04 08   mov     $0x80485e9,%eax  
80485de: c3              ret  
80485df: 6d              insl    (%dx),%es:(%edi)  
80485e0: 6f              outsl   %ds:(%esi),(%dx)  
80485e1: 64 65 3a 20     fs cmp %fs:%gs:(%eax),%ah  
...
```

C3

(d) Partial binary code of sort

More Real World Examples

```
$ ls *.so*
libavcodec.so.57  libcrypto.so.1.0.0  libffi.so.6
libfreeblpriv3.so
$ ls *.asm
avodec.asm  crypto.asm  ffi.asm  free.asm
$ cat crypto.asm |grep -C3 bad
    f3ae5:      00 00                add     %al, (%rax)
    f3ae7:      00 ff                add     %bh, %bh
    f3ae9:      ff                    (bad)
    f3aea:      ff                    (bad)
    f3aeb:      ff                    (bad)
    f3aec:      fb                    sti
    f3aed:      ff                    (bad)
$ cat *.asm |grep bad|wc
5113    27640   255015
```

C4: Handling function pointer arguments (e.g., callbacks)

```
1 // gcc -m32 -o sort cmp.o fstring.o sort.c
2 #include <stdio.h>
3 #include <unistd.h>
4
5 extern char *array[6];
6 int gt(void *, void *);
7 int lt(void *, void *);
8 char* get_fstring(int select);
9
10 void model(void){
11     qsort(array, 5, sizeof(char*), gt);
12 }
13 void mode2(void){
14     qsort(array, 5, sizeof(char*), lt);
15 }
16
17 void (*modes[2])() = {model, mode2};
18
19 void main(void){
20     int p = getpid() & 1;
21     printf(get_fstring(0),p);
22     (*modes[p])();
23     print_array();
24 }
```

C4

C4

C1

C2

(a) Source code of `sort.c`

C4: Handling function pointer arguments (e.g., callbacks)

```
...  
80485a0 <gt;:  
80485a0: 53                                push    %ebx  
...  
80485f4 <model>:  
...  
80485fa: c7 44 24 0c a0 85 04  movl    $0x80485a0,0xc(%esp)  
8048601: 08  
8048602: c7 44 24 08 04 00 00  movl    $0x4,0x8(%esp)  
8048609: 00  
804860a: c7 44 24 04 05 00 00  movl    $0x5,0x4(%esp)  
8048611: 00  
8048612: c7 04 24 24 a0 04 08  movl    $0x804a024, (%esp)  
8048619: e8 12 fe ff ff          call    8048430 <qsort@plt>  
...
```

C4

(d) Partial binary code of sort

C5: Handling PIC

```
1 // gcc -m32 -c -o cmp.o cmp.c -fPIC -O2
2 #include <stdio.h>
3 #include <stdlib.h>
4 #include <string.h>
5
6 char *array[6] = {"foo", "bar", "quuz", "baz", "flux"};
7 char* get_fstring(int select);
8
9 void print_array() {
10     int i;
11     for (i = 0; i < 5; i++){
12         fprintf(stdout, get_fstring(1), array[i]);
13     }
14 }
15 int lt(void *a, void *b){
16     return strcmp(*(char **) a, *(char **)b);
17 }
18
19 int gt(void *a, void *b){
20     return strcmp(*(char **) b, *(char **)a);
21 }
```

C1

C5

(c) Source code of `cmp.c`

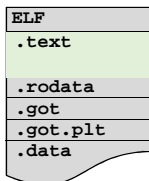
C5: Handling PIC

```
8048510 <print_array>:
...
8048515: 53                push    %ebx
8048516: e8 b1 00 00 00    call    80485cc <__i686.get_pc_thunk.bx>
804851b: 81 c3 d9 1a 00 00 add     $0x1ad9,%ebx
8048521: 83 ec 1c          sub     $0x1c,%esp
8048524: 8b ab fc ff ff ff mov     -0x4(%ebx),%ebp
...
80485a0 <gt;:
80485a0: 53                push    %ebx
...
80485cc <__i686.get_pc_thunk.bx>:
80485cc: 8b 1c 24          mov     (%esp),%ebx
80485cf: c3                ret
```

(d) Partial binary code of sort

Overview of MULTIVERSE

Original Executable,
Shared Library



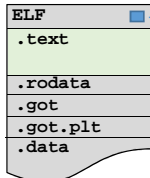
Mapping Phase

Brute-Force
Disassembler

Instruction
Rewriter

Rewriting Phase

New Executable,
Shared Library



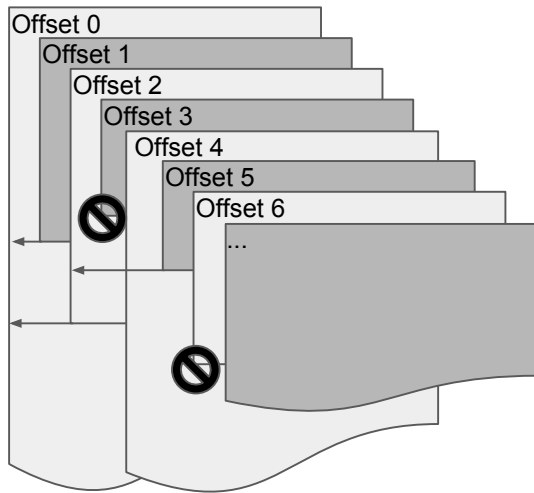
.newtext

.localmapping

Superset Disassembler

*“When in doubt, use brute force.” –
Ken Thompson*

Superset Disassembler



Implementations (with Python)

- ▶ Disassembler engine: the python bindings for CAPSTONE [cap]
- ▶ Parse the ELF data structures: pyelftools [pye]
- ▶ Reassemble the instructions: pwntools [pwn]
- ▶ Additional 3,000 lines of our own python code to implement our algorithm and maintain our data structures
- ▶ ...

Optimizations

- ▶ Lack of assumptions increases overhead
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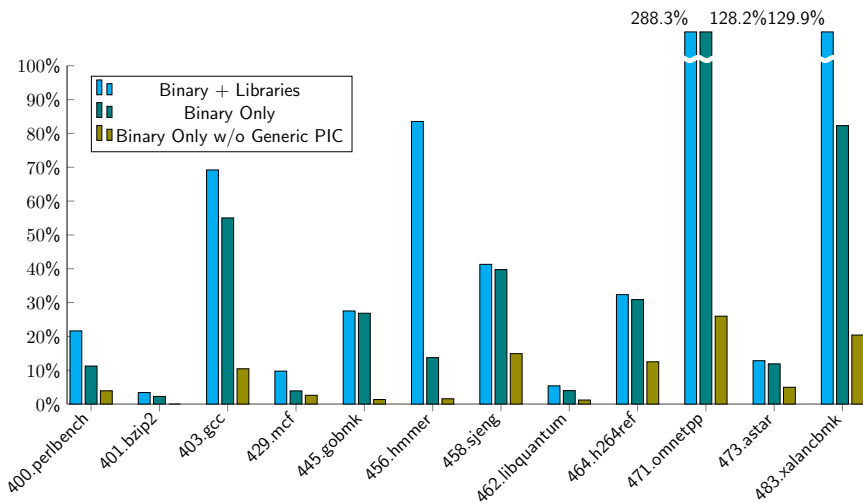
Optimization 2: No Generic PIC

- ▶ Assume only PIC is via `get_pc_thunk`
- ▶ True for many binaries
- ▶ Significant performance increase for compatible binaries

Statistics of our rewritten binaries and libraries

Benchmark	Dir. Calls	Dir. Jumps	Ind. Calls	Ind. Jumps	Cond. Jumps	Rets	.text (KB)	.newtext (KB)	Size Inc. (X)
400.perlbench	30888	24778	3896	4442	126876	22306	1047	5146	12.88
401.bzip2	1100	1050	170	152	7342	874	55	268	70.71
403.gcc	110122	64532	8916	15680	380920	45410	3225	15290	10.32
429.mcf	276	216	44	78	1300	250	12	57	202.98
445.gobmk	23548	14946	3550	3480	117378	20918	1488	6520	5.39
456.hmmer	8020	4942	556	666	28924	4106	277	1279	22.56
458.sjeng	2566	2338	256	658	12236	1570	132	604	36.17
462.libquantum	1094	758	94	146	3376	812	40	181	93.73
464.h264ref	7124	6518	1782	2000	47850	6318	520	2441	16.23
471.omnetpp	33578	10032	3830	1782	51642	14326	635	3029	13.49
473.astar	912	552	162	160	3314	750	39	184	92.52
483.xalancbmk	115154	58678	39392	14630	307122	75674	3850	17369	7.60
libc.so.6	32798	33370	9816	9012	189384	32458	1735	8435	9.77
libgcc_s.so.1	2158	2514	374	484	12862	1740	112	538	9.70
libm.so.6	5450	8870	874	892	21796	7406	277	1268	9.51
libstdc++.so.6	22456	10418	4300	4008	144516	15784	900	4258	9.53

MULTIVERSE Overhead: No Instrumentation



Instrumentation Evaluation

Instruction Counting

- ▶ Ultimate purpose of a rewriter is to insert instrumentation code

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Instrumentation Evaluation

Instruction Counting

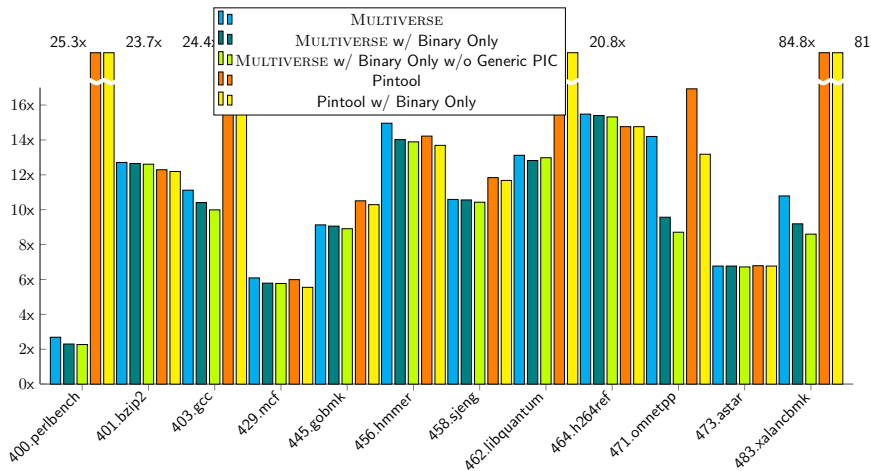
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Instrumentation Evaluation

Instruction Counting

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- ▶ Created straightforward instrumentation API
- ▶ For evaluation created instruction counting instrumentation in MULTIVERSE
- ▶ Compared with instruction counting Pintools

Instrumentation Overhead



Related Work

Systems	Year	w/o Relocation (Debugging) Symbols for Static Address											
		w/o	w/o	w/o	w/o	w/o	w/o	w/o	Heuristics for PIC	Heuristics for Callbacks	Instrumentation	Profiling	Binary Code Hardening
ETCH [RVL+97]	1997	✓	✓	×	×	×	×	✓	✓	✓	✓	✓	✓
SASI [ES99]	1999	×	×	✓	✓	✓	✓	×	×	×	✓	×	×
PLTO [SDAL01]	2001	×	×	✓	✓	✓	✓	✓	✓	✓	×	×	×
VULCAN [SEV01]	2001	✓	×	✓	✓	✓	✓	✓	✓	✓	×	×	×
DIABLO [VPCDB+05]	2005	×	×	✓	✓	✓	✓	✓	✓	✓	×	×	×
CFI [ABEL09]	2005	✓	×	✓	✓	✓	✓	×	×	×	✓	✓	×
XFI [EVA+06]	2006	✓	×	✓	✓	✓	✓	×	×	×	✓	×	×
PITTSFIELD [MM06]	2006	×	×	✓	✓	✓	✓	×	×	×	✓	×	×
BIRD [NLLC06]	2006	✓	✓	×	✓	✓	×	✓	✓	✓	✓	×	×
NACL [YSD+09]	2009	×	×	✓	✓	✓	✓	×	×	×	✓	×	×
PEBIL [LTCS10]	2010	×	×	✓	✓	✓	✓	✓	✓	✓	✓	×	×
SECONDWRITE [OAK+11]	2011	✓	✓	✓	×	×	×	✓	✓	✓	✓	×	×
DYNINST [BM11]	2011	✓	✓	×	×	✓	×	✓	✓	✓	✓	✓	×
STIR/REINS [WMHL12a, WMHL12b]	2012	✓	✓	✓	×	×	✓	×	×	×	✓	✓	×
CCFIR [ZWC+13]	2013	×	✓	✓	✓	×	×	×	×	×	✓	✓	×
BISTRO [DZX13]	2013	✓	✓	✓	×	×	×	×	×	×	✓	×	✓
BINCFI [ZS13]	2013	✓	✓	✓	✓	✓	×	×	×	×	✓	✓	×
PSI [ZQHS14]	2014	✓	✓	✓	✓	✓	×	×	×	✓	✓	✓	×
UROBOROS [WWW16]	2016	✓	✓	×	×	×	×	✓	✓	✓	✓	✓	✓
RAMBLR [WSB+17]	2017	✓	✓	✓	×	×	×	✓	✓	✓	✓	✓	✓
MULTIVERSE [BLH18]	2018	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓	✓

Limitations and Future Work

x86-64 Support

- ▶ Paper only covers 32-bit support
- ▶ MULTIVERSE now supports 64-bit applications

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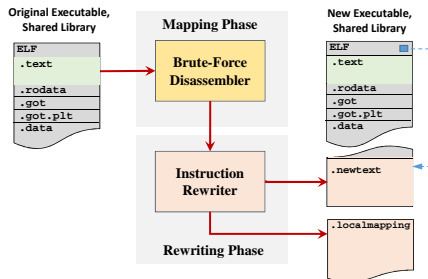
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Instrumentation API

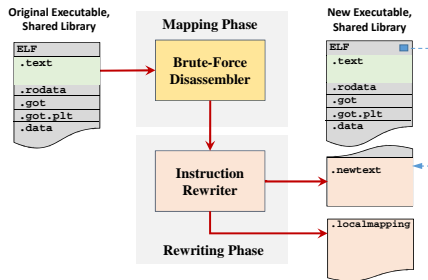
- ▶ For paper, used simple instruction-level API
- ▶ Currently working on more robust API

Conclusion



- MULTIVERSE: **Statically rewriting** x86 binaries w/o heuristics
- Works for x86/64 binaries
- Useful for many security applications (e.g., hardening)

Thank You



Q&A

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MULTIVERSE Source Code

github.com/utds3lab/multiverse

References I



Martín Abadi, Mihai Budiu, Úlfar Erlingsson, and Jay Ligatti, *Control-flow integrity principles, implementations, and applications*, ACM Trans. Information and System Security **13** (2009), no. 1.



Kapil Anand, Matthew Smithson, Khaled Elwazeer, Aparna Kotha, Jim Gruen, Nathan Giles, and Rajeev Barua, *A compiler-level intermediate representation based binary analysis and rewriting system*, Proceedings of the 8th ACM European Conference on Computer Systems, ACM, 2013, pp. 295–308.



Erick Bauman, Zhiqiang Lin, and Kevin Hamlen, *Superset disassembly: Statically rewriting x86 binaries without heuristics*, Proceedings of the 25th Annual Network and Distributed System Security Symposium (NDSS'18) (San Diego, CA), February 2018.



Andrew R Bernat and Barton P Miller, *Anywhere, any-time binary instrumentation*, Proceedings of the 10th ACM SIGPLAN-SIGSOFT workshop on Program analysis for software tools, ACM, 2011, pp. 9–16.



Capstone: The ultimate disassembler, <http://www.capstone-engine.org/>.



Xi Chen, Herbert Bos, and Cristiano Giuffrida, *CodeArmor: Virtualizing the Code Space to Counter Disclosure Attacks*, EuroS&P, April 2017.



Brian Cox and Jeffrey Robert. Forshaw, *The quantum universe: everything that can happen does happen*, Penguin, 2012.



Juan Caballero, Noah M. Johnson, Stephen McCamant, and Dawn Song, *Binary code extraction and interface identification for security applications*, NDSS, Feb. 2010.



Zhui Deng, Xiangyu Zhang, and Dongyan Xu, *Bistro: Binary component extraction and embedding for software security applications*, Computer Security—ESORICS 2013, Springer, 2013, pp. 200–218.

References II



Úlfar Erlingsson and Fred B. Schneider, *SASI enforcement of security policies: A retrospective*, Proc. New Security Paradigms Workshop, 1999.



Úlfar Erlingsson, Silicon Valley, Martín Abadi, Michael Vrable, Mihai Budiu, and George C. Necula, *Xfi: software guards for system address spaces*, Proceedings of the 7th USENIX Symposium on Operating Systems Design and Implementation (OSDI'06) (Seattle, WA), USENIX Association, 2006, pp. 6–6.



Christopher Kruegel, William Robertson, Fredrik Valeur, and Giovanni Vigna, *Static disassembly of obfuscated binaries*, USENIX security Symposium, vol. 13, 2004, pp. 18–18.



Chi-Keung Luk, Robert Cohn, Robert Muth, Harish Patil, Artur Klauser, Geoff Lowney, Steven Wallace, Vijay Janapa Reddi, and Kim Hazelwood, *Pin: Building customized program analysis tools with dynamic instrumentation*, Proceedings of ACM SIGPLAN Conference on Programming Language Design and Implementation (PLDI'05) (Chicago, IL, USA), 2005, pp. 190–200.



Michael A Laurenzano, Mustafa M Tikir, Laura Carrington, and Allan Snaveley, *Pebil: Efficient static binary instrumentation for linux*, Performance Analysis of Systems & Software (ISPASS), 2010 IEEE International Symposium on, IEEE, 2010, pp. 175–183.



Evangelos Ladakis, Giorgos Vasiliadis, Michalis Polychronakis, Sotiris Ioannidis, and Georgios Portokalidis, *Gpu-disasm: A gpu-based x86 disassembler*, International Information Security Conference, Springer, 2015, pp. 472–489.



Stephen McCamant and Greg Morrisett, *Evaluating SFI for a CISC architecture*, Proc. USENIX Security Sym., 2006.

References III



Susanta Nanda, Wei Li, Lap-Chung Lam, and Tzi-cker Chiueh, *Bird: Binary interpretation using runtime disassembly*, Proceedings of the International Symposium on Code Generation and Optimization (Washington, DC, USA), CGO '06, IEEE Computer Society, 2006, pp. 358–370.



Pádraig O'Sullivan, Kapil Anand, Aparna Kotha, Matthew Smithson, Rajeev Barua, and Angelos D. Keromytis, *Retrofitting security in COTS software with binary rewriting*, Proc. Int. Information Security Conf., 2011, pp. 154–172.



Pwntools, <https://github.com/Gallopsled/pwntools>.



Pyelftools, <https://github.com/eliben/pyelftools>.



Ted Romer, Geoff Voelker, Dennis Lee, Alec Wolman, Wayne Wong, Hank Levy, Brian Bershad, and Brad Chen, *Instrumentation and optimization of win32/intel executables using etch*, Proceedings of the USENIX Windows NT Workshop on The USENIX Windows NT Workshop 1997 (Berkeley, CA, USA), NT'97, USENIX Association, 1997, pp. 1–1.



Benjamin Schwarz, Saumya Debray, Gregory Andrews, and Matthew Legendre, *Plto: A link-time optimizer for the intel ia-32 architecture*, Proc. 2001 Workshop on Binary Translation (WBT-2001), 2001.



Amitabh Srivastava, Andrew Edwards, and Hoi Vo, *Vulcan: Binary transformation in a distributed environment*, Tech. report, technical report msr-tr-2001-50, microsoft research, 2001.



Ludo Van Put, Dominique Chagnet, Bruno De Bus, Bjorn De Sutter, and Koen De Bosschere, *Diablo: a reliable, retargetable and extensible link-time rewriting framework*, Signal Processing and Information Technology, 2005. Proceedings of the Fifth IEEE International Symposium on, IEEE, 2005, pp. 7–12.

References IV



Robert Wahbe, Steven Lucco, Thomas E. Anderson, and Susan L. Graham, *Efficient software-based fault isolation*, Proc. ACM Sym. Operating Systems Principles, 1993, pp. 203–216.



Richard Wartell, Vishwath Mohan, Kevin Hamlen, and Zhiqiang Lin, *Binary stirring: Self-randomizing instruction addresses of legacy x86 binary code*, Proceedings of the 19th ACM Conference on Computer and Communications Security (CCS'12) (Raleigh, NC), October 2012.



———, *Securing untrusted code via compiler-agnostic binary rewriting*, Proceedings of the 28th Annual Computer Security Applications Conference (ACSAC'12) (Orlando, FL), December 2012.



Ruoyu Wang, Yan Shoshitaishvili, Antonio Bianchi, Aravind Machiry, John Grosen, Paul Grosen, Christopher Kruegel, and Giovanni Vigna, *Ramblr: Making reassembly great again*.



Shuai Wang, Pei Wang, and Dinghao Wu, *Uroboros: Instrumenting stripped binaries with static reassembling*, Software Analysis, Evolution, and Reengineering (SANER), 2016 IEEE 23rd International Conference on, vol. 1, IEEE, 2016, pp. 236–247.



Richard Wartell, Yan Zhou, Kevin W Hamlen, and Murat Kantarcioglu, *Shingled graph disassembly: Finding the undecidable path*, Pacific-Asia Conference on Knowledge Discovery and Data Mining, Springer, 2014, pp. 273–285.



Bennet Yee, David Sehr, Gregory Dardyk, J. Bradley Chen, Robert Muth, Tavis Ormandy, Shiki Okasaka, Neha Narula, and Nicholas Fullagar, *Native Client: A sandbox for portable, untrusted x86 native code*, Proc. IEEE Sym. Security and Privacy, 2009, pp. 79–93.

References V



Mingwei Zhang, Rui Qiao, Niranjana Hasabnis, and R. Sekar, *A platform for secure static binary instrumentation*, Proceedings of the 10th ACM SIGPLAN/SIGOPS International Conference on Virtual Execution Environments (New York, NY, USA), VEE '14, ACM, 2014, pp. 129–140.



Mingwei Zhang and R. Sekar, *Control flow integrity for cots binaries*, Presented as part of the 22nd USENIX Security Symposium (USENIX Security 13) (Washington, D.C.), USENIX, 2013, pp. 337–352.



Chao Zhang, Tao Wei, Zhaofeng Chen, Lei Duan, Laszlo Szekeres, Stephen McCamant, Dong Song, and Wei Zou, *Practical control flow integrity and randomization for binary executables*, Security and Privacy (SP), 2013 IEEE Symposium on, IEEE, 2013, pp. 559–573.