

STM32F205RCT6																		
64pin QFP package		AF0	AF1	AF2	AF3	AF4	AF5	AF6	AF7	AF8	AF9	AF10	AF11	AF12	AF13	AF014	AF15	
Pin #	Port	SYS	TIM1/2	TIM3/4/5	TIM8/9/10/11	I2C1/I2C2/I2C3	SPI1/SPI2/I2S2	SPI3/I2S3	USART1/2/3	UART4/5/ USART6	CAN1/CAN2/ TIM12/13/14	OTG_FS/OTG_HS	ETH	FSMC/SDIO/OT G_HS	DCMI			
2	PC13																	
3	PC14-OSC32_IN																	
4	PC15-OSC32_OUT																	
8	PC0											OTG_HS_ULPI_STP					EVENTOUT	
9	PC1												ETH_MDC				EVENTOUT	
10	PC2						SPI2_MISO					OTG_HS_ULPI_DIR	ETH_MII_TXD2				EVENTOUT	
11	PC3						SPI2_MOSI					OTG_HS_ULPI_NXT	ETH_MII_TX_CLK				EVENTOUT	
14	PA0-WKUP		TIM2_CH1_ETR	TIM5_CH1	TIM8_ETR				USART2_CTS	UART4_TX			ETH_MII_CR5				EVENTOUT	
15	PA1		TIM2_CH2	TIM5_CH2					USART2_RTS	UART4_RX			ETH_MII_RX_CLK				EVENTOUT	
16	PA2		TIM2_CH3	TIM5_CH3	TIM9_CH1				USART2_TX				ETH_RMII					
17	PA3		TIM2_CH4	TIM5_CH4	TIM9_CH2				USART2_RX			OTG_HS_ULPI_D0	REF_CLK				EVENTOUT	
20	PA4						SPI1_NSS	SPI3_NSS	USART2_CK				ETH_MDIO			OTG_HS_SOF	DCMI_HSYNC	
21	PA5		TIM2_CH1_ETR		TIM8_CH1N		SPI1_SCK	I2S3_WS				OTG_HS_ULPI_CK	ETH_MII_COL				EVENTOUT	
22	PA6		TIM1_BKIN	TIM3_CH1	TIM8_BKIN		SPI1_MISO				TIM13_CH1				DCMI_PIXCK		EVENTOUT	
23	PA7		TIM1_CH1N	TIM3_CH2	TIM8_CH1N		SPI1_MOSI				TIM14_CH1						EVENTOUT	
24	PC4												ETH_RMII				EVENTOUT	
25	PC5												CRS_DV				EVENTOUT	
26	PB0		TIM1_CH2N	TIM3_CH3	TIM8_CH2N							OTG_HS_ULPI_D1	ETH_MII_RXD0				EVENTOUT	
27	PB1		TIM1_CH3N	TIM3_CH4	TIM8_CH3N							OTG_HS_ULPI_D2	ETH_MII_RXD1				EVENTOUT	
28	PB2												ETH_RMII_RXD0				EVENTOUT	
29	PB10		TIM2_CH3			I2C2_SCL	SPI2_SCK		USART3_TX			OTG_HS_ULPI_D3	ETH_MII_RX_ER				EVENTOUT	
30	PB11		TIM2_CH4			I2C2_SDA			USART3_RX			OTG_HS_ULPI_D4	ETH_MII_TX_EN				EVENTOUT	
33	PB12		TIM1_BKIN			I2C2_SMBA	SPI2_NSS		USART3_CK		CAN2_RX	OTG_HS_ULPI_D5	ETH_RMII_TX_EN	OTG_HS_ID			EVENTOUT	
34	PB13		TIM1_CH1N				I2S2_WS		USART3_CTS		CAN2_TX	OTG_HS_ULPI_D6	ETH_MII_TXD0				EVENTOUT	
35	PB14		TIM1_CH2N		TIM8_CH2N		I2S2_SCK		USART3_RTS		TIM12_CH1		ETH_RMII_TXD1	OTG_HS_DM			EVENTOUT	
36	PB15	RTC_50Hz	TIM1_CH3N		TIM8_CH3N		SPI2_MISO				TIM12_CH2			OTG_HS_DP			EVENTOUT	
37	PC6			TIM3_CH1	TIM8_CH1		I2S2_SD							SDIO_D6	DCMI_D0		EVENTOUT	
38	PC7			TIM3_CH2	TIM8_CH2			I2S3_MCK		USART6_TX				SDIO_D7	DCMI_D1		EVENTOUT	
39	PC8			TIM3_CH3	TIM8_CH3					USART6_RX				SDIO_D0	DCMI_D2		EVENTOUT	
40	PC9	MCO2		TIM3_CH4	TIM8_CH4	I2C3_SDA	I2S2_CKIN	I2S3_CKIN						SDIO_D1	DCMI_D3		EVENTOUT	
41	PA8	MCO1	TIM1_CH1			I2C3_SCL			USART1_CK			OTG_FS_SOF					EVENTOUT	
42	PA9		TIM1_CH2			I2C3_SMBA			USART1_TX						DCMI_D0		EVENTOUT	
43	PA10		TIM1_CH3						USART1_RX			OTG_FS_ID			DCMI_D1		EVENTOUT	
44	PA11		TIM1_CH4						USART1_CTS		CAN1_RX	OTG_FS_DM					EVENTOUT	
45	PA12		TIM1_ETR						USART1_RTS		CAN1_TX	OTG_FS_DP					EVENTOUT	
46	PA13	JTMS-SWDIO															EVENTOUT	
49	PA14	JTCK-SWCLK															EVENTOUT	
50	PA15	JTDI	TIM2_CH1				SPI1_NSS	SPI3_NSS									EVENTOUT	
			TIM2_ETR					I2S3_WS										
51	PC10							SPI3_SCK	USART3_TX	UART4_TX				SDIO_D2	DCMI_D8		EVENTOUT	
52	PC11							I2S3_SCK	USART3_RX	UART4_RX				SDIO_D3	DCMI_D4		EVENTOUT	
53	PC12							SPI3_MISO	USART3_CK	UART5_TX				SDIO_CK	DCMI_D9		EVENTOUT	
54	PD2							SPI3_MOSI		UART5_RX								
55	PB3	JTDO/ TRACESWO	TIM2_CH2	TIM3_ETR				I2S3_SD						SDIO_CMD	DCMI_D11		EVENTOUT	
56	PB4	JTRST					SPI1_SCK	SPI3_SCK									EVENTOUT	
57	PB5			TIM3_CH1			SPI1_MISO	I2S3_SCK									EVENTOUT	
58	PB6			TIM3_CH2		I2C1_SMBA	SPI1_MOSI	SPI3_MISO			CAN2_RX	OTG_HS_ULPI_D7	ETH_PPS_OUT		DCMI_D10		EVENTOUT	
59	PB7			TIM4_CH1		I2C1_SCL		I2S3_SD	USART1_TX		CAN2_TX			FSMC_NL	DCMI_VSYNC		EVENTOUT	
61	PB8			TIM4_CH2	TIM10_CH1	I2C1_SDA			USART1_RX					SDIO_D4	DCMI_D6		EVENTOUT	
62	PB9			TIM4_CH3			SPI2_NSS				CAN1_RX		ETH_MII_TXD3	SDIO_D5	DCMI_D7		EVENTOUT	
				TIM4_CH4	TIM11_CH1	I2C1_SDA	I2S2_WS				CAN1_TX						EVENTOUT	
	PD0										CAN1_RX			FSMC_D2			EVENTOUT	
	PD1										CAN1_TX			FSMC_D3			EVENTOUT	