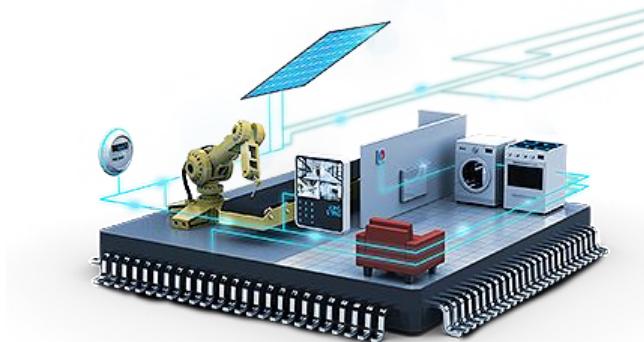


Giris



Suhap SAHIN

Kaynaklar

<http://users.ece.utexas.edu/~valvano/Volume1/E-Book/>

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This material is being developed for an online class that is running January 2016 to May 2016 on the EdX platform. This website is meant to supplement not replace the content on edX. This site has been our sandbox where we first built the information before uploading to edX. If you need closed captions, please use the edX site because the captions on edX have been reviewed and edited by Valvano and Yerraballi. When viewing the videos on YouTube you can activate their closed captioning, but these captions have not been reviewed or edited. However, many students have asked for two things: 1) a more linear or book-like resource of the class material; and 2) a list of the video links. This website provides both. All videos are hosted in two places: YouTube and Amazon S3. It is our plan to make the edX pages as accurate as possible and will strive to make corrections as we can. Again, this website contains the material prior to uploading to edX and hence may be more inaccurate. Knowing that however, it is our goal to reach as many people as possible and we hope this site makes the class more accessible for those having technical issues reading and watching the material on edX. All quizzes and labs must be performed on the edX site.

<https://www.edx.org/course/embedded-systems-shape-world-utaustinxt-ut-6-03x>



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Home > All Subjects > Computer Science > Embedded Systems - Shape The World: Microcontroller Input/Output



Embedded Systems - Shape The World: Microcontroller Input/Output

Introduction to the world of embedded systems with a focus on microcontroller input/output in this hands-on, lab-based course.

 **TEXAS**
The University of Texas at Austin

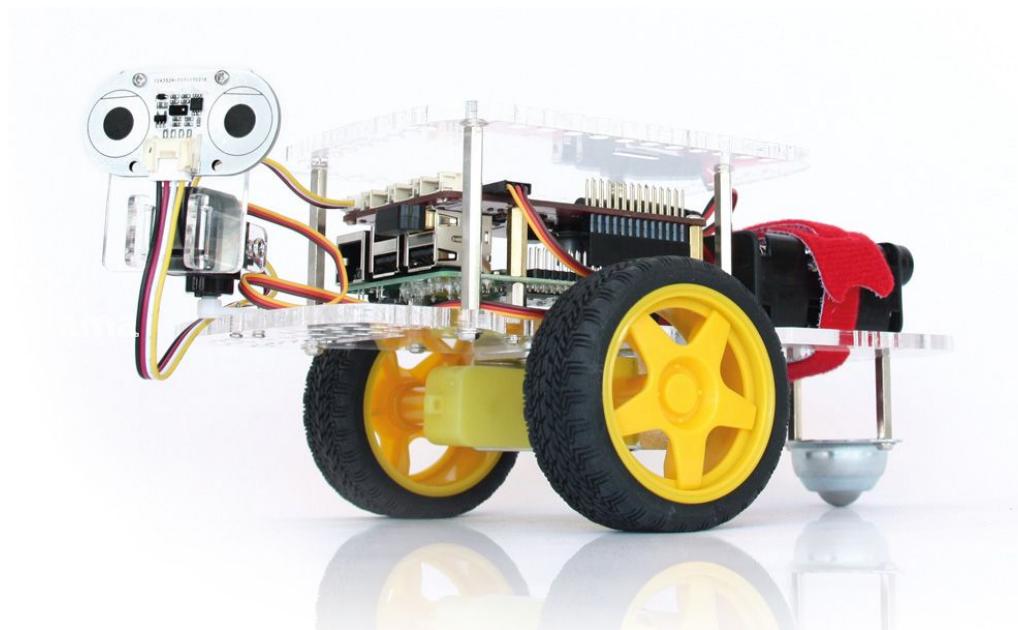
Archived
Future Dates To Be Announced

Enroll Now

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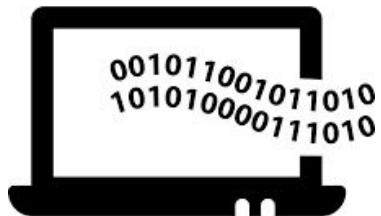
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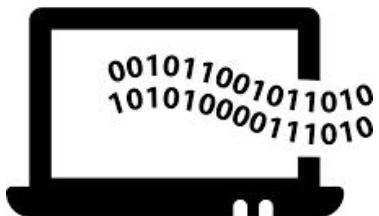
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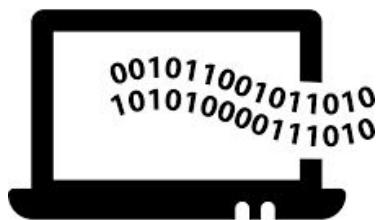
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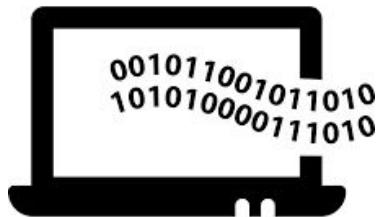
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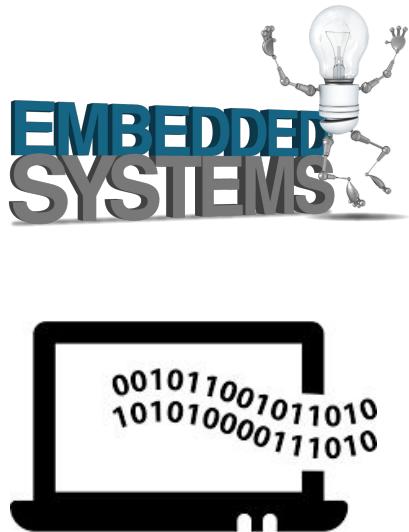
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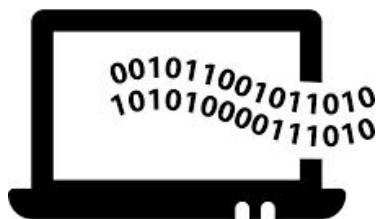
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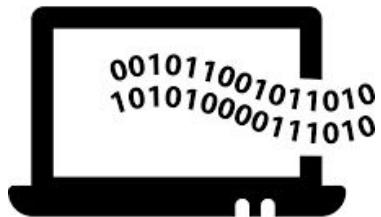
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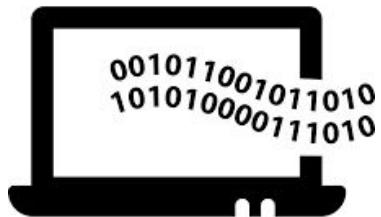
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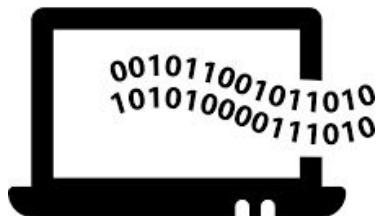
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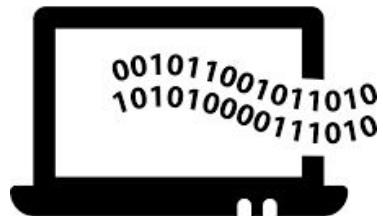
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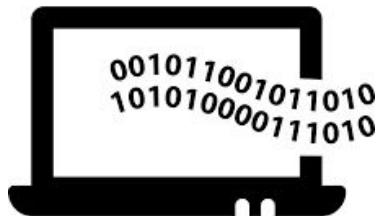
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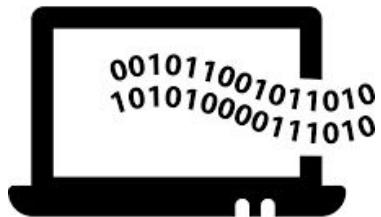
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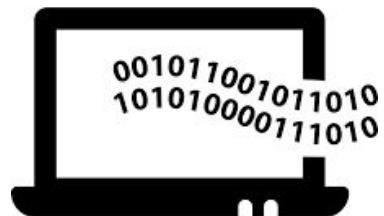
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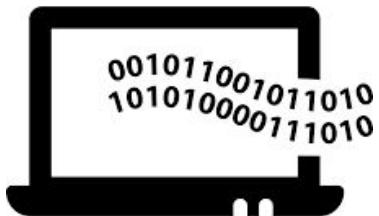
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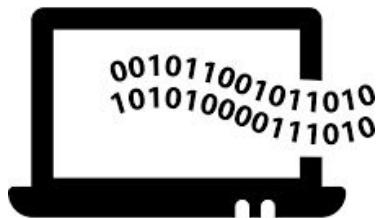
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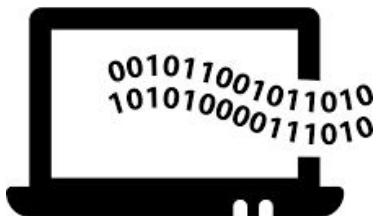
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2. Temel Kavramlar



Konular

1. Giriş
2. Temel Kavramlar



Konular

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2. Temel Kavamlar
3. Elektronik

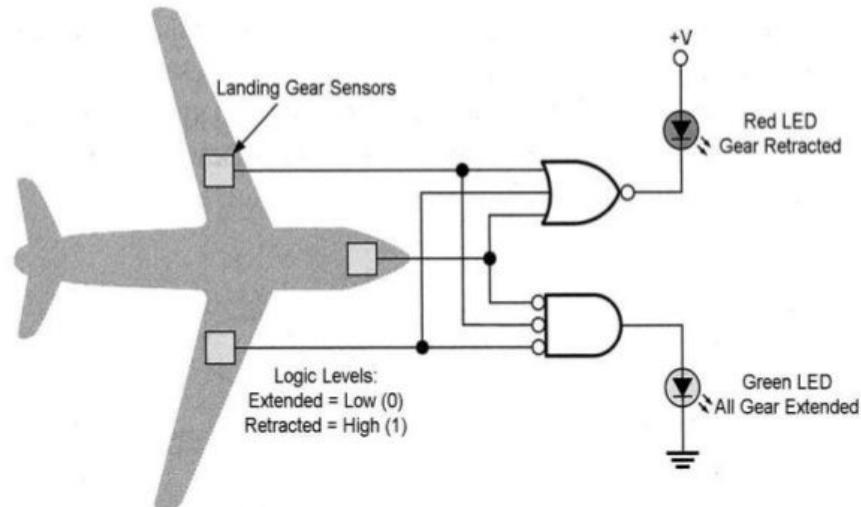


Konular

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2. Temel Kavramlar
3. Elektronik
4. Lojik

AIRCRAFT LOGIC CIRCUIT

- LANDING GEAR WARNING CIRCUIT



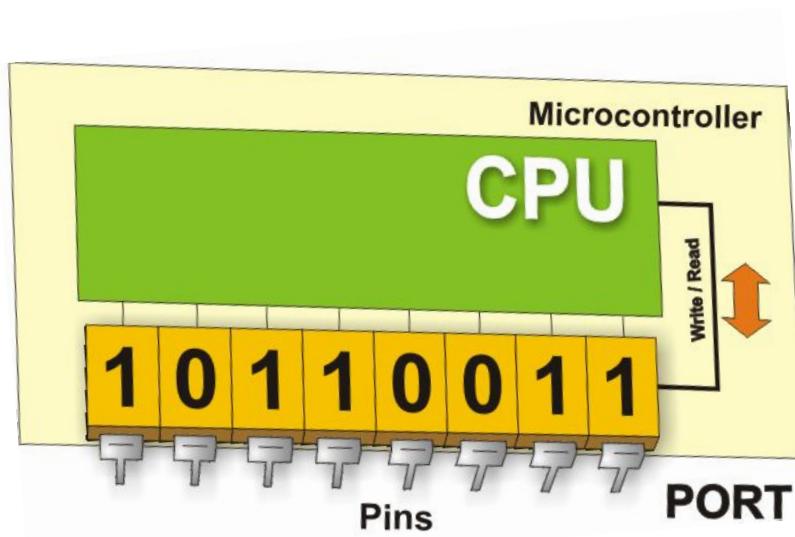
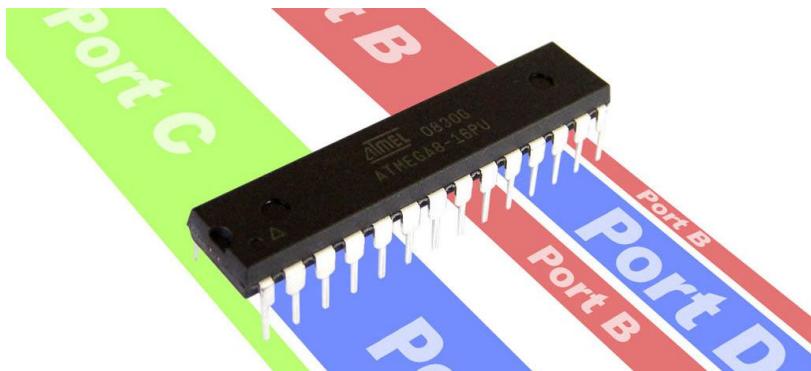
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4. Lojik
5. C Diline Giriş



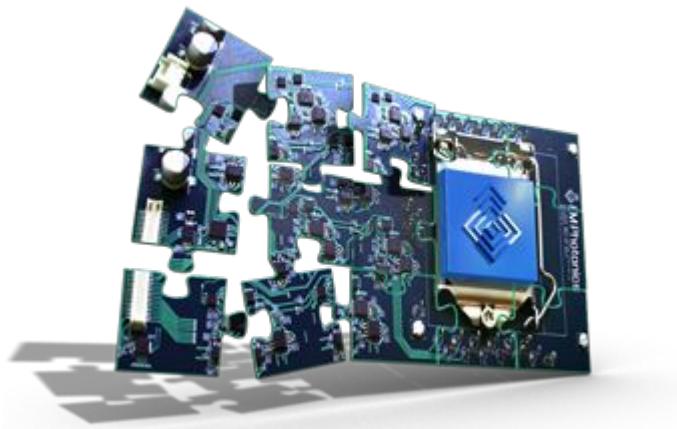
Konular

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2. Temel Kavramlar
3. Elektronik
4. Lojik
5. C Diline Giriş
6. Mikrokontrolcü portları



Konular

1. Giriş
2. Temel Kavramlar
3. Elektronik
4. Lojik
5. C Diline Giriş
6. Mikrokontrolcü portları
7. Tasarım ve Geliştirme



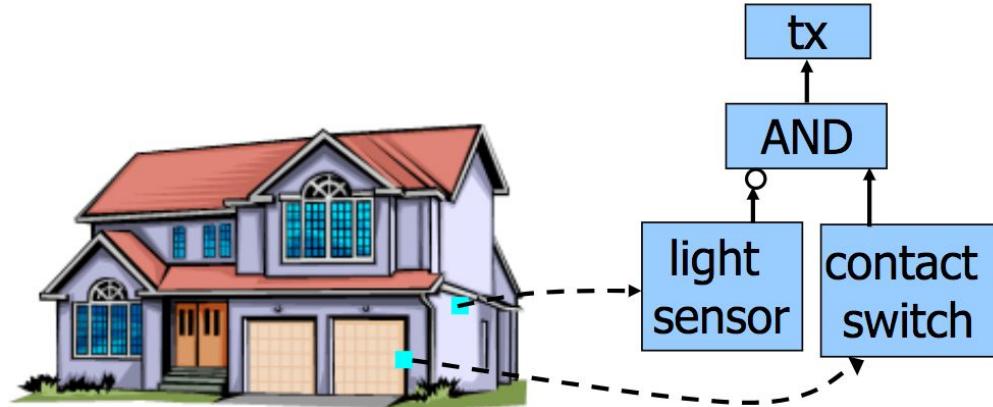
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4. Lojik
5. C Diline Giriş
6. Mikrokontrolcü portları
7. Tasarım ve Geliştirme
8. Led ve Anahtarlar



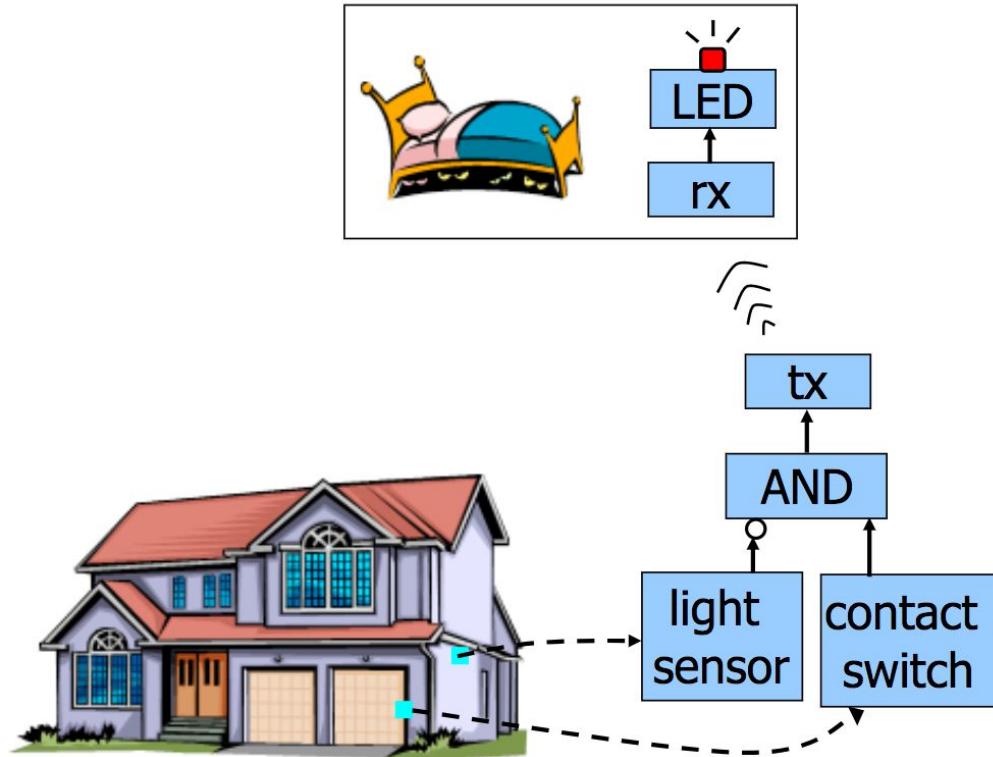
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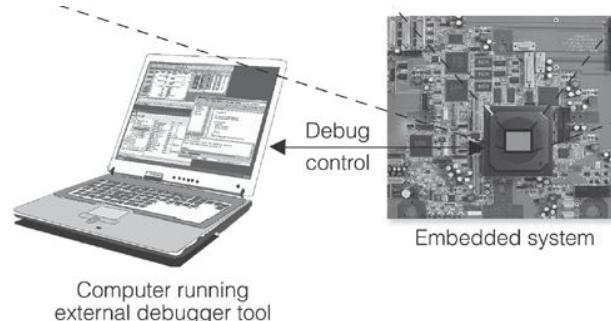
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8. Led ve Anahtarlar
9. Diziler ve Fonksiyonel Hata Ayıklama



Computer running
external debugger tool

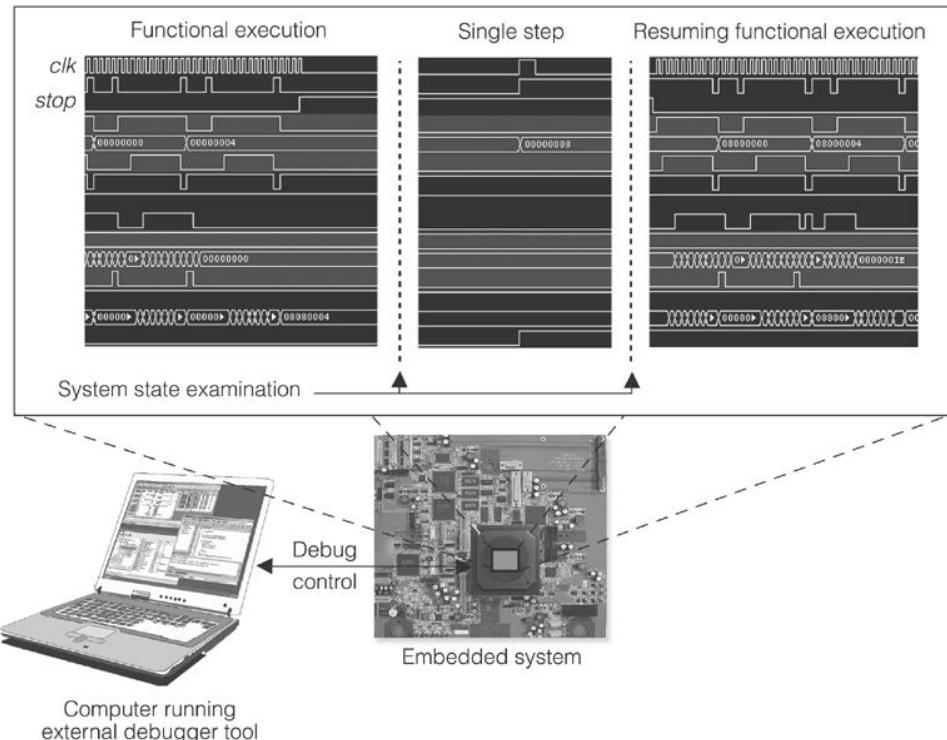
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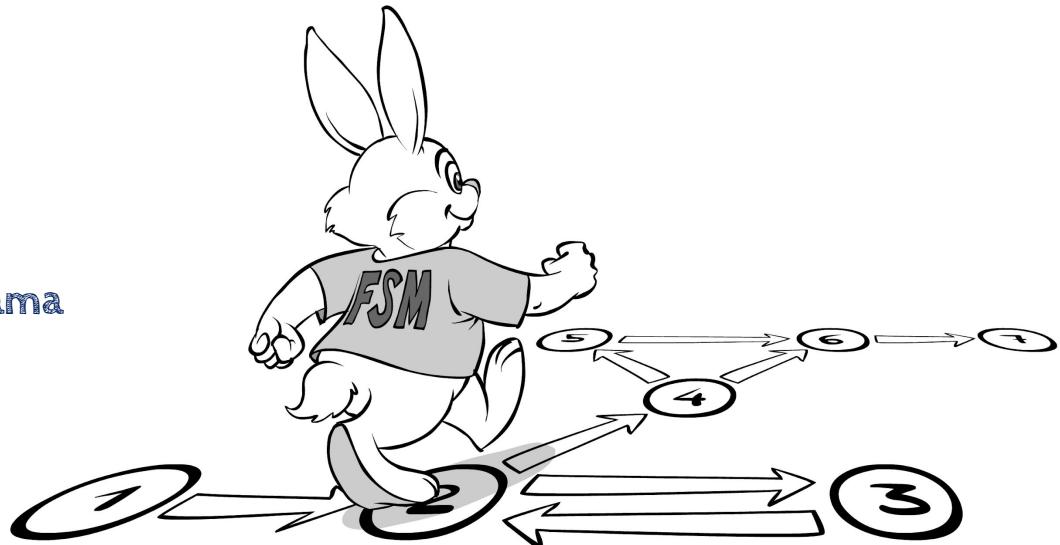
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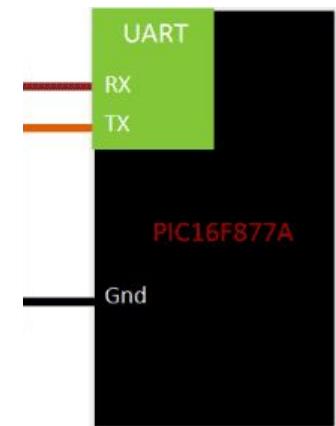
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9. Diziler ve Fonksiyonel Hata Ayıklama
10. Sonlu Durum Makineleri



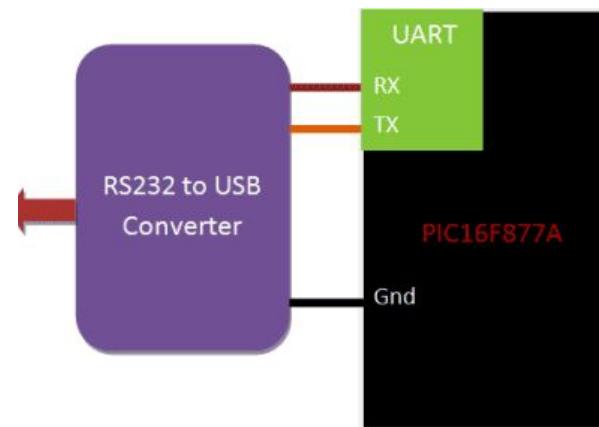
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10. Sonlu Durum Makineleri
11. UART Seri port



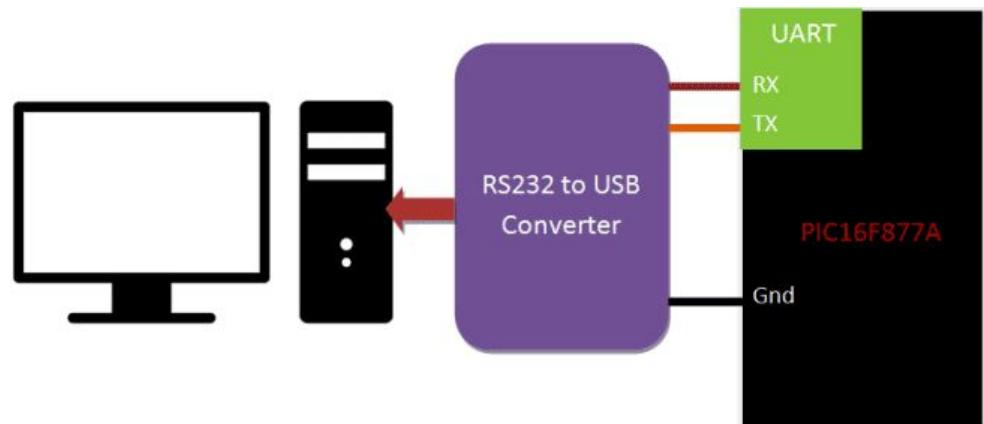
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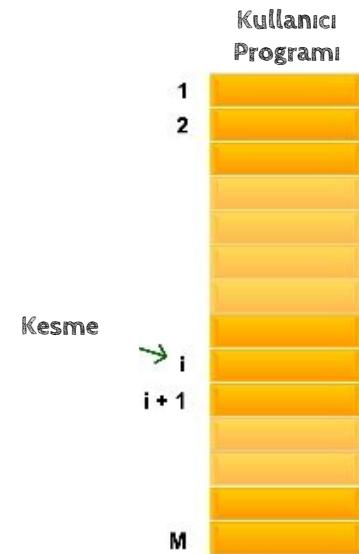
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12. Kesmeler



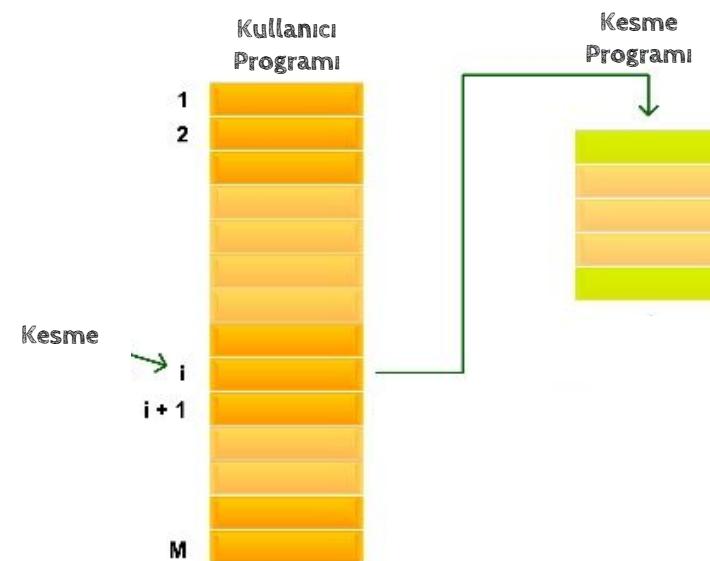
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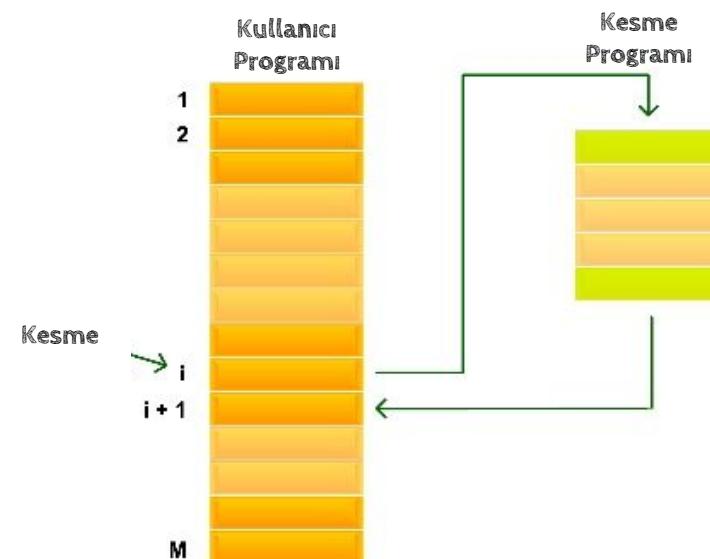
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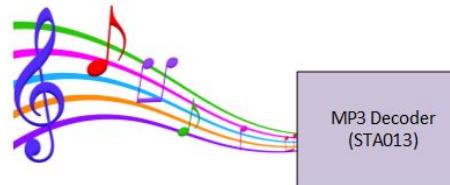
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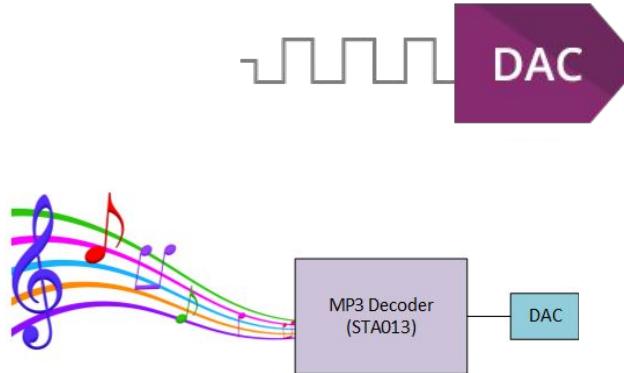
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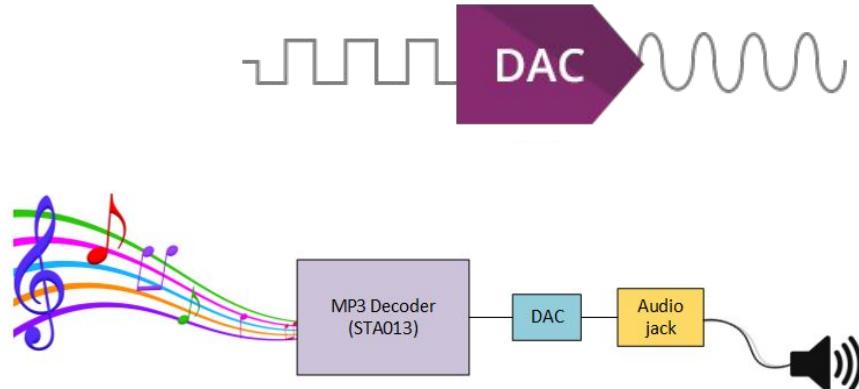
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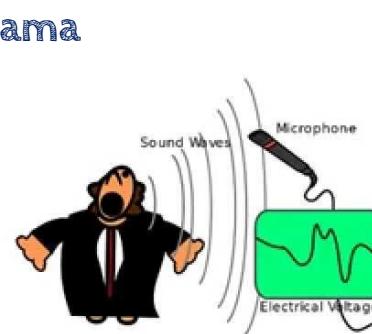
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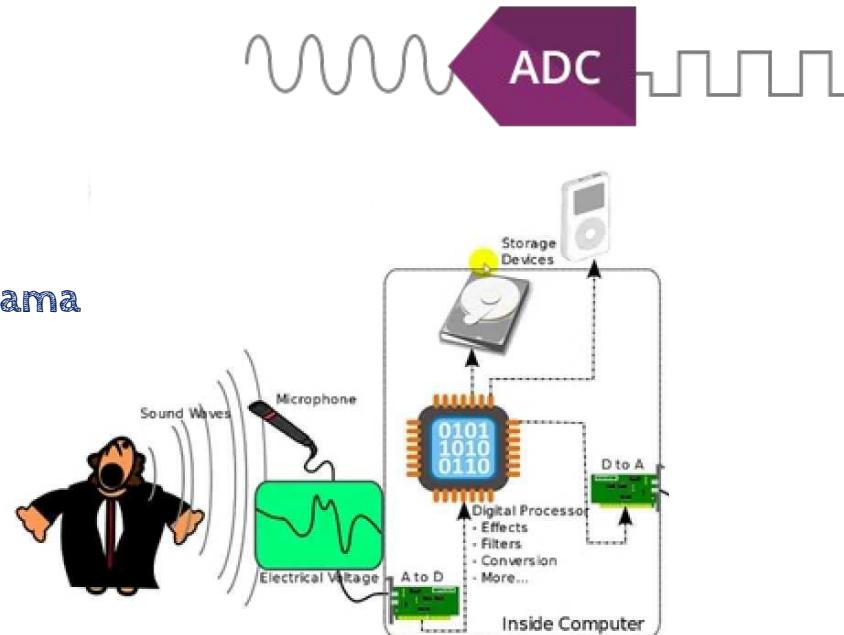
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14. ADC ve Veri Alma



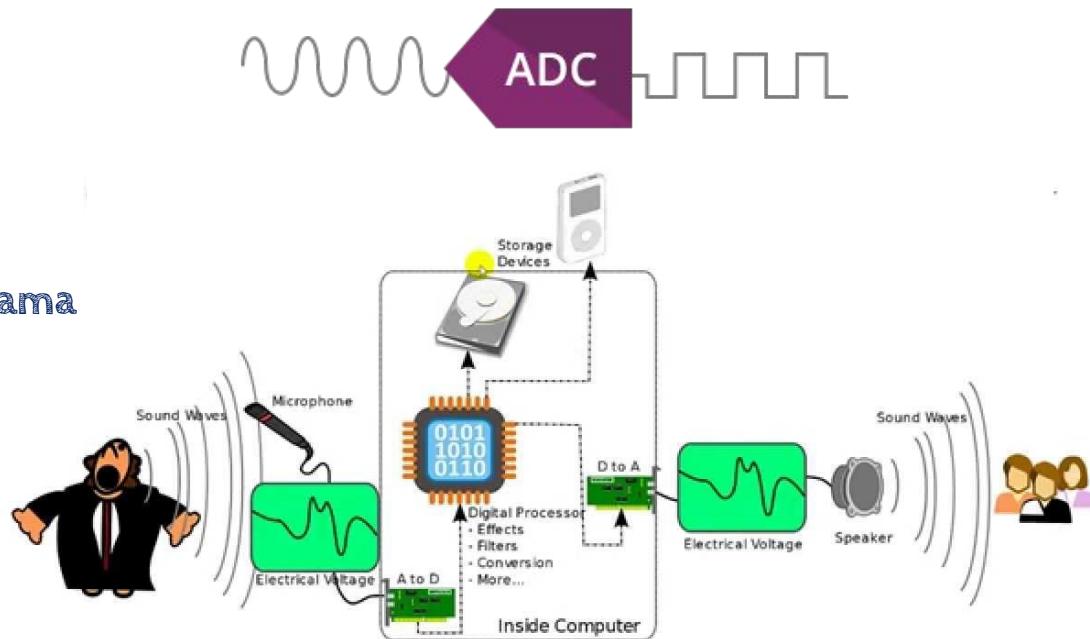
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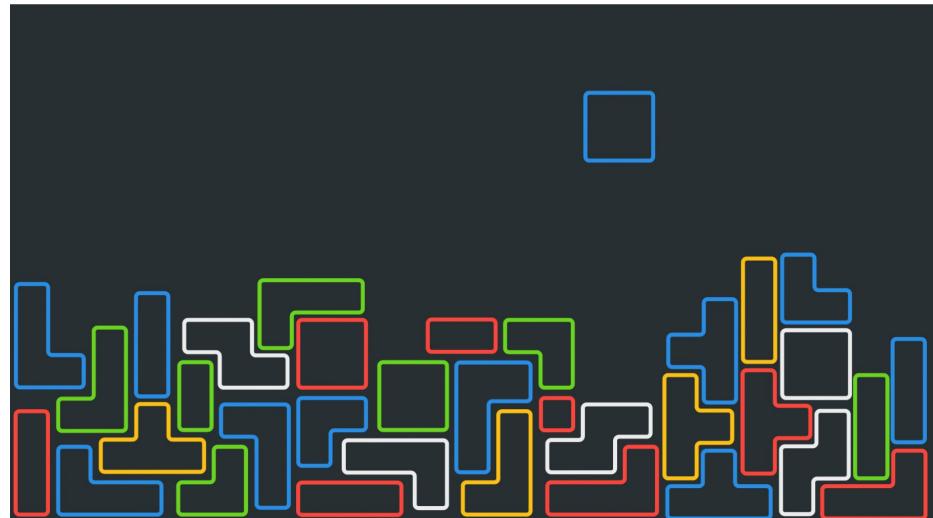
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15. Sistem Yaklaşımı ve Oyun Tasarımı



Konular

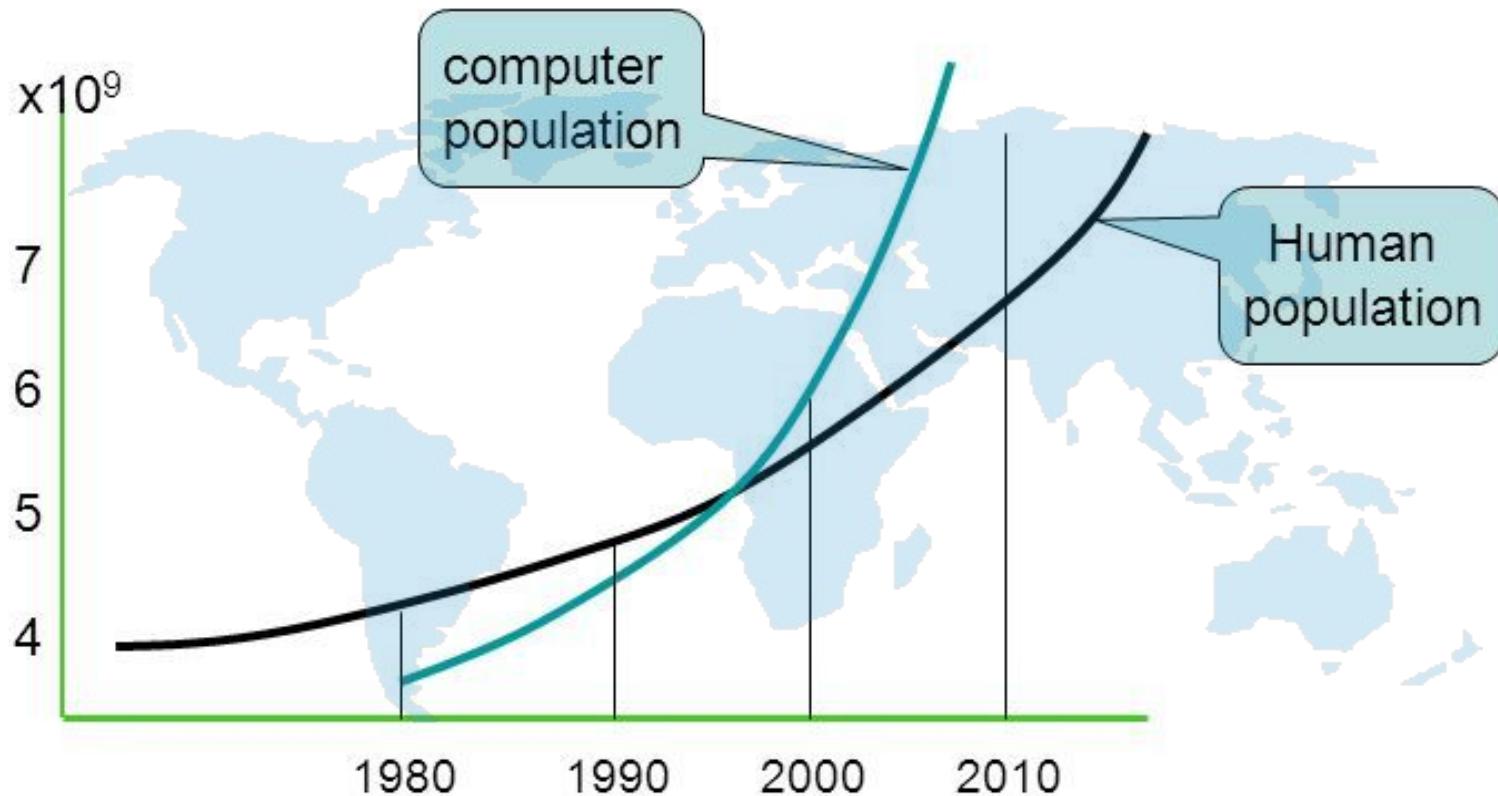
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16. Nesneleri interneti



Gömülü Sistemler



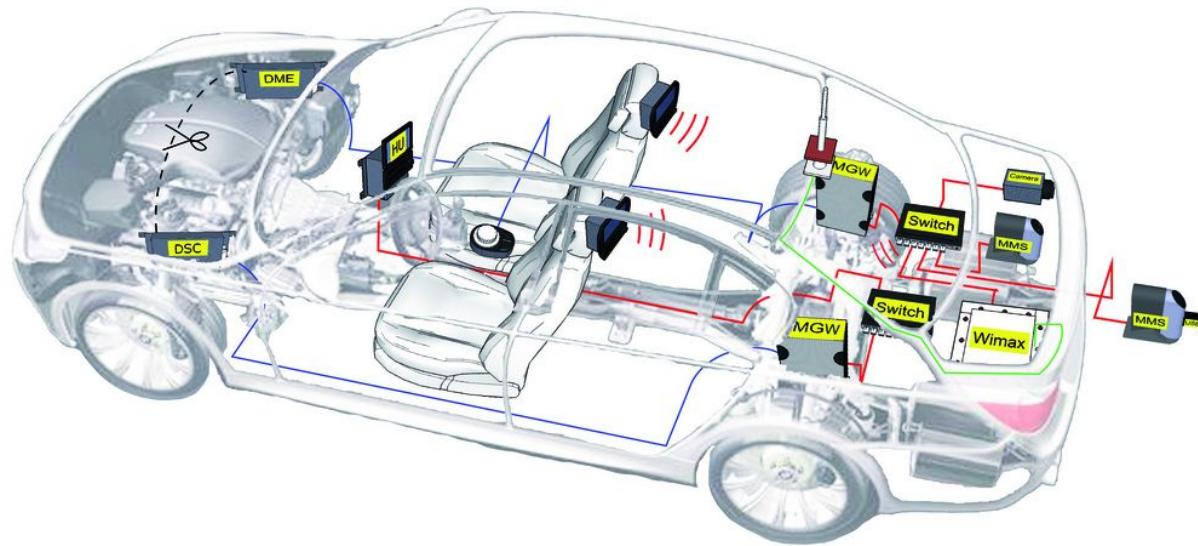
Gömülü Sistemler



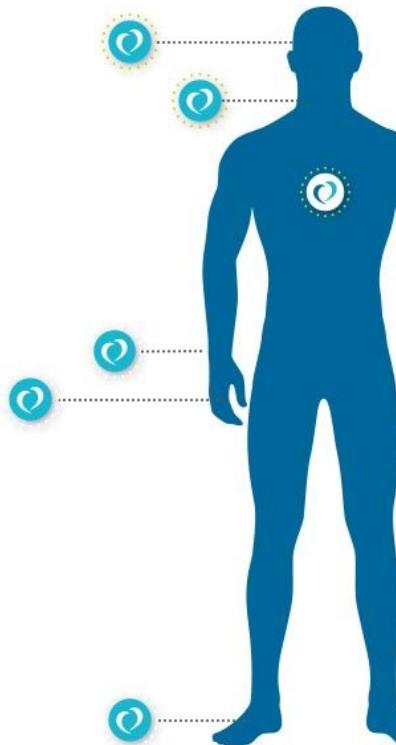
Gömülü Sistemler



Gömülü Sistemler



Gömülü Sistemler



Activity Trackers



Smart watches



Smartphones

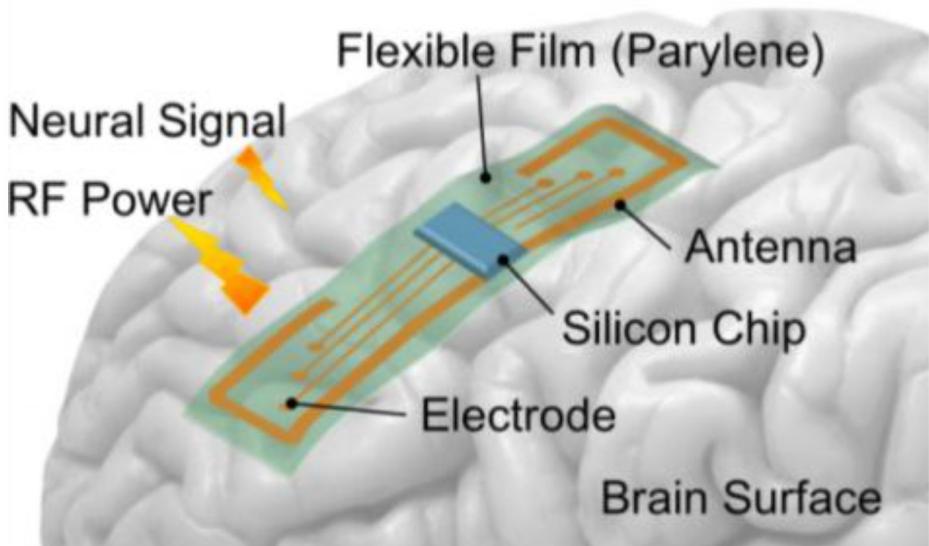


Interactive shoes

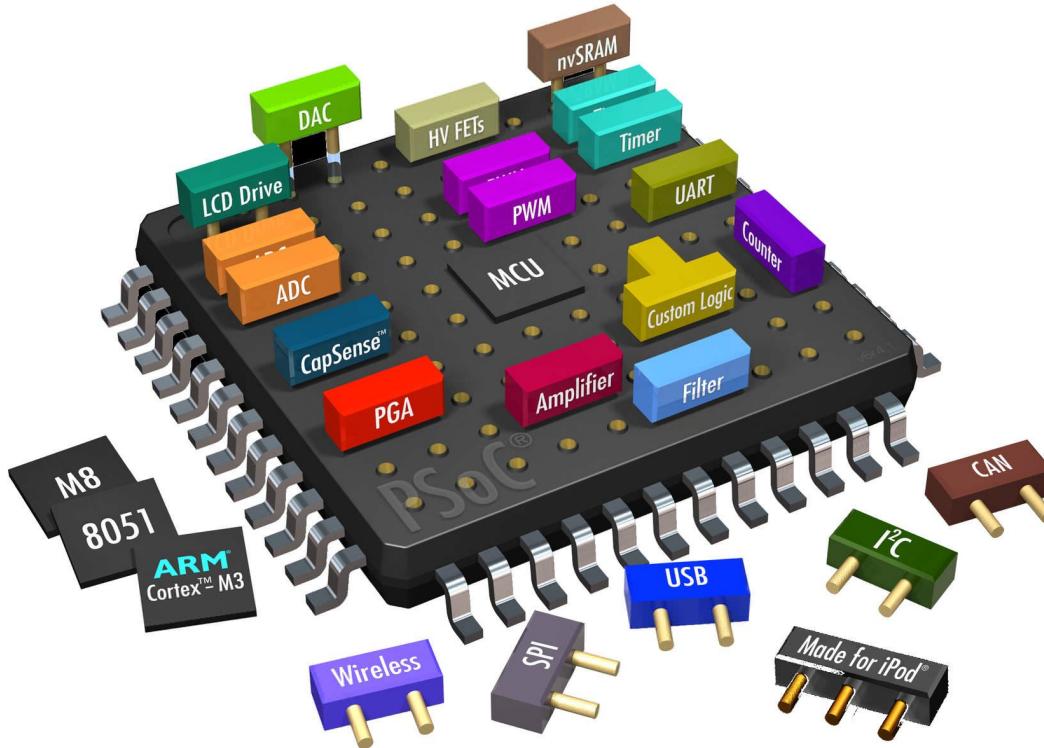


Smart clothing

Gömülü Sistemler



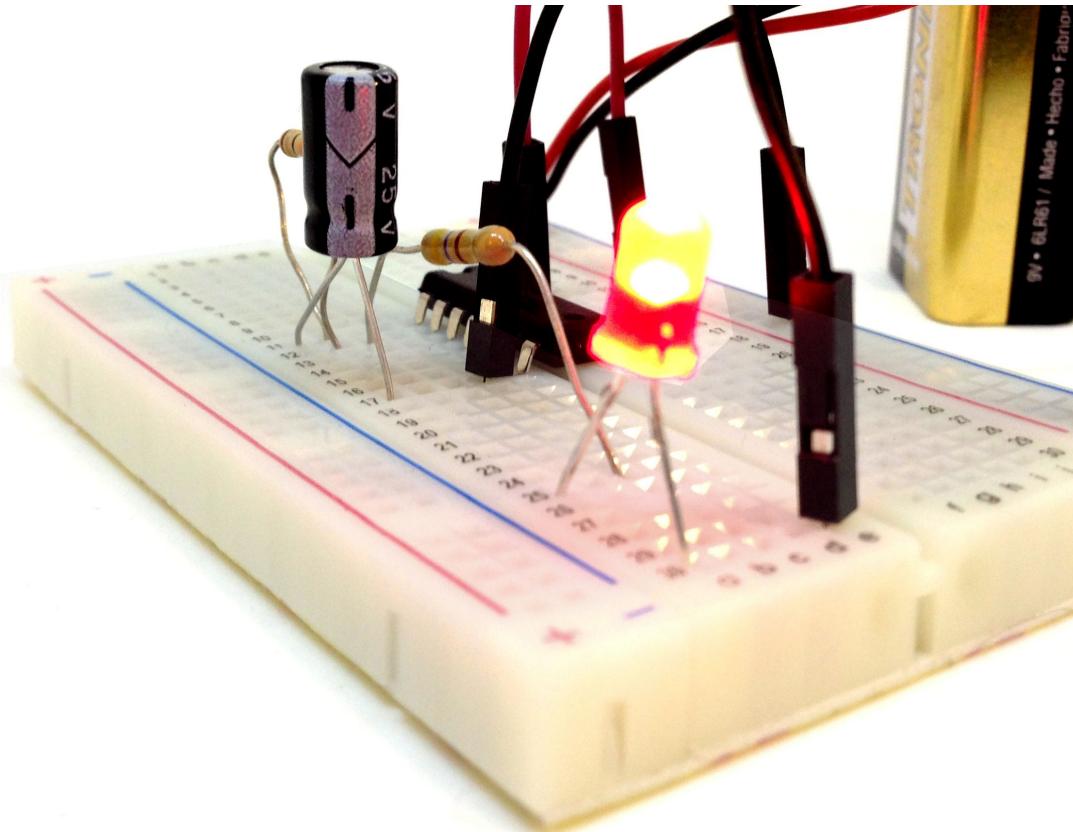
Development



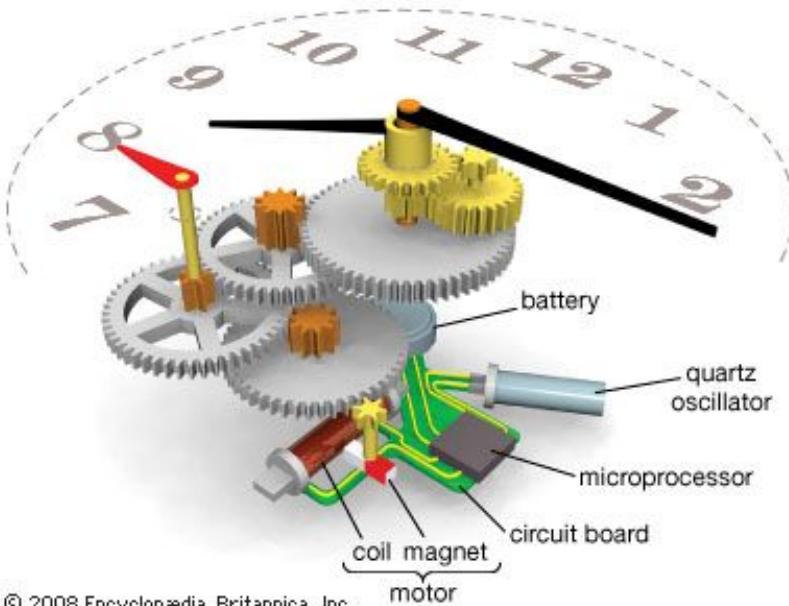
Gerekilikler



Gereklikler



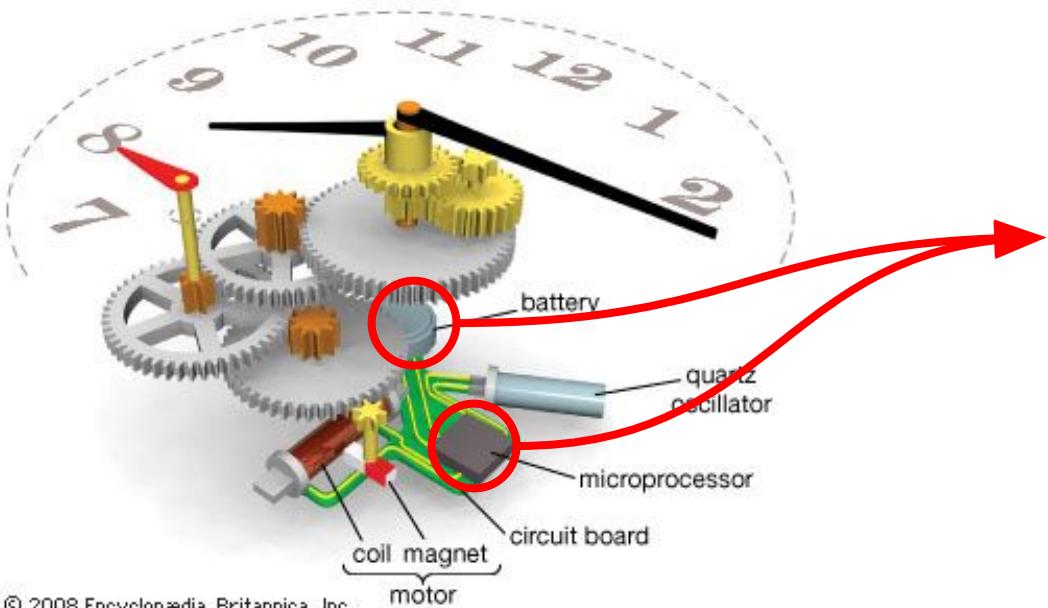
Gömülü Sistem



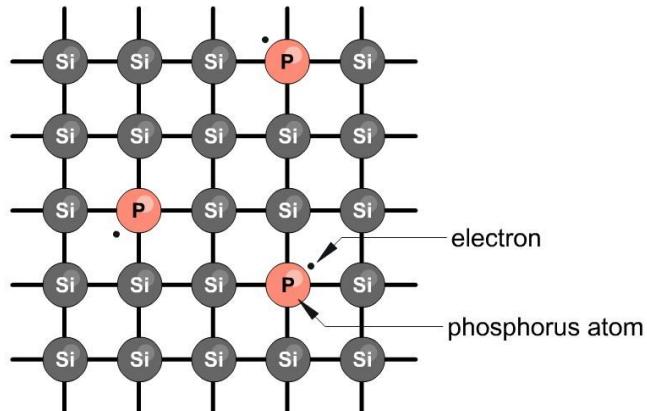
Bir amaç için
tasarlanmış



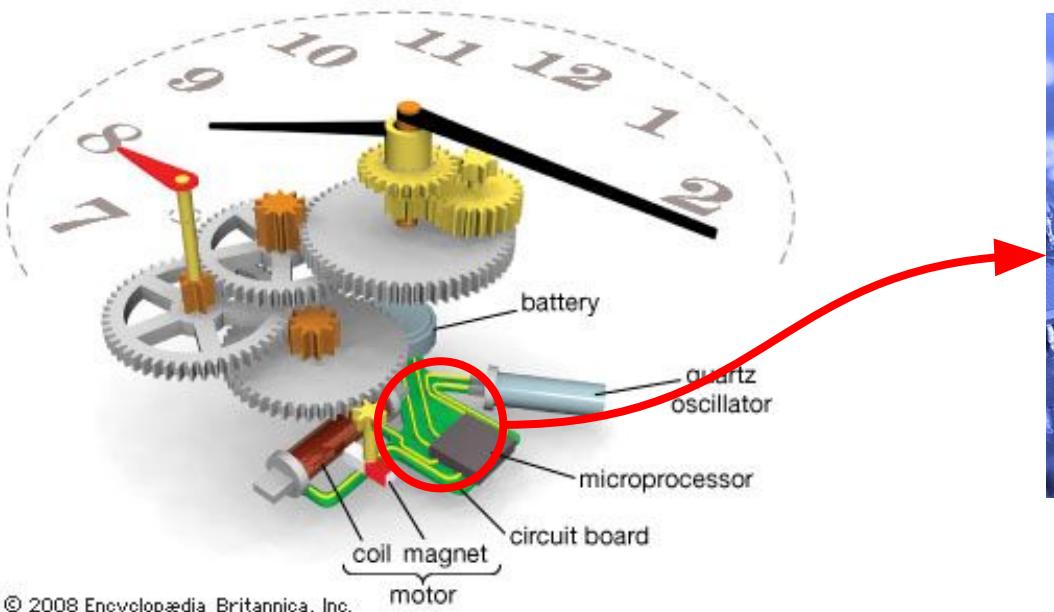
Gömülü Sistem



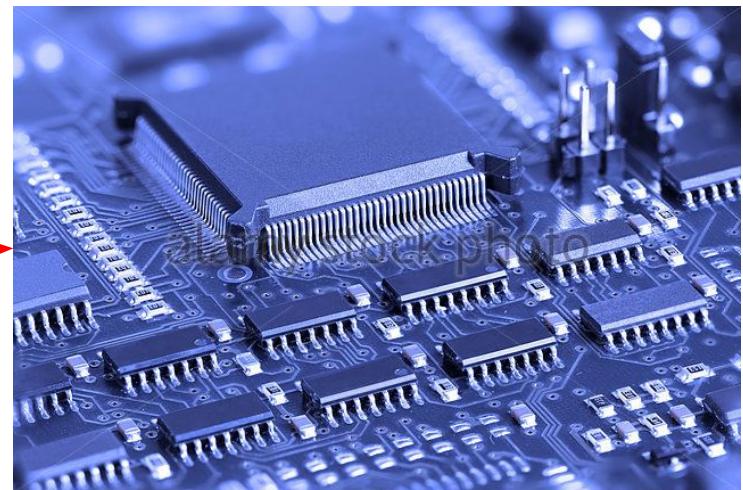
Kimsal



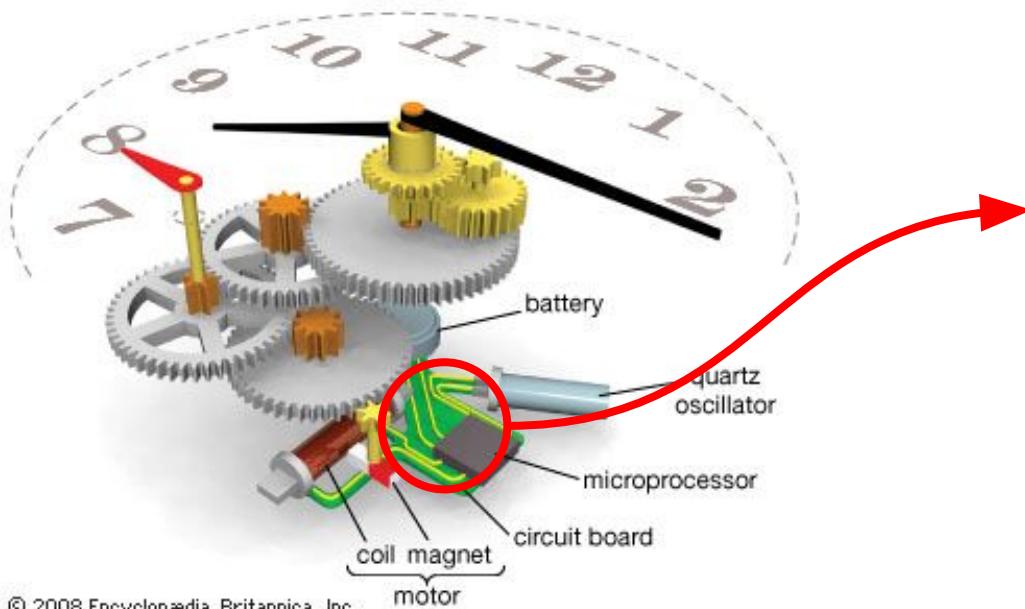
Gömülü Sistem



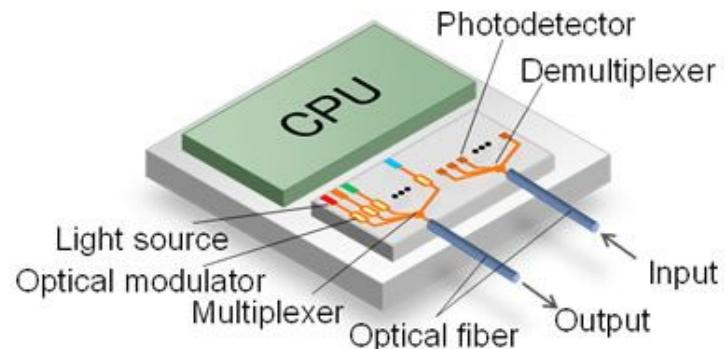
Elektronik



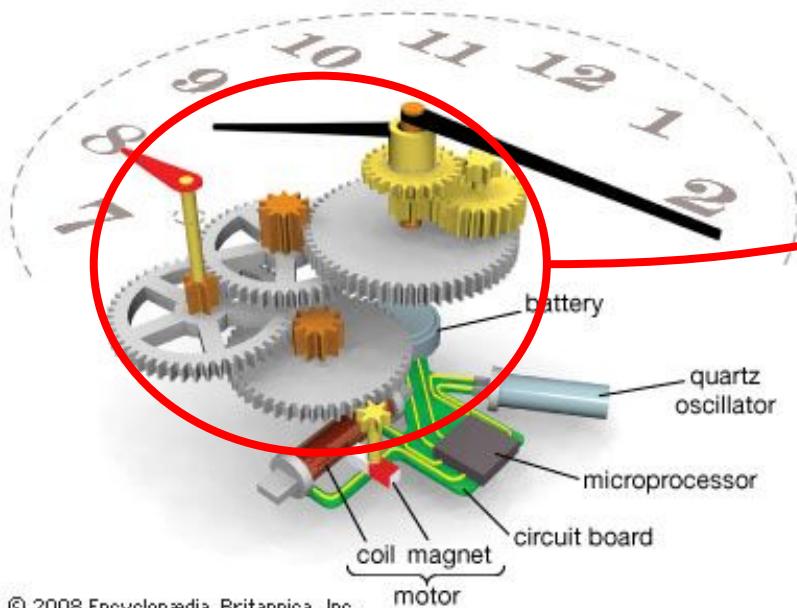
Gömülü Sistem



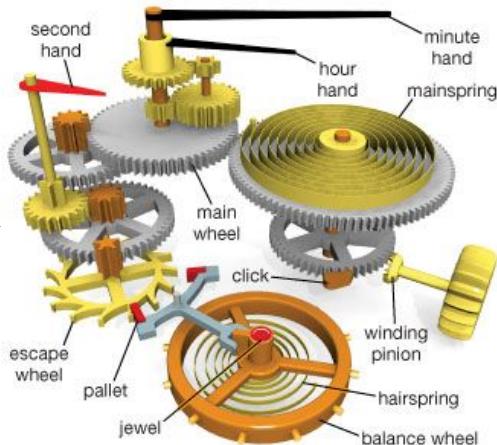
Optik



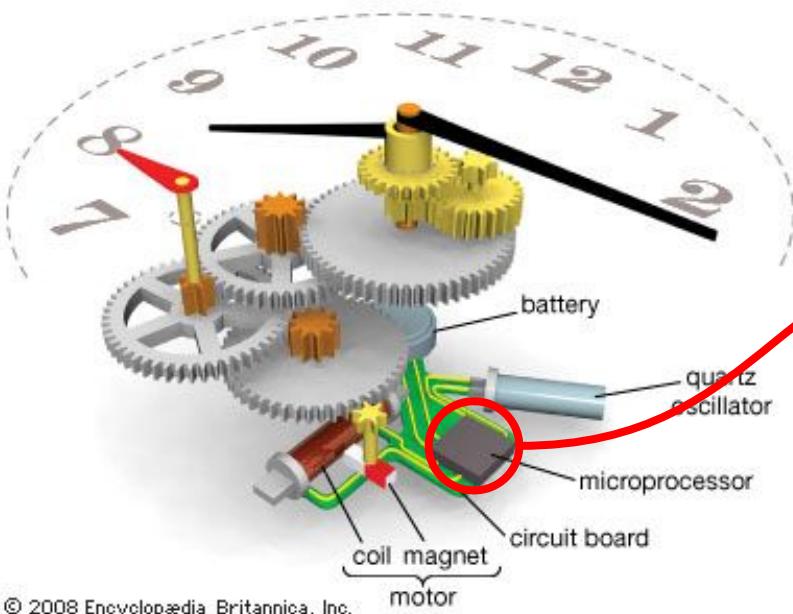
Gömülü Sistem



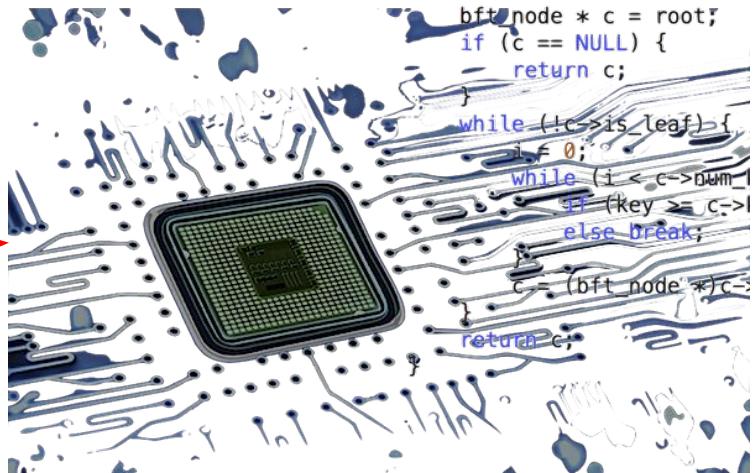
Mekanik



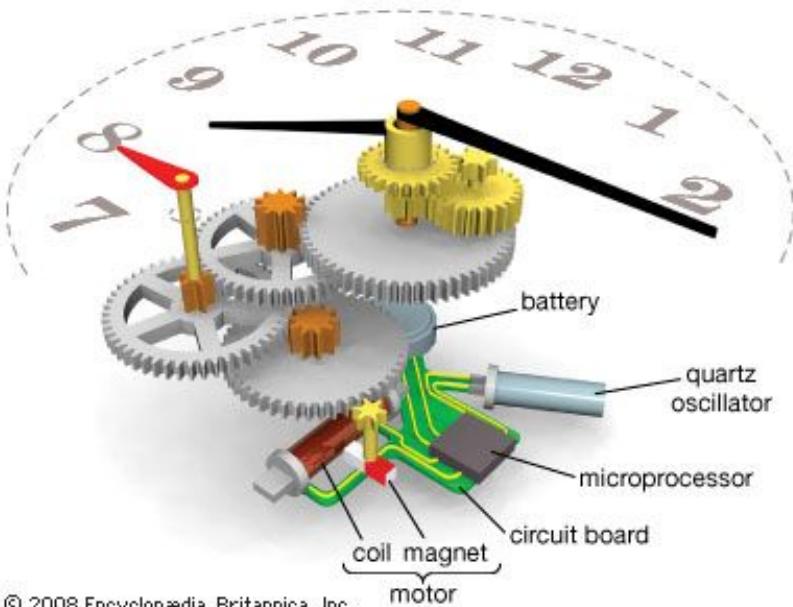
Gömülü Sistem



Yazılım



Gömülü Sistem

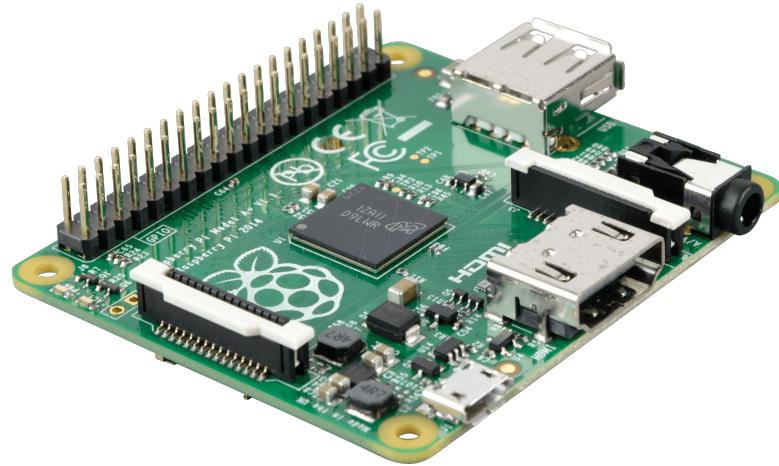


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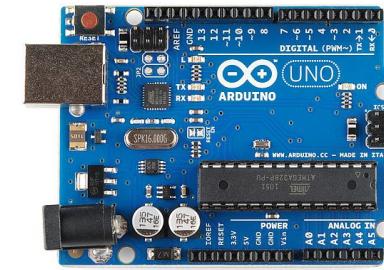
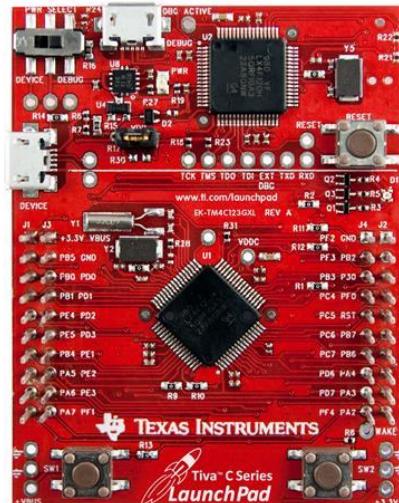


Gömülü Sistem Tipleri

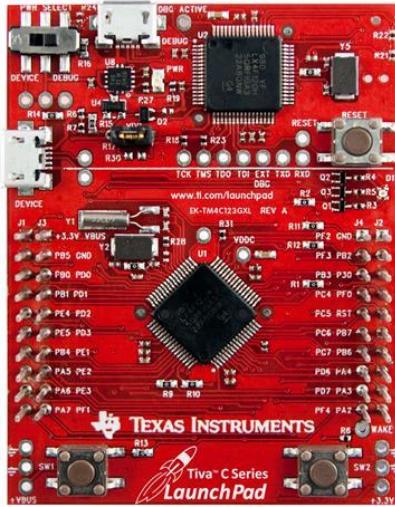
Mikroislemci tabanlı
gömülü sistem



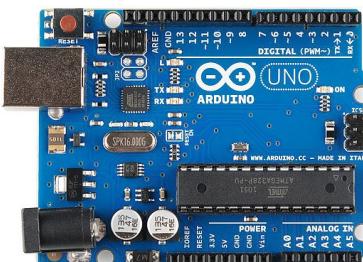
Mikrokontrolcü tabanlı
gömülü sistem



Mikrokontrolcü Tabanlı GS



- ❖ Küçük işlem gücüne sahip işlemci(PIC, AVR, ARM Cortex M)
- ❖ 8 veya 16 bitlik basit komut seti
- ❖ 10-50 Mhz saat sinyali
- ❖ işletim sistemi destegi yok
- ❖ C veya assembly ile programlama
- ❖ Düşük maliyet



Mikroişlemci Tabanlı GS



- ❖ Güçlü işlemci: Intel Atom, PowerPC,
ARM Cortex A ve üst versiyonlar
- ❖ 32 veya 64 bitlik basit komut seti
- ❖ 1000MHz - 2 GHz veya üstü saat sinyali
- ❖ işletim sistemine destegi (Linux, Windows ...)
- ❖ C/C++, Python, Java v.b. ile programlama
- ❖ Karmasık sistemler: Image/Video işleme

Haberleşme sistemleri

ARM Cortex Ailesi

Cortex®-M processors

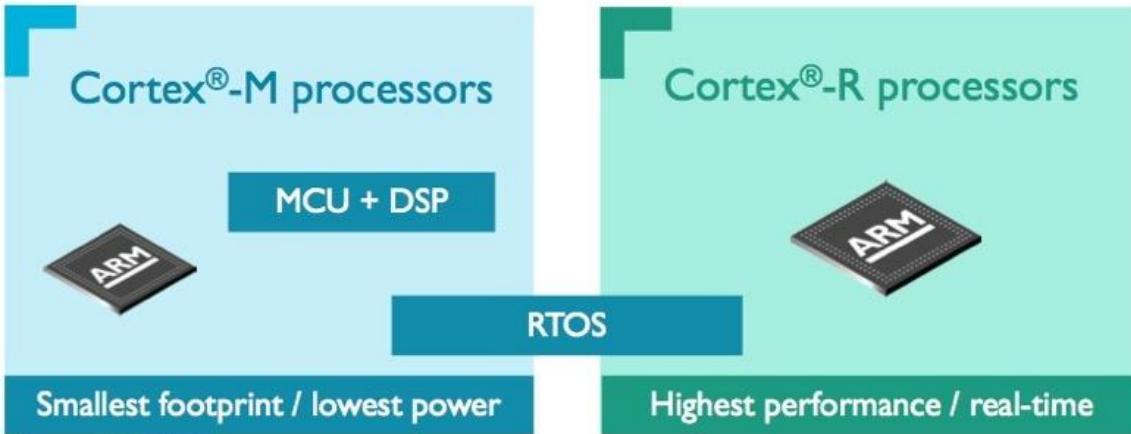
MCU + DSP



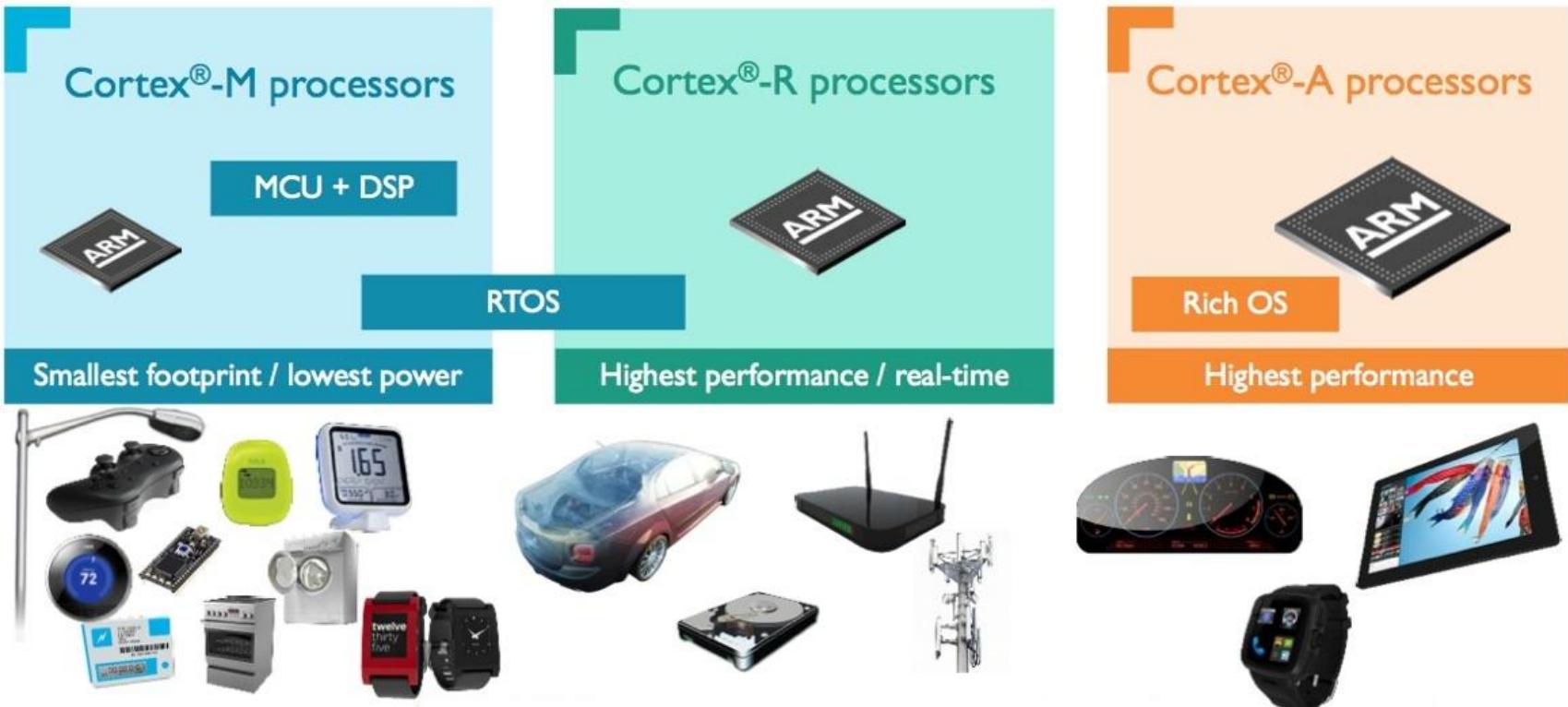
Smallest footprint / lowest power



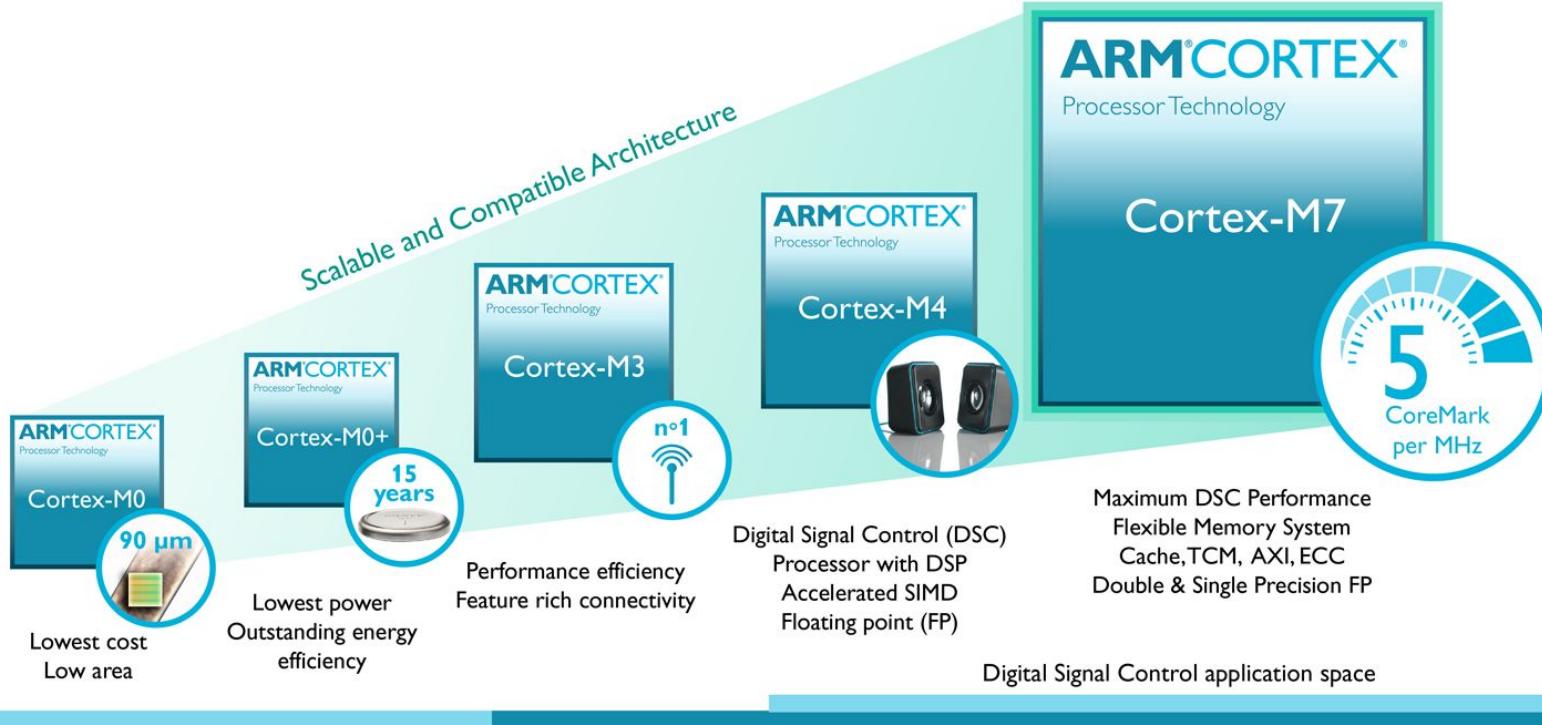
ARM Cortex Ailesi



ARM Cortex Ailesi



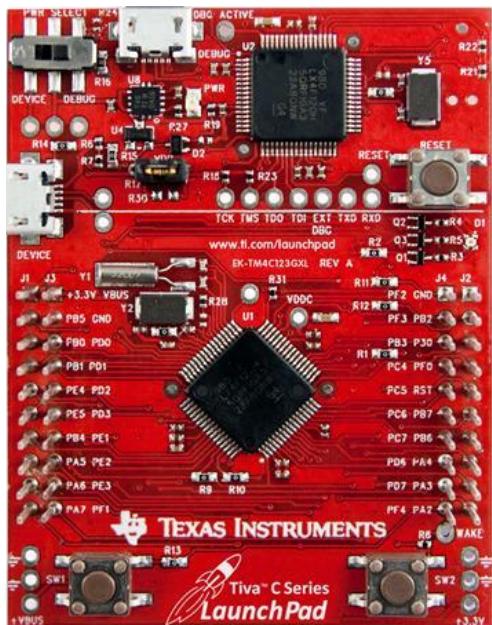
ARM Cortex M



'8/16-bit' Traditional application space

'16/32-bit' Traditional application space

Texas Stellaris ve Tiva



ARM® Compatible Architecture



Performance efficiency
Feature rich connectivity



Digital Signal Control (DSC)
Processor with DSP
Accelerated SIMD
Floating point (FP)

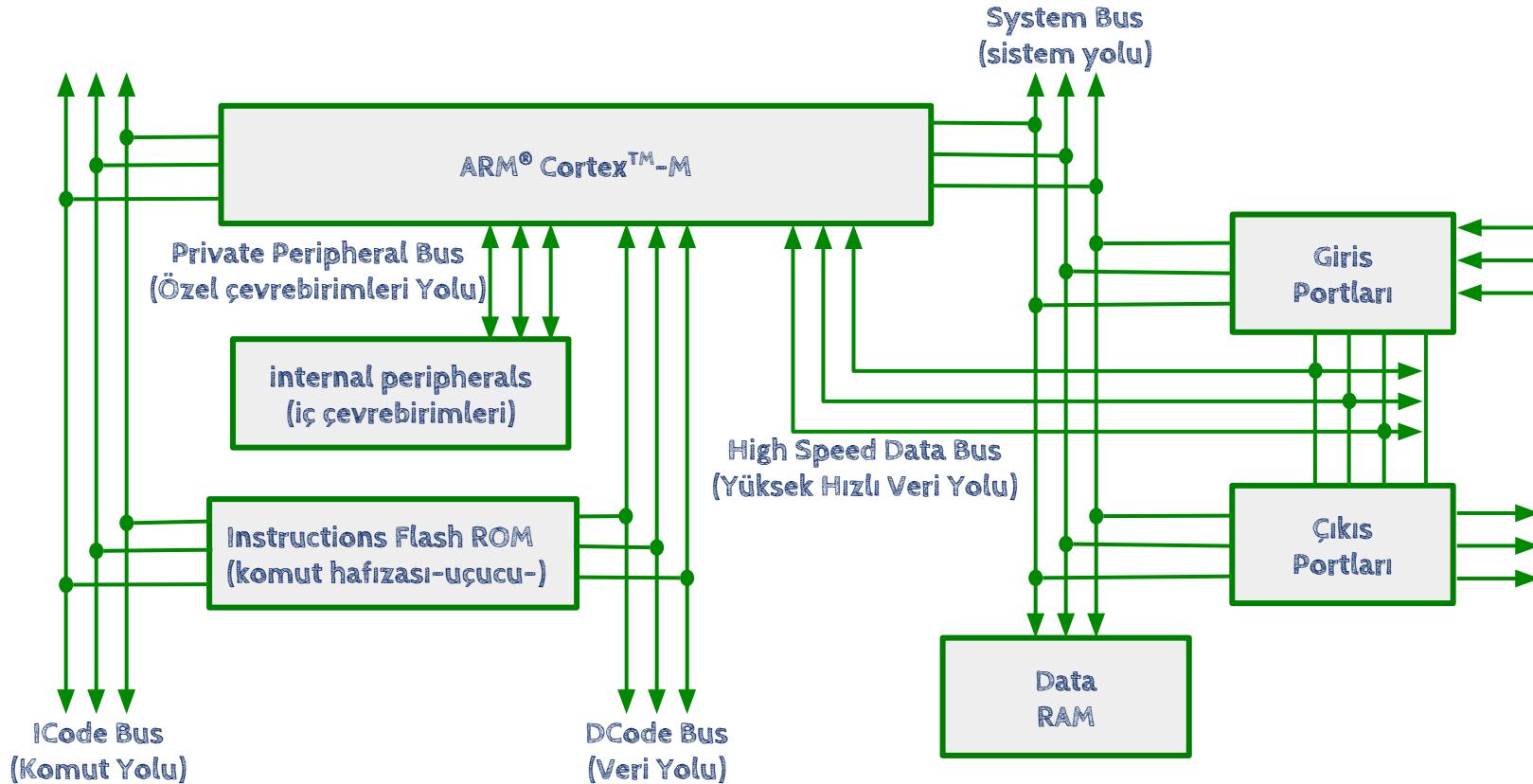
Digital Signal Control application space



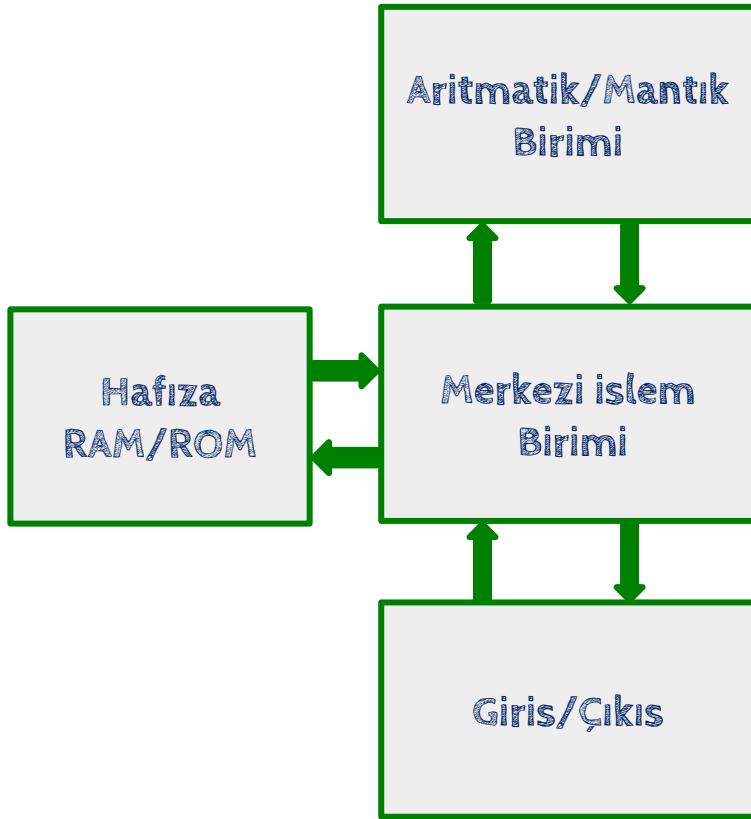
Maximum DSC Performance
Flexible Memory System
Cache, TCM, AXI, ECC
Double & Single Precision FP

TM4C Series MCU - 80 MHz ARM Cortex-M4 CPU w/ Floating Point

ARM® Cortex-M



Bilgisayar mimarisi

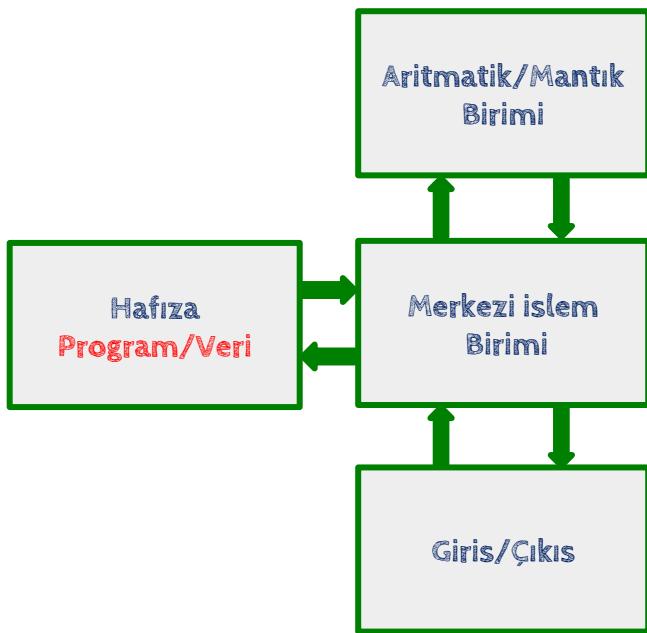


Donanım Mimarisi
(Hardware System Architecture - HSA)

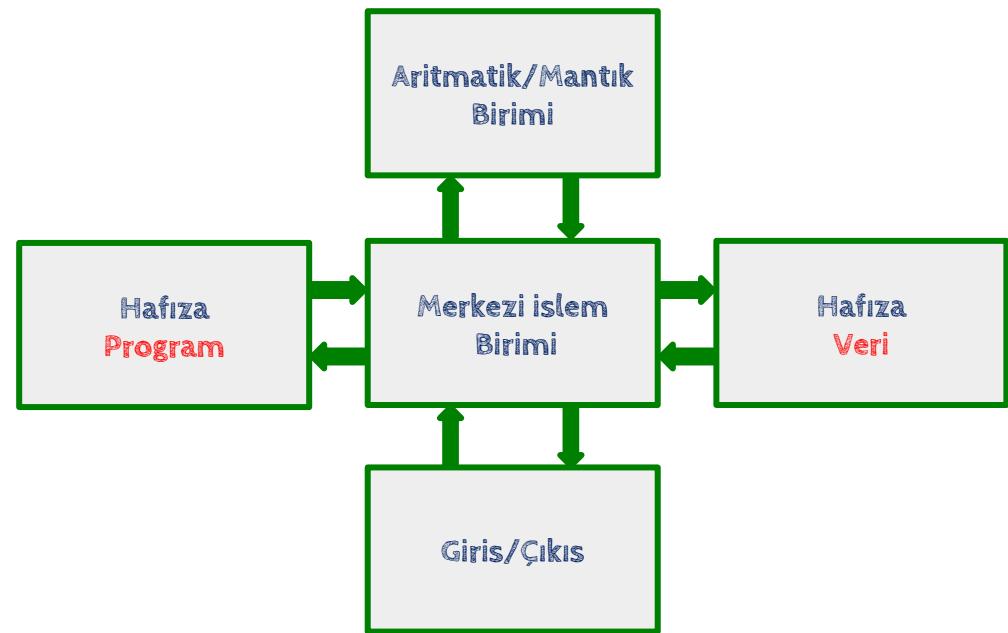
Komut Seti Mimarisi
(Instruction Set Architecture - ISA)

Donanım Mimarisi

von-Neuman



Harward



Komut Seti Mimarisi ISA

❖ Hangi komut setini desteklediği

- CISC Mimarisi (Complex Instruction Set Computer)
- RISC Mimarisi (Reduce Instruction Set Computer)

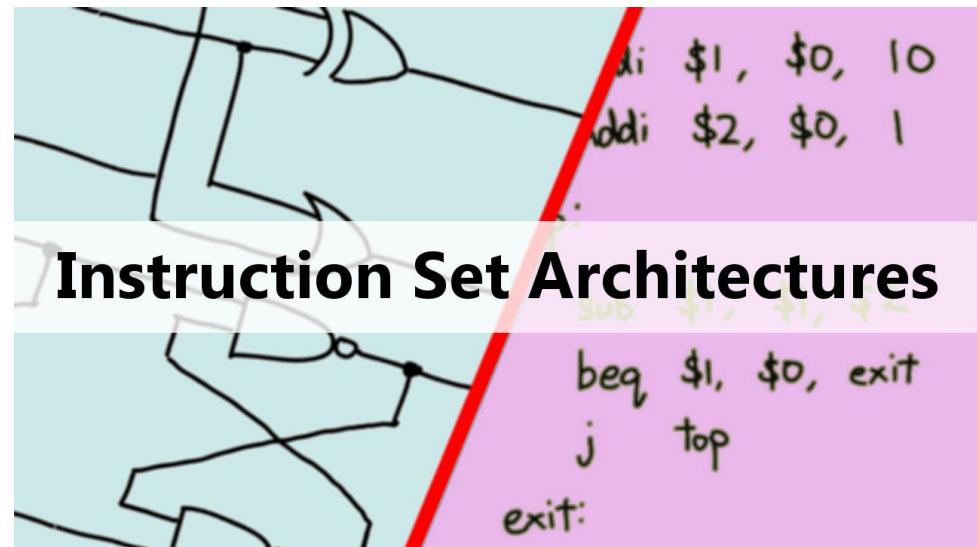
❖ Veri Tipleri

- "char" veri tipi
- "int" veri tipi
- "floating point" veri tipi

❖ Saklayıcılar

- Genel amaçlı saklayıcılar
- Özel amaçlı saklayıcılar

❖ Adres Alanı



RISC VS CISC

RISC

LW A, 2:3

LW B, 5:2

MULT A,B

SW 2:3,A

CISC

MULT 2:3, 5:2



VS



Veri Tipleri

Sayı sistemleri

$$1984 = 1 \times 10^3 + 9 \times 10^2 + 8 \times 10^1 + 4 \times 10^0$$

$$273.15 = 2 \times 10^2 + 7 \times 10^1 + 3 \times 10^0 + 1 \times 10^{-1} + 5 \times 10^{-2}$$

İkili sayı sistemi

$$\begin{aligned}01101010_2 &= 0 \times 2^7 + 1 \times 2^6 + 1 \times 2^5 + 0 \times 2^4 + 1 \times 2^3 + 0 \times 2^2 + 1 \times 2^1 + 0 \times 2^0 \\&= 64 + 32 + 8 + 2 \\&= 106\end{aligned}$$

Onaltılık sayı sistemi

İkilik sayı sistemi

```
00111110000001111001001100011011100101001  
110111110000111010111110110001110111111  
1101111100001110001101010010111010011111000  
110001110111101111111111010001111110  
111100110111011110001111011101011101000111  
1100110111111011000100100000010111011101110  
11110001111100011111111111001110011111  
0111111011001110111110101111101111111  
11111111000111111100101001010001111011110  
00011111111011000101000011110010000000111  
10111110011111110101111110001011101111000  
01110000111101110111111001111111001111111001  
1011101101000010011001100011101110000110010  
1101100110001010111101101000111110100011010  
111111110011000111100111101010000110100111  
11101000010111100111111100000111110111111011  
11100100110101111111110011111111111111110  
0000000001111111000011001111011100100011011  
010001001111111001111111111110011111000111  
0100011110010011111110000101111011100110111
```

Onaltılık sayı sistemi

30	39	30	39	31	36	35	30	30	30	31	39	36	32	33	0D
0A	3A	31	30	31	45	35	30	30	30	39	30	39	33	36	35
30	30	38	30	39	33	36	34	30	30	32	46	35	46	33	46
34	46	38	30	39	31	36	36	30	31	45	46	0D	0A	3A	31
54	68	69	73	20	69	73	20	61	20	68	65	78	61	64	65
63	69	6D	61	6C	20	74	75	74	6F	72	69	61	6C	21	46
38	39	34	45	31	39	39	33	36	0D	0A	3A	31	30	31	45
37	30	30	30	00	01	02	03	04	05	06	07	08	09	0A	0B
0C	0D	0E	0F	10	11	12	13	14	15	16	17	18	19	1A	1B
1C	1D	1E	1F	20	21	22	23	24	25	26	27	28	29	2A	2B
2C	2D	2E	2F	30	31	32	33	34	35	36	37	38	39	3A	3B
3C	3D	3E	3F	40	41	42	43	44	45	46	47	48	49	4A	4B
4C	4D	4E	50	51	52	53	54	55	56	57	58	59	5A	5B	5C
5D	5E	5F	60	61	62	63	64	65	66	67	68	69	6A	6B	6C
6D	6E	6F	70	71	72	73	74	75	76	77	78	79	7A	7B	7C
7D	7E	7F	80	81	82	83	84	85	86	87	88	89	8A	8B	8C
8C	8D	8E	8F	90	91	92	93	94	95	96	97	98	99	9A	9B
9C	9D	9E	9F	A0	A1	A2	A3	A4	A5	A6	A7	A8	A9	AA	AB
AC	AD	AE	AF	B0	B1	B2	B3	B4	B5	B6	B7	B8	B9	BA	BB
BC	BD	BE	BF	C0	C1	C2	C3	C4	C5	C6	C7	C8	C9	CA	CB
CC	CD	CE	CF	D0	D1	D2	D3	D4	D5	D6	D7	D8	D9	DA	DB
DC	DD	DE	DF	E0	E1	E2	E3	E4	E5	E6	E7	E8	E9	EA	EB
EC	ED	EE	EF	F0	F1	F2	F3	F4	F5	F6	F7	F8	F9	FA	FB
FC	FD	FE	FF	33	39	43	0D	0A	3A	31	30	31	45	44	30
30	30	35	37	30	30	45	38	39	35	33	32	39	36	30	32

Onaltılık sayı sistemi

Binary

0011011011001101

Hexadecimal	Decimal	Binary
0	0	0
1	1	1
2	2	10
3	3	11
.	.	.
.	.	.
.	.	.
9	9	1001
A	10	1010
B	11	1011
C	12	1100
D	13	1101
E	14	1110
F	15	1111

Onaltılık sayı sistemi

Binary

0011011011001101

Nibbles

0011 0110 1100 1101

Hexadecimal

36CD

Hexadecimal	Decimal	Binary
0	0	0
1	1	1
2	2	10
3	3	11
.	.	.
.	.	.
.	.	.
9	9	1001
A	10	1010
B	11	1011
C	12	1100
D	13	1101
E	14	1110
F	15	1111

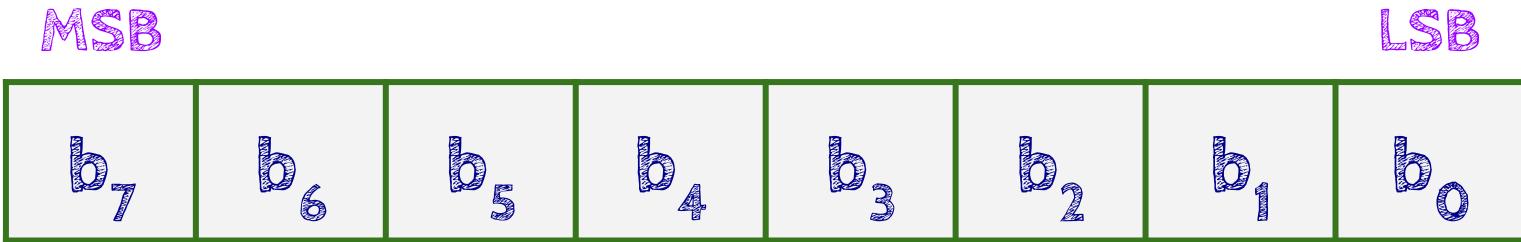
Onaltılık sayı sistemi

$$0011011011001101_2 = \text{0x36CD}$$

$$0011011011001101_2 = 36CDh$$

$$0011011011001101_2 = \$36CD$$

Byte



$$N = 128 * b_7 + 64 * b_6 + 32 * b_5 + 16 * b_4 + 8 * b_3 + 4 * b_2 + 2 * b_1 + 1 * b_0$$

Byte

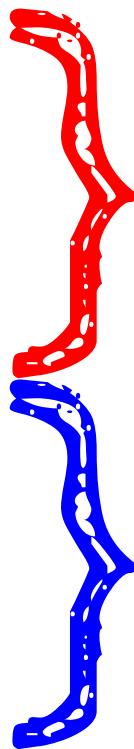
Binary	Hex	Ondalık
0 0 0 0 0 0 0 0	0x00	0
0 0 1 0 0 0 0 1	0x21	33
0 1 0 0 0 1 1 0	0x46	70
1 0 0 0 1 0 1 1	0x8B	139
1 1 1 1 1 1 1 1	0xFF	255

Taban Dönüşümleri

Sayı	Taban	Sınır	Bit	İşlem
100	128	-	bit 7 = 0	-
100	64	astı	bit 6 = 1	$100 - 64$
36	32	astı	bit 5 = 1	$36 - 32$
4	16	-	bit 4 = 0	-
4	8	-	bit 3 = 0	-
4	4	esit	bit 2 = 1	$4 - 4$
0	2	-	bit 1 = 0	-
0	1	-	bit 0 = 0	-

Negatif Sayılar(8 bit)

bir'e tümleyen
one's complement



$$(0)_{10}$$

$$(1)_{10}$$

$$= (00000000)_2$$

$$= (00000001)_2$$

...

...

...

$$(126)_{10}$$

$$(127)_{10}$$

$$(-127)_{10}$$

$$(-126)_{10}$$

$$= (01111110)_2$$

$$= (01111111)_2$$

$$= (10000000)_2$$

$$= (10000001)_2$$

...

...

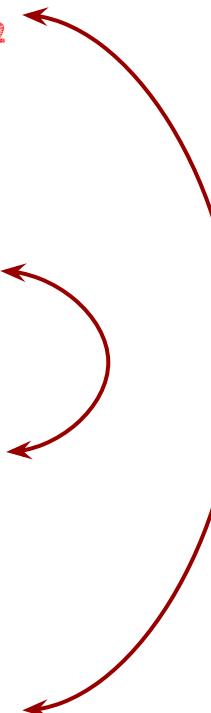
...

$$(-2)_{10}$$

$$(-0)_{10}$$

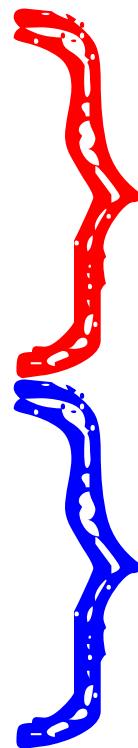
$$= (11111110)_2$$

$$= (11111111)_2$$



Negatif Sayılar(8 bit)

bir'e tümleyen
one's complement



$$(0)_{10} = (0000000^{\circ})_2$$

$$(25)_{10} \\ (00011001)_2$$

$$(-25)_{10} = 11^{\circ}$$

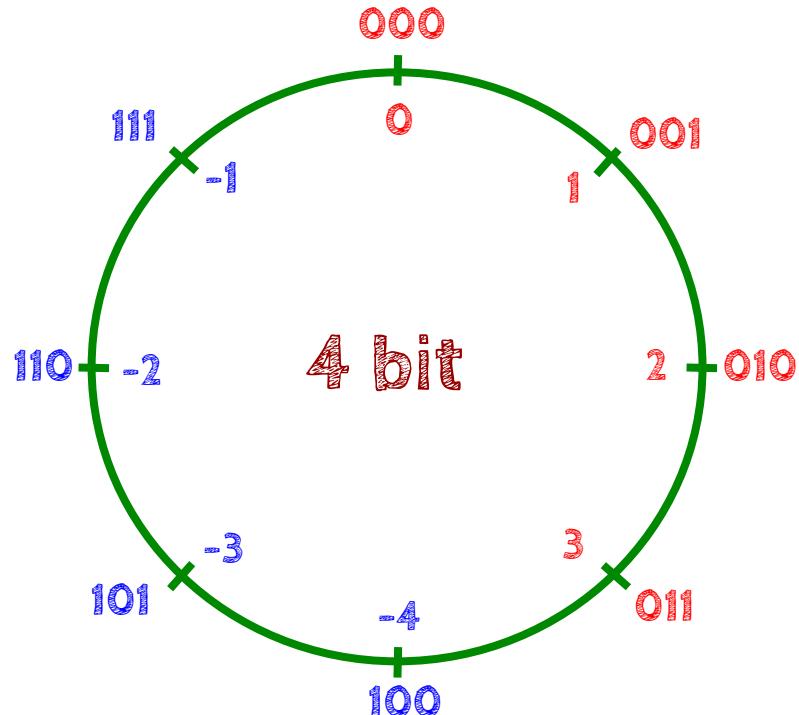
$$(-0)_{10} = (11111111)_2$$

iki tane sıfır var

Taban?

Negatif Sayılar(4 bit)

iki'ye tümleyen
two's complement



$$\begin{array}{r} (25)_{10} = (00011001)_2 \Rightarrow \\ + \\ (11100110)_2 \\ (00000001)_2 \\ \hline (-25)_{10} = (11100111)_2 \end{array}$$

Negatif Sayılar

iki'ye tümleyen
two's complement

Binary	Hex	Hesaplama	Ondalık
00000000	0x00		0
00010010	0x12	16+2	18
00100110	0x26	32+4+2	38
11000111	0xC7	128+64+4+2+1	-57
11111111	0xFF	-128+64+32+16+8+4+2+1	-1

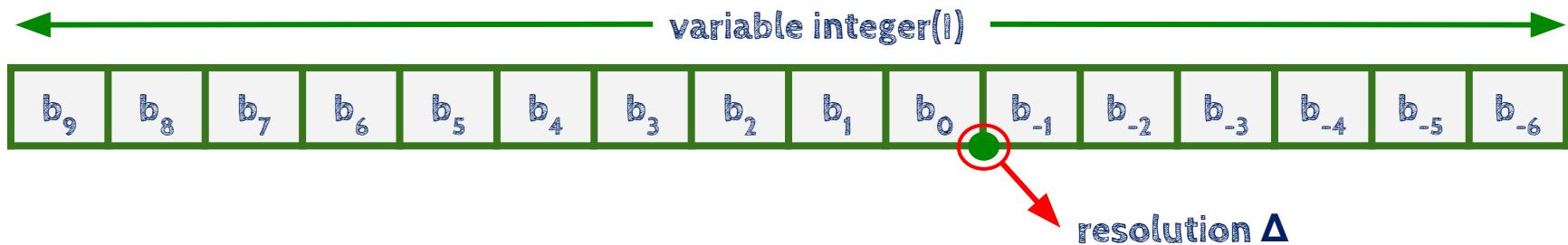
Negatif Sayılar

Sayı	Taban	Sınır	Bit	İşlem
-100	-128	astı	bit 7 = 1	$-100 - (-128)$
28	64	-	bit 6 = 0	-
28	32	-	bit 5 = 0	-
28	16	astı	bit 4 = 1	$28 - 16$
12	8	astı	bit 3 = 1	$12 - 8$
4	4	esit	bit 2 = 1	$4 - 4$
0	2	-	bit 1 = 0	-
0	1	-	bit 0 = 0	-

Fixed-Point Numbers

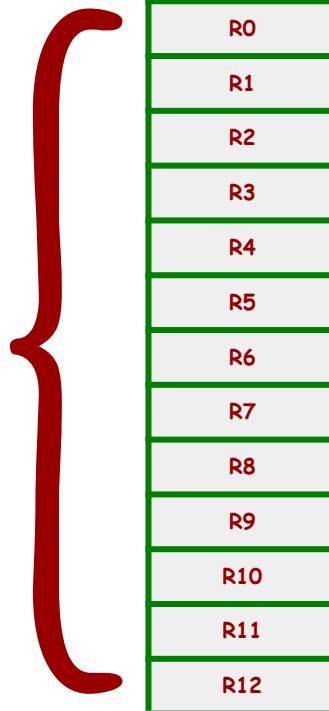
Precision (hassasiyet): temsil edilebilen değerlerin toplam sayısıdır.

16-bit binary fixed-point format with $\Delta=2^{-6}$



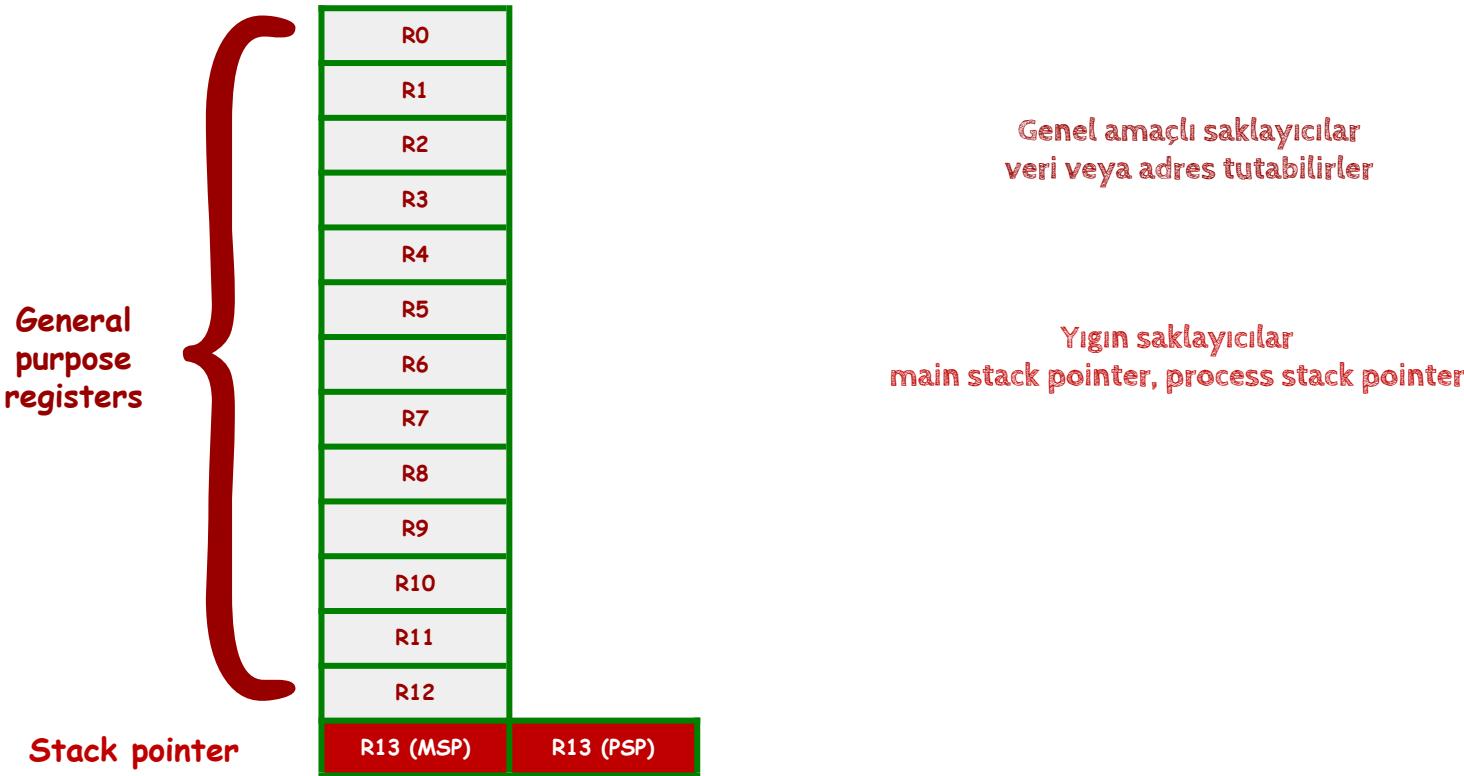
Saklayıcılar

General
purpose
registers



Genel amaçlı saklayıcılar
veri veya adres tutabilirler

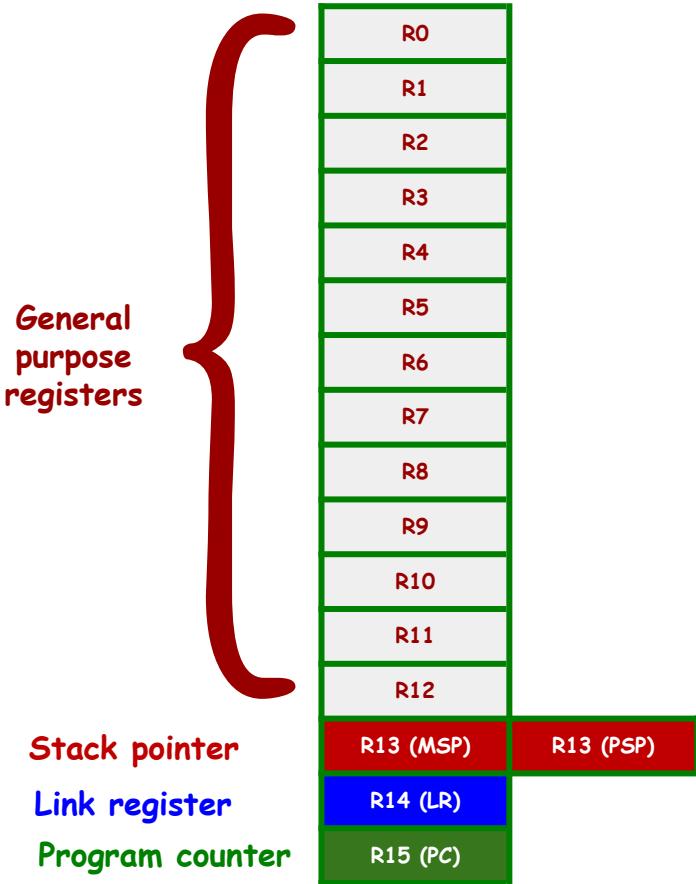
Saklayıcılar



Saklayıcılar



Saklayıcılar



Genel amaçlı saklayıcılar
veri veya adres tutabilirler

Yığın saklayıcılar
main stack pointer, process stack pointer

Link(Bag) saklayıcısı
Fonksiyonlardan dönüş adreslerini saklar
Kesme gibi ekstra durumlar için kullanılır

Program sayacı saklayıcısı
islemcinin bir sonraki adımda hangi komutu
isleyeceğini belirler

Program Durum Saklayiciları

APSR: Application Program Status Register

ALU islemeleri



N: işlem sonucu negatif

Z: işlem sonucu sıfır

C: işlem sonucu tasma (isaretsiz) var

V: işlem sonucu tasma (isaretli) var

Q: işlem sonucu tasma doyma var

Program Durum Saklayiciları

IPSR: Interrupt Program Status Register

Kesme islemeleri

31

8

0

Reserved

ISR_NUMBER

ISR_NUMBER: Kesme islem numarasi

Program Durum Saklayıcıları

EPSR: Execution Program Status Register

Çalışma İşlemleri



T=1: İşlemci Thumb komut setini kullanır

ICI/IT: Kesmeye ugramış çoklu işlemlerin devam edebilmesini sağlar

IT: Programdaki if-then komutlarının yürütme sayısını ve durumunu tutar

Program Durum Saklayiciları

PSR: Program Status Register



N: islem sonucu negatif

Z: islem sonucu sifir

C: islem sonucu tasma (isaretsiz) var

V: islem sonucu tasma (isaretli) var

Q: islem sonucu tasma doyma var

ICI/IT: Kesmeye ugramis çoklu islemlerin devam edebilmesini saglar

T=1: islemci Thumb komut setini kullanır

ISR_NUMBER: Kesme islem numarası

Özel Saklayıcılar

Special
registers

PSR	Program status register
PRIMASK	
FAULTMASK	
BASEPRI	
CONTROL	CONTROL register

} Exception mask registers

PRIMASK(Bit 0): interrupt mask bit
bit_0=1 bir çok kesmeye izin verilmez

FAULTMASK(Bit 0): fault mask bit
bit_0=1 tüm kesmelere ve hatalarına izin verilmez

BASEPRI: kesme önceligi
BASEPRI=3 ise 0,1,2 seviyesindeki kesmeler çalışır

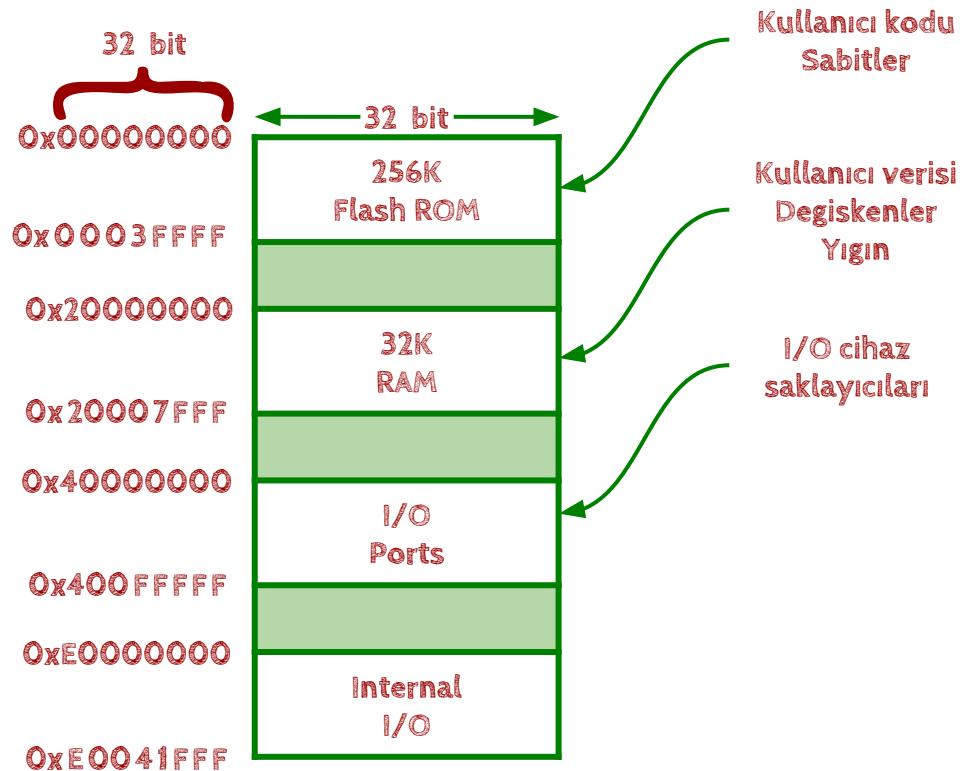
Adres Alanı

Adres Alanı

Adres Alanı

1K	1024 Byte	0x400
2K	2048 Byte	0x800
4K	4096 Byte	0x1000
8K	8192 Byte	0x2000
16K	16384 Byte	0x4000
32K	32768 Byte	0x8000
64K	65536 Byte	0x10000
128K	131072 Byte	0x20000
256K	262144 Byte	0x40000

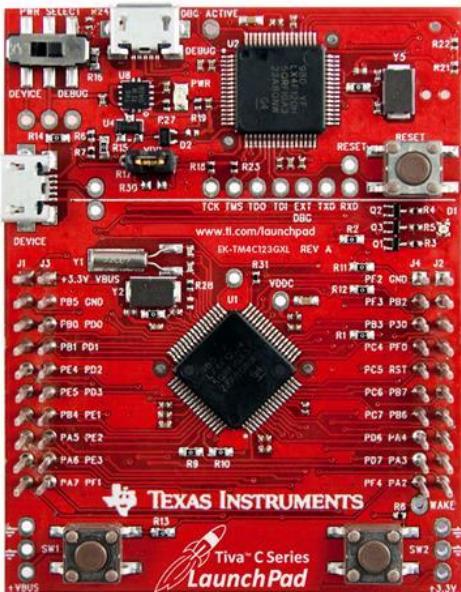
Adres Alanı



Gelistirme Kartlari

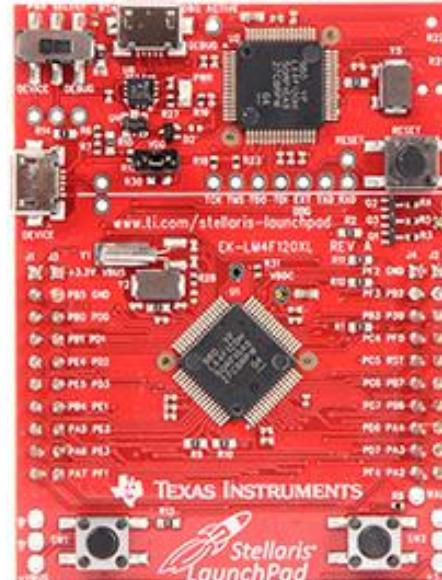
Tiva C Series EK-TM4C123GXL

<http://www.ti.com/litv/pdf/spmu296>

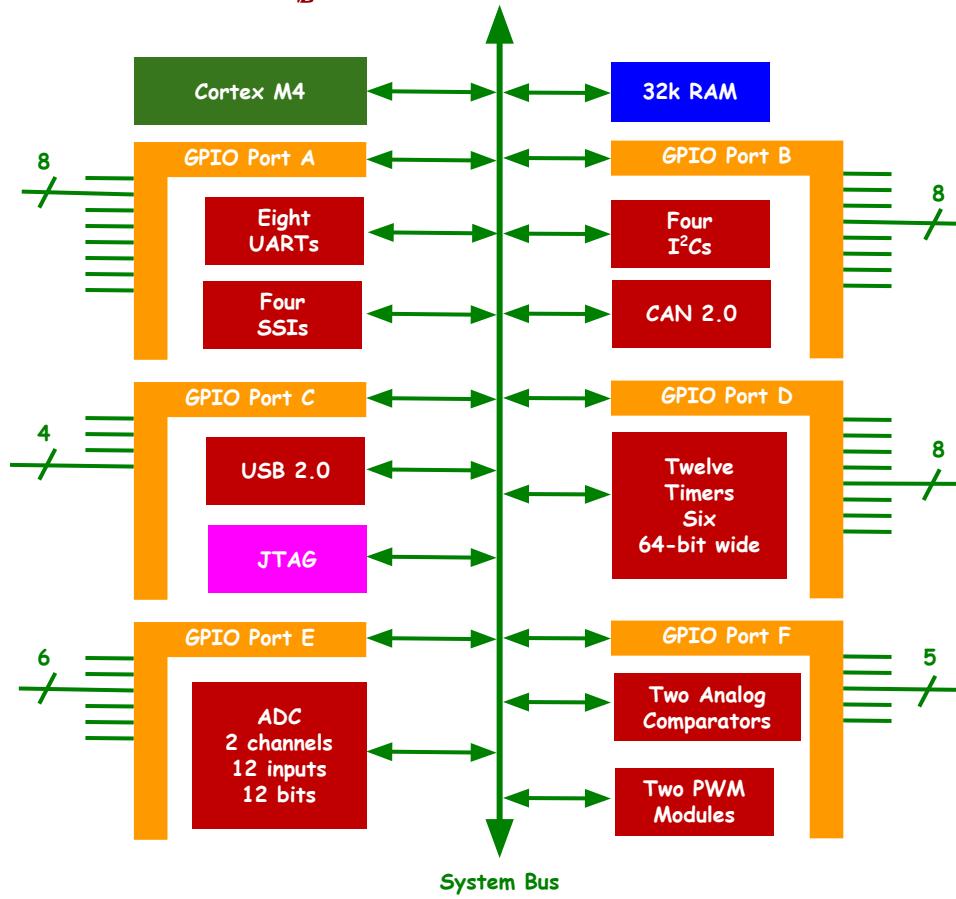


Stellaris EK-LM4F120XL

<http://www.ti.com/lit/ug/spmu289c/spmu289c>



I/O Portları



Sorular

