

STM32MP25XXAK_LPDDR4

Schematics not complete, only for layout example

Table of contents

Sheet 1: Project overview (this page)

Sheet 2: TOP VIEW

Sheet 3: STM32MP25XXAK I/Os

Sheet 4: PMIC

Sheet 5: STM32MP25XXAK Power

Sheet 6: LPDDR4

Sheet 7: eMMC

Legend

General comment such as function title, configuration, ...

Text to be added to silkscreen.

Warning text.

△ Notes to generate the board layout.

Open Platform License Agreement

The Open Platform License Agreement (“Agreement”) is a binding legal contract between you (“You”) and STMicroelectronics International N.V. (“ST”), a company incorporated under the laws of the Netherlands acting for the purpose of this Agreement through its Swiss branch 39, Chemin du Champ des Filles, 1228 Plan-les-Ouates, Geneva, Switzerland.

By using the enclosed reference designs, schematics, PC board layouts, and documentation, in hardcopy or CAD tool file format (collectively, the “Reference Material”), You are agreeing to be bound by the terms and conditions of this Agreement. Do not use the Reference Material until You have read and agreed to this Agreement terms and conditions. The use of the Reference Material automatically implies the acceptance of the Agreement terms and conditions.

The complete Open Platform License Agreement can be found on www.st.com/opla.

U_STM32MP25XXAK_LPDDR4-TOP
STM32MP25XXAK_LPDDR4-TOPSchDoc



Title: Project overview	Project: STM32MP25XXAK_LPDDR4
Variant: [No Variations]	
Revision: A-01	Reference: Schematics no
Size: A4	Date: 02-OCT-24
	Sheet: 1 of 7













