J1
INHIB-STOP-INV-CPU 7
SUPPLY-BUZ-CPU 6
TEST-BUZZER-CPU 5
STOP-INV-CPU 4
PWM-BUZZER-CPU 3
+5V_CPU 2
GND 1
Conn_01x07

D1
NC

FI1
BLA31BD221SN4D
SUPPLY-BUZ
INHIB-STOP-INV
TESTBUZ
STOP-INV

FU1 500mA
L1 Core_Ferrite
+5V

C2 220pF
GND

C1 47pF
GND

R4 10k
R5 10k

C6 680pF
C4 680pF

IC2A
MC33172
GND

R9 32.4k
GND
R11 105k

IC3A
MC33172

+12V
T1 BC847
R13
R12 470
R14
T2 BC857
-12V

BUZ1 PKM33EPH
PKM33EPH BUZ2

BAT54S
D3
R15 33
BAT54S
D4

+12V
T3 BC847
R19
R22 470
R20
T4 BC857
-12V

C12 470µF

R26 137k
IC3B
MC33172
R28 32.4k
GND

R6 56k
SUPPLY-BUZ
C5 100nF
GND

R2 100k
R3 100k

IC1
LMC7211
R7 51.1k
GND

C3 10nF
GND

D2 BAT54C

C9 100nF
GND

R8 1k
R10 100k
GND

+12V
C10 100nF
GND
IC3C
MC33172
C11 100nF
-12V
GND

+12V
C7 100nF
GND
IC2C
MC33172
C8 100nF
-12V
GND

C14 33nF
R21 150k
R16 10k
IC2B
MC33172
R17 10k
R18 150k
C13 33nF
GND
R23 2k

BUZ-ACT

R27 68k
GND
R29 100k
+5V
C30 100nF
GND
R24 100k
D5 BZX84C2V7
GND
IC4
LMV321
TESTBUZ
GND

VBAT
R25 NC
R69 200k
VCHARG
R59 100k
GND

Sheet: GR106768_buzzer_charger
BUZ-ACT — BUZ-ACT   SUPPLY-BUZ — SUPPLY-BUZ
STOP-INV — STOP-INV   VBAT — VBAT
INHIB-STOP-INV — INHIB-STOP-INV   VCHARG — VCHARG
File: GR106768_buzzer_charger.sch
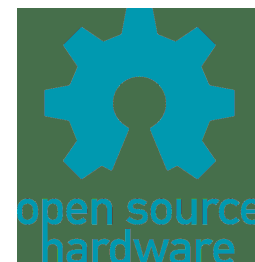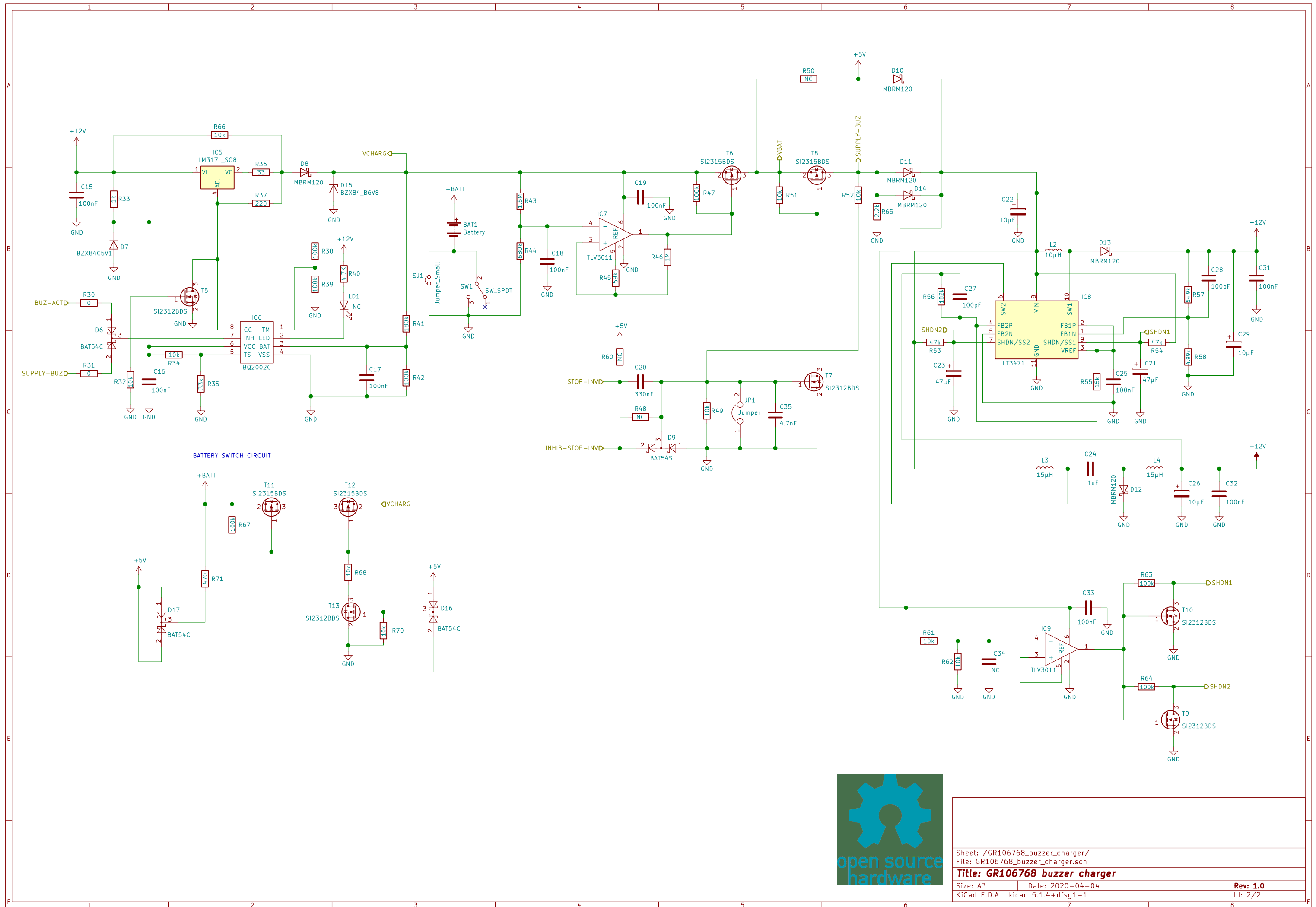
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Design for OSHPark 4-Layer Service
Sheet: /
File: GR106768 Buzzer.sch
Title:
Size: A3   Date: 2020-04-06   Rev: 1
KiCad E.D.A. kicad 5.1.4+dfsg1-1   Id: 1/2

BATTERY SWITCH CIRCUIT

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