Reverse Engineering 101

...in **20** minutes

Who even am I?

Morgan Whitlow

- Currently:
 - Embedded firmware reverse engineer
 - Hardware enthusiast
 - Sarcastic smartass

Education:

- Master of Science in Applied Computer Science
- B.S. Biology, B.A. Psychology

Formerly:

- SOC analyst
- Lockpicking instructor
- Nanomaterials researcher
- Various other stuff



Overview

- Human vs machine code
- Layers of Abstraction
- What is reverse engineering?
- Static & Dynamic
- IDA vs Ghidra
- Niches & Specialties

Who speaks what?

Binary

- Mother tongue of machinery. Number system consisting literally of 1's and 0's

Hexadecimal ('hex')

 Base 16 number system (0,1,2,3,4,5,6,7,8,9,A,B,C,D,E,F). Easier to read than binary, useful for quickly assessing specific characters or 'op(eration) codes'

Assembly language

 Hex translated into mnemonics. Lowest level 'human readable' programming language. Says what specific instructions the processor is doing step-by-step. Diff kind of processor? Might be a diff assembly language.

Language Levels

- Machine Code

 - Represented as hex:

- Low Level Languages
 - Assembly

X86 (Computers)

```
.tco:
    .string "Hello, World!"

main:
    push rbp
mov rbp, rsp
mov edi, OFFSET FLAT:.LCO
mov eax, 0
call printf
mov eax, 0
pop rbp
ret
```

ARM (Embedded)

```
.LCO:

.ascii "Hello, World!\000"

push {fp, lr}
add fp, sp, #4
ldr r0, .L3
bl printf
mov r3, #0
mov r0, r3
pop {fp, pc}

.L3:
.word .LC0
```

- High Level Languages
 - C, C++

Source code in C

```
#include <stdio.h>
int main() {
    printf("Hello, World!");
    return 0;
}
```

Physical to Abstract

- Memory (RAM) matrices of + or charged 'cells' ← 1's and 0's (binary)
- Processor reads the + and cells like a map or instruction booklet ← 1's and 0's as data or instructions

Physical

Logical/Abstract

- Machine code instructions can be represented in hexadecimal
- Hexadecimal can be disassembled(translated) to assembly language
- Assembly is the compiled result of human readable code, ie higher level languages like C and C++



Birth of a Program

- Human writes something in high level programming language (usually) like C or C++
- Compiler translates human readable code (C, C++) to machine readable (binary op codes) and spits out the end result (.exe, .bin, .dll, etc.)
- Computer runs the assembled instructions in the compiled program

Human Code → Compiler assembles → Machine Code

So what is RE?

Moving backwards:

Machine Code

- → Disassemble
 - → Recreated human code ("decompilation")

• Figuring out how something works in human terms, by looking at the machine code instructions packaged in the binary

Dynamic Analysis

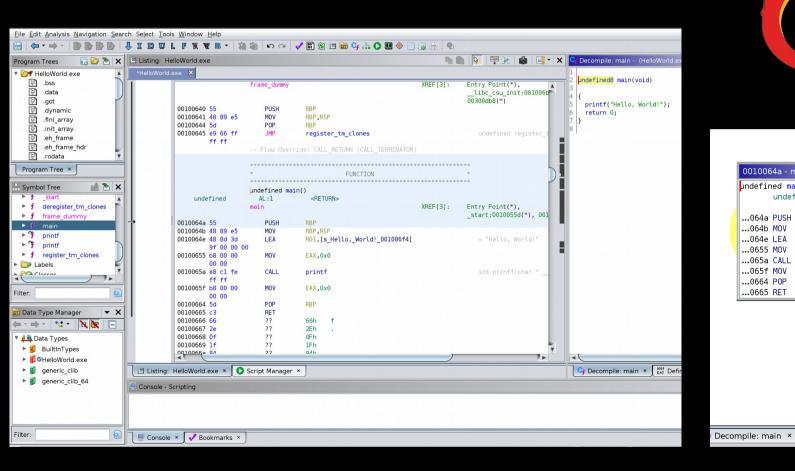
- Take file, run file, poke it with a stick and see what happens. "Vivisection"
- Detonate in a Sandbox → Take file, put in isolated digital space (virtual machine), run file, observe
- Debugger → Take file, run file, attach to it's process and play with it. Can be risky if you're not careful.
- Common Tool Options:
 - WinDBG, Immunity, OllyDBG

Static Analysis

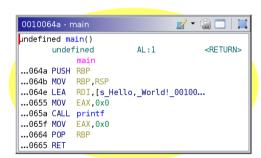
Take file, take file apart. "Dissection"

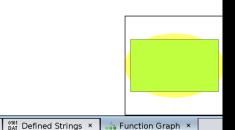
- Disassembler → Binary in, assembly out
- Decompiler → Binary in, higher level code out (usually C)
- Most common tool choices:
 - IDA Pro, Ghidra, Binary Ninja, Radare2

Ghidra



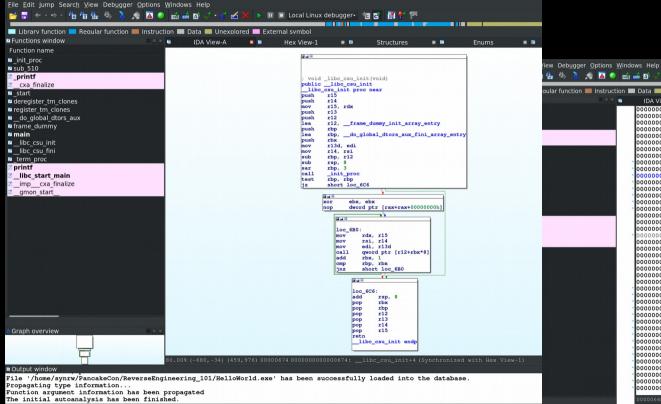












🐁 🦫 🕽 🚜 🔼 🔘 📾 📸 🎁 👉 📲 🗹 🗶 🕨 🔟 🔳 Local Linux debugger- 🐚 🗃 🐩 🚏 gular function 🔳 Instruction 🔳 Data 🔲 Unexplored 🔲 External symbol IDA View-A **8** Hex View-1 B 13 Structures B 1 Enums H 10 000000064A : ----- S II B D O II T I N E -----000000064A 000000064A ; Attributes: bp-based frame 000000064A 00000064A; int __cdecl main(int argc, const char **argv, const char **envp) 000000064A public main 000000064A main ; DATA XREF: _start+1D+o proc near 000000064A push 000000064B rbp, rsp mov 000000064E lea rdi, format ; "Hello, World!" 0000000655 eax, 000000065A call _printf 000000065F mov eax, 0 0000000664 0000000665 retn 00000000665 main endr 0000000665 0000000665 align 10h 0000000670 0000000670 0000000670 0000000670 0000000670 void _libc_csu_init(void) 0000000670 public __libc_csu_init 0000000670 _libc_csu_init proc near ; DATA XREF: _start+16+o 0000000670 push r15 0000000672 push r14 0000000674 mov r15, rdx 0000000677 push r13 0000000679 push r12 000000067B lea r12, __frame_dummy_init_array_entry 0000000682 push rbp 0000000683 lea rbp, __do_global_dtors_aux_fini_array_entry 000000068A push 000000068B mov r13d. edi 000000068E mov r14, rsi 0000000691 sub rbp, r12 0000000694 sub rsp, 8 0000000698 sar rbp, 3 000000069C _init_proc call 00000006A1 test rbp, rbp

Output window

File '/home/synrw/PancakeCon/ReverseEngineering_101/HelloWorld.exe' has been successfully loaded into the database. Propagating type information...

Function argument information has been propagated The initial autoanalysis has been finished.

IDC

AU: idle Down Disk: 1496GB

RE Subfields by Layer

- Software <-> Firmware <-> Hardware spectrum
- General leanings, not hard definitions:
 - Software: Web, Apps, Programs
 - Firmware: Drivers, bootloaders
 - Hardware: Embedded, PCBs
- Super fuzzy overlapping boundaries and definitions that no one agrees on

RE Subfields by Target

- Traditional "Computers"
 - PC, Servers
- Embedded devices
 - Phones, vehicles, medical devices, ICS
- Malware
- Signals & Protocols
 - RF (these people are insane), networks

Questions, comments, or just geeking out?

Contact info:

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Stay Safe, Stay Curious!