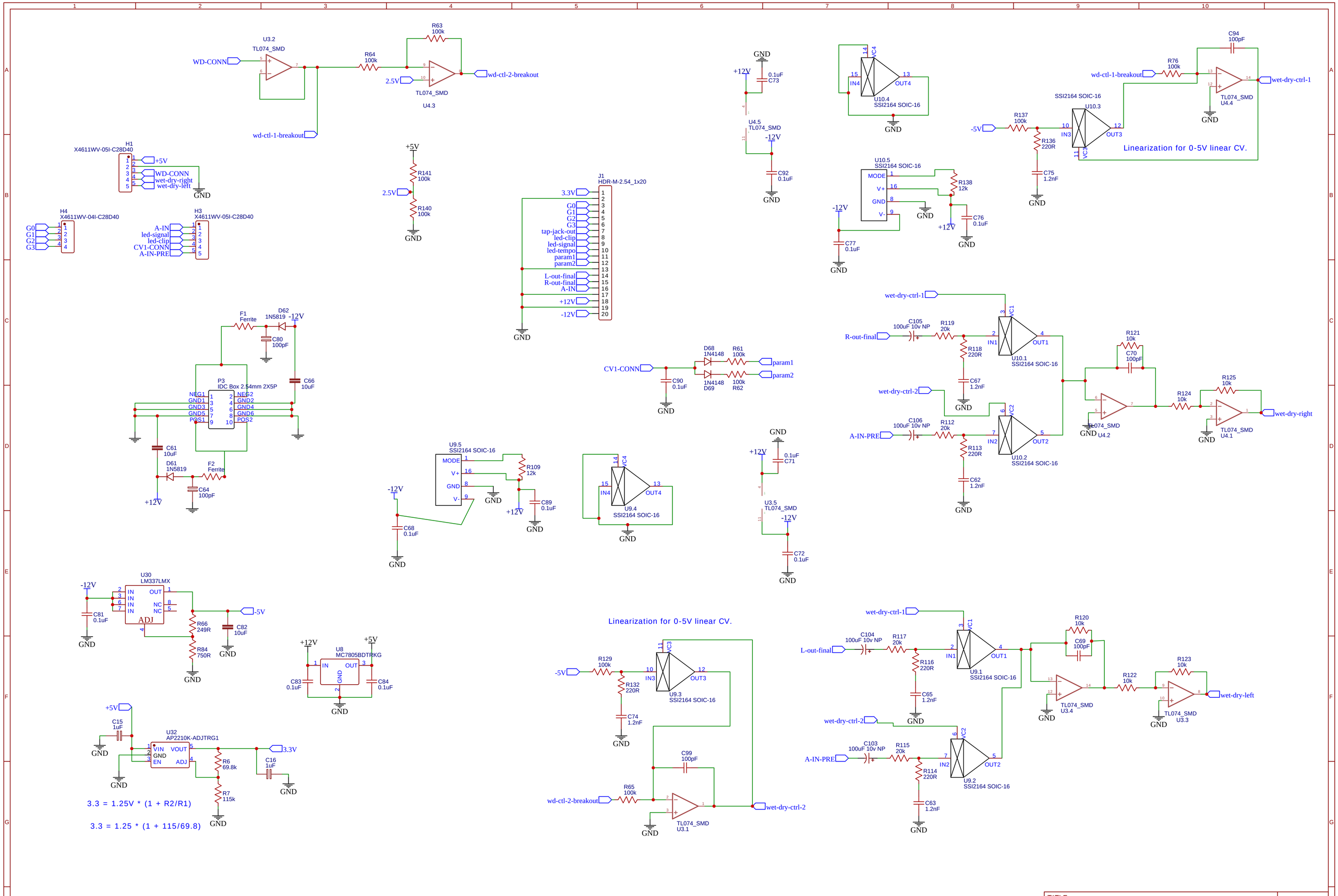
U3.2
TL074_SMD
WD-CONN
R64
100k
R63
100k
2.5V
TL074_SMD
U4.3
wd-ctl-2-breakout
wd-ctl-1-breakout
H1
X4611WV-05I-C28D40
+5V
WD-CONN
wet-dry-right
wet-dry-left
GND
H4
X4611WV-04I-C28D40
G0
G1
G2
G3
H3
X4611WV-05I-C28D40
A-IN
led-signal
led-clip
CV1-CONN
A-IN-PRE
+5V
R141
100k
2.5V
R140
100k
GND
J1
HDR-M-2.54_1x20
3.3V
G0
G1
G2
G3
tap-jack-out
led-clip
led-signal
led-tempo
param1
param2
L-out-final
R-out-final
A-IN
+12V
-12V
GND
F1
Ferrite
D62
1N5819
-12V
C80
100pF
C66
10uF
P3
IDC Box 2.54mm 2X5P
NEG1
GND1
GND3
GND5
POS1
NEG2
GND2
GND4
GND6
POS2
C61
10uF
D61
1N5819
F2
Ferrite
C64
100pF
+12V
CV1-CONN
C90
0.1uF
D68
1N4148
R61
100k
param1
1N4148
D69
100k
R62
param2
GND
U9.5
SSI2164 SOIC-16
MODE
V+
GND
V-
R109
12k
C89
0.1uF
-12V
C68
0.1uF
GND
U9.4
SSI2164 SOIC-16
IN4
OUT4
VC4
+12V
0.1uF
C71
U3.5
TL074_SMD
-12V
C72
0.1uF
GND
U10.4
SSI2164 SOIC-16
IN4
OUT4
VC4
GND
U10.5
SSI2164 SOIC-16
MODE
V+
GND
V-
R138
12k
C76
0.1uF
+12V
-12V
C77
0.1uF
GND
U4.5
TL074_SMD
-12V
C92
0.1uF
GND
C94
100pF
wd-ctl-1-breakout
R76
100k
TL074_SMD
U4.4
wet-dry-ctrl-1
SSI2164 SOIC-16
U10.3
-5V
R137
100k
IN3
OUT3
VC3
R136
220R
C75
1.2nF
Linearization for 0-5V linear CV.
wet-dry-ctrl-1
R-out-final
C105
100uF 10v NP
R119
20k
IN1
OUT1
VC1
R118
220R
C67
1.2nF
U10.1
SSI2164 SOIC-16
wet-dry-ctrl-2
A-IN-PRE
C106
100uF 10v NP
R112
20k
IN2
OUT2
VC2
R113
220R
C62
1.2nF
U10.2
SSI2164 SOIC-16
R121
10k
C70
100pF
TL074_SMD
U4.2
R124
10k
R125
10k
TL074_SMD
U4.1
wet-dry-right
-12V
U30
LM337LMX
IN
OUT
NC
ADJ
C81
0.1uF
R66
249R
R84
750R
C82
10uF
-5V
+12V
U8
MC7805BDTRKG
IN
OUT
GND
C83
0.1uF
C84
0.1uF
+5V
C15
1uF
U32
AP2210K-ADJTRG1
VIN
GND
EN
VOUT
ADJ
R6
69.8k
R7
115k
C16
1uF
3.3V
3.3 = 1.25V * (1 + R2/R1)
3.3 = 1.25 * (1 + 115/69.8)
Linearization for 0-5V linear CV.
-5V
R129
100k
R132
220R
C74
1.2nF
U9.3
SSI2164 SOIC-16
IN3
OUT3
VC3
C99
100pF
R65
100k
wd-ctl-2-breakout
TL074_SMD
U3.1
wet-dry-ctrl-2
wet-dry-ctrl-1
L-out-final
C104
100uF 10v NP
R117
20k
IN1
OUT1
VC1
R116
220R
C65
1.2nF
U9.1
SSI2164 SOIC-16
R120
10k
C69
100pF
TL074_SMD
U3.4
R122
10k
R123
10k
TL074_SMD
U3.3
wet-dry-left
wet-dry-ctrl-2
A-IN-PRE
C103
100uF 10v NP
R115
20k
IN2
OUT2
VC2
R114
220R
C63
1.2nF
U9.2
SSI2164 SOIC-16
TITLE:
nverb4 main board
REV: 2.1
Company: 508: Loop Detected
Sheet: 1/1
Date: 2021-12-15
Drawn By: rossgrady
EasyEDA