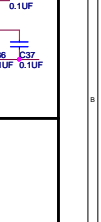
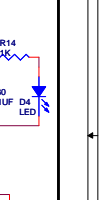
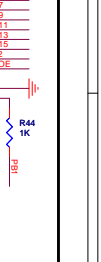
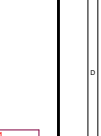
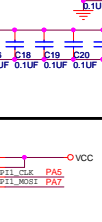
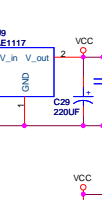
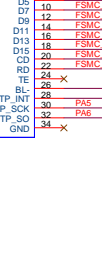
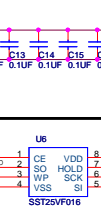
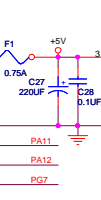
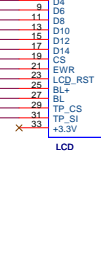
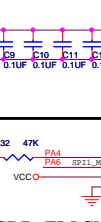
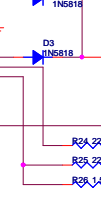
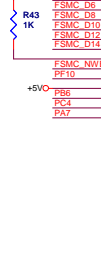
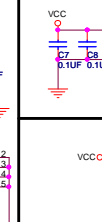
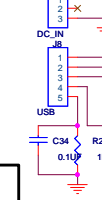
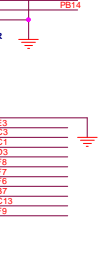
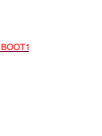
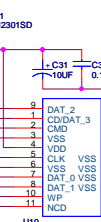
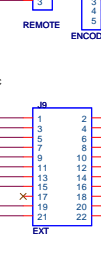
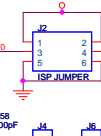
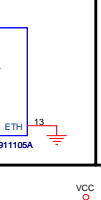
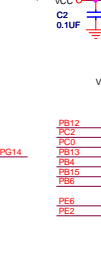
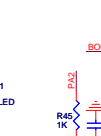
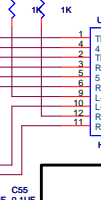
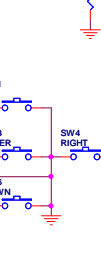
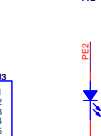
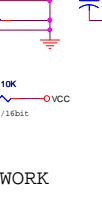
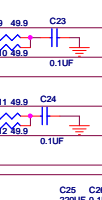
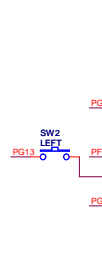
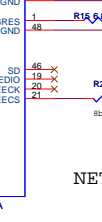
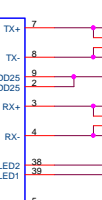
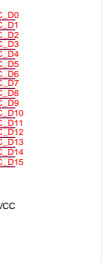
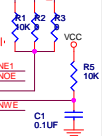
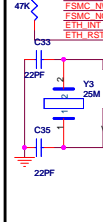
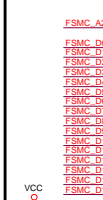
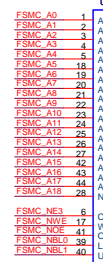
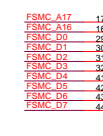
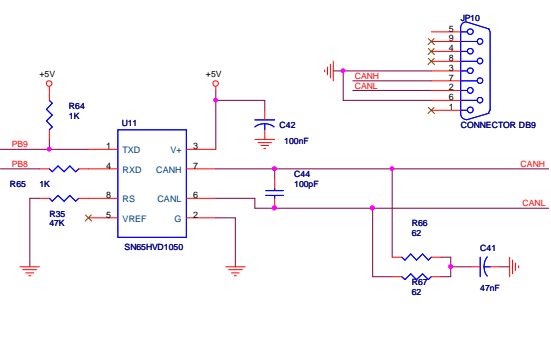
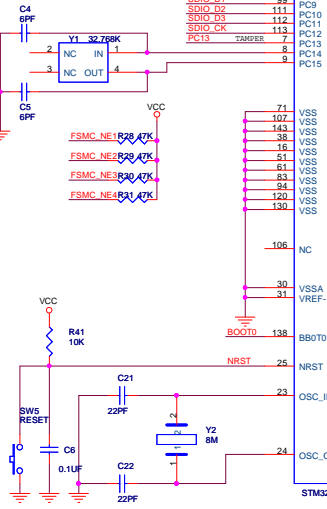
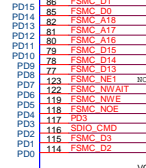
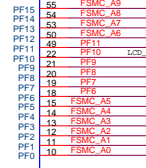
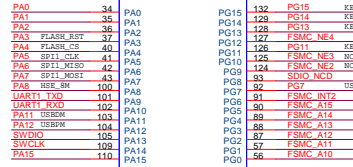
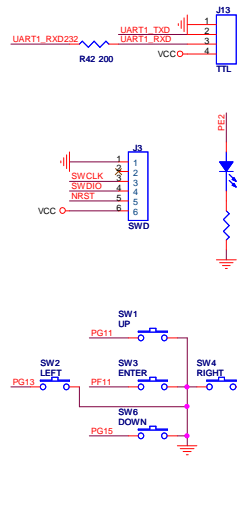


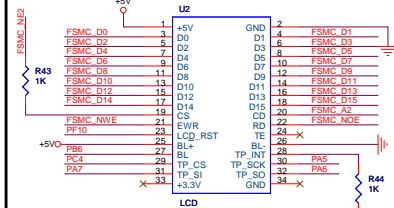
STM32 CORE



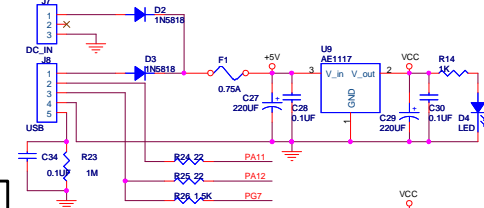
KEY\SWD\ISP..



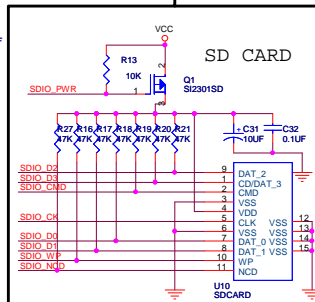
LCD



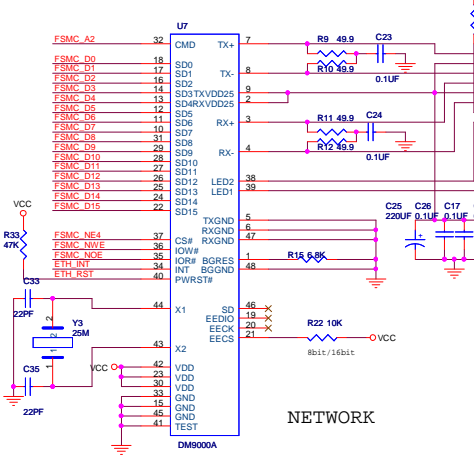
POWER & USB



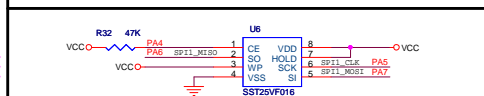
SD CARD



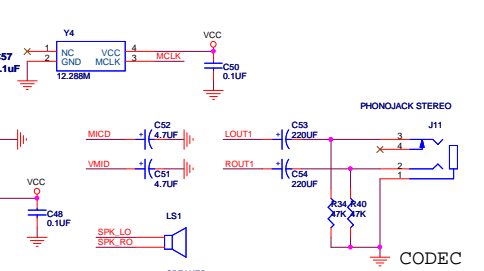
NETWORK



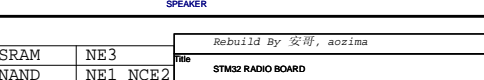
SPI FLASH



PHONOJACK STEREO



CODEC



SRAM	NE3
NAND	NE1 NCE2
LCD	NE2
ETH	NE4

Rebuild By	安哥, azima
File	STM32 RADIO BOARD
Size	Document Number
C	1/1
Date	Sunday, November 21, 2010
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