
EMBER[®] EM35X BREAKOUT BOARD TECHNICAL SPECIFICATION

Silicon Labs' Ember EM35x Breakout Board contains the hardware peripherals for the development and deployment of a low-data-rate, low-power ZigBee application on the EM300 series System-on-Chips (SoCs). The SoC is part of the four-layer (FR4-based) module that connects to the EM35x Breakout Board through the board-to-board connectors. The EM35x Breakout Board hardware stimuli include a temperature sensor, two buttons, a piezo buzzer, two LEDs, and a 2" x 2" through-hole prototyping area. In addition, the EM35x Breakout Board contains a USB transceiver with USB connector, a RS-232 transceiver with DB-9 connector, Data Emulation Interface (DEI), Packet Trace Port programming interface, and regulated power planes. Revision B0 and later of the EM35x Breakout Board include an optional interface to 2 MB external DataFlash in support of the ZigBee OTA Profile for over-the-air (OTA) application bootloader purposes.

You can obtain the EM35x Breakout Board voltage supply from one of four sources: Ember Debug Adapter (ISA3) (through the Packet Trace Port), external VDC supply, USB port, or AAA battery pack. The various voltage supplies offer a degree of flexibility when testing different network topologies.

This document provides the technical specification for the EM35x Breakout Board. It describes the board-level interfaces as well as the key performance parameters. In addition, it provides the necessary information for developers to validate their application designs using the EM35x Breakout Board.

New in This Revision

Additional rebranding for Silicon Labs.

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1 Breakout Board Filters

The EM35x Breakout Board offers:

- Configurable hardware support for application development
 - Temperature sensor (connects to EM35x GPIO)
 - Two buttons (connect to EM35x GPIO)
 - Piezo buzzer (connect to EM35x GPIO)
 - Two LEDs (connect to EM35x GPIO)
- RS-232 transceiver with DB-9 connector for serial communication (with hardware (HW) handshake support)
- USB transceiver with USB connector (Type B)
- 2 MB external DataFlash for ZigBee OTA Profile support
- Control Interface for the EM35x Radio Communications Module (RCM)
 - RCM RESET button
 - Voltage Supply connection (VBRD)
- 2" x 2", 0.1" pitch prototyping area
- 26-pin, 0.1" pitch, dual-row logic-analyzer shrouded connector
- 10-pin, 0.05" pitch, dual-row Packet Trace Port connector
- 12-pin, 0.1" pitch, dual-row, data emulation interface (DEI) with configuration header
- 14-pin, 0.05" pitch, single-row along with a 19-pin 0.05" pitch, single-row, board-to-board connector for the module
- Selection pins for DC power source selection (either external DC power supply, USB, Debug Adapter (ISA3), or AAA battery pack). LEDs indicate which power supply has been selected.
- 2-pin module VDC pin for connection of an ammeter for module current measurements
- 2-pin jumpers for each of the HW application peripherals, buzzer, buttons, piezo, temperature sensor, and LEDs
- 3-pin selection jumpers for connection to the EM35x UART (SC1). The selection jumpers route signals (RXD, TXD, nRTS, and nCTS) to either an USB transceiver, RS-232 transceiver, or allow access to the TTL levels.

Table 1 lists the DC electrical characteristics of the EM35x Breakout Board.

Table 1. DC electrical characteristics

Parameter	Min.	Typ.	Max.	Unit
VDD supply				
External DC Supply (J1 / J32)	4		20 ¹	V
USB Host	4.5	5		V
Debug Adapter (ISA3)	3.1	3.3V	3.5	V
Battery	2.1		3.6	
External DC supply (J3.2)	3.1	3.3	3.5	V
Current draw (peripherals)				
Piezo buzzer			10	mA
Buttons (enabled)			6	mA
Temperature sensor (enabled)			5	mA
Current draw (miscellaneous)				
RS-232 transceiver			4	mA
USB transceiver			15	mA
LDO distribution			10	mA
Operating temperature	0		+ 55	C

¹ Maximum external DC supply voltage is 15 V for revisions B1 and earlier of the EM35x Breakout Board. See document [420-0089-000TS12](#), *Errata EM35x Breakout Board Technical Specification*, for more information.

2 Components

Figure 1 illustrates the components on layer 1 (top side).

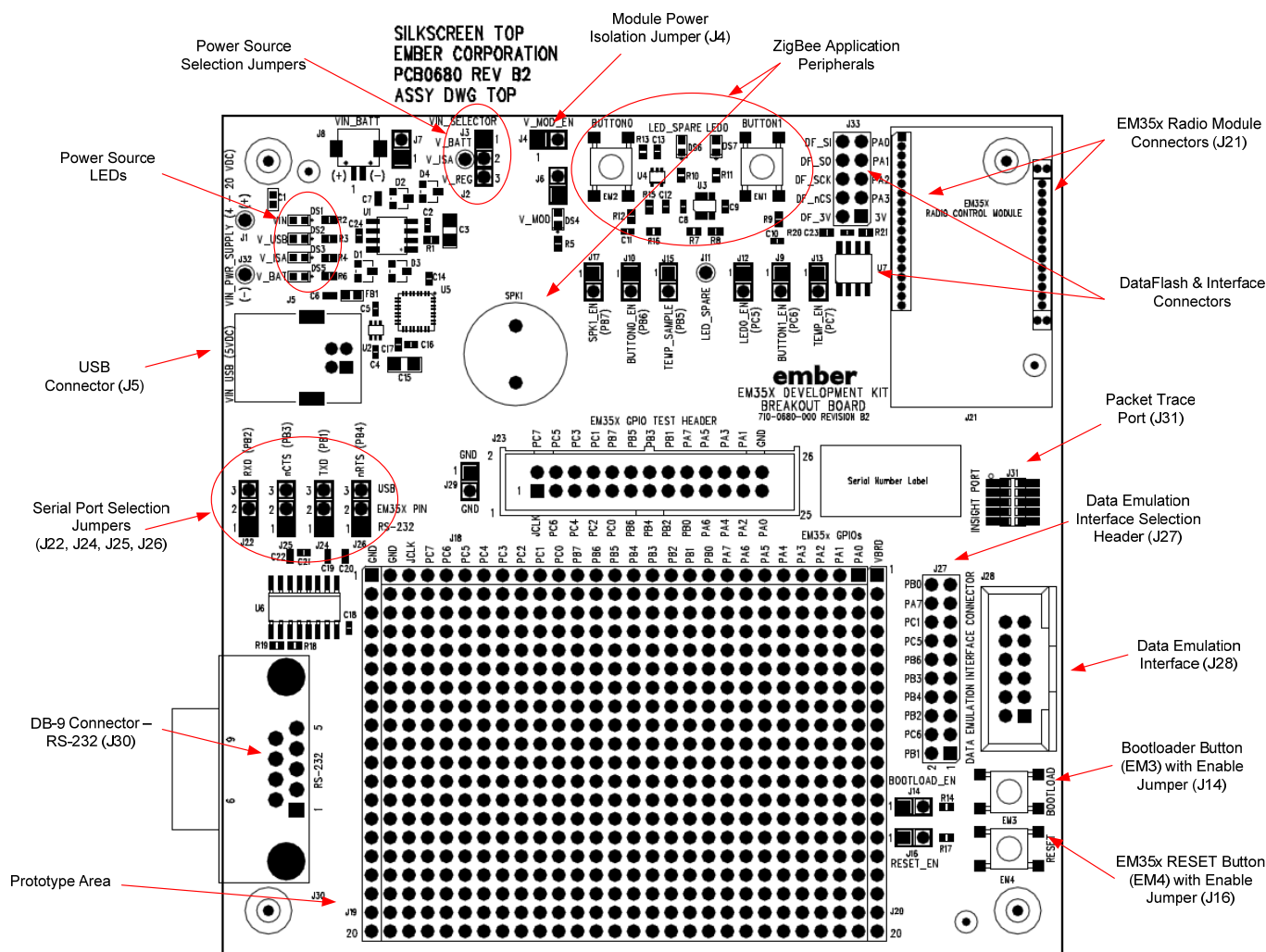


Figure 1. Assembly Print for Layer 1

2.1 Power Supply and Distribution

The EM35x Breakout Board can be powered from one of five sources:

- 4V to 20V External DC Power supply (Positive connected J1 and Ground connected to J32)
- Battery pack connector (J8)
- USB Host (J5, via Wall wart or PC connection)
- Debug Adapter (ISA3) (through Packet Trace Port, J31)
- 2.1 to 3.6V External DC Power supply (Positive connected to J3.2 and Ground connected to J32)

The EM35x Breakout Board contains power source selection jumpers (J2 and J3) which allows only one DC source to power the board. This eliminates the possibility of overcurrent resulting from power supply contention. Table 2 illustrates the connection scheme and LED indication for each power source.

Table 2: Power Supply Connections

Power Source	Selection Scheme (J2 and J3)	LED Indicator
High Voltage External supply (4 V to 20 V) <i>Connect VDD to J1 and GND to J32.</i>		VPS VUSB VISA VBAT
USB Host <i>Connect USB cable to J5.</i>		VPS VUSB VISA VBAT
Debug Adapter (ISA3) <i>Connect ISA3 to J31.</i>		VPS VUSB VISA VBAT
Battery pack <i>Connect AAA battery pack (supplied by Silicon Labs).</i>		VPS VUSB VISA VBAT
Low Voltage External DC supply (3.1 to 3.5) <i>Connect directly to J3.2 with Ground connected to J32.</i>		VPS VUSB VISA VBAT

2.1.1 External DC Power Supply (J1 and J32 or J3.2 and J32)

The EM35x Breakout Board allows two easy to use connections to an external power supply.

- The first connection (Low Voltage) allows for a 3.1 to 3.5 V DC external supply to be connected to J3.2 (positive) and J32 (Ground). The power supply should be able to source up to 250 mA at the set voltage. When using a power supply in this mode, there should be no jumpers on J2 or J3 as shown in Table 2.

- The second connection (High Voltage) allows for a 4 V to 20 V DC external supply to be connected to J1 (positive) and J32 (Ground). The power supply should be able to source up to 300 mA at the set voltage. When using a power supply in this mode, there should be a jumper connecting J3.3 and J3.2 as shown in Table 2.

2.1.2 Battery Connector (J8)

The 2-pin, keyed battery connector (Hirose, P/N: DF13-2P-1.25H(50)) allows for connection to a DC power supply or battery pack. The EM35x Breakout Board is shipped with a 2-AAA battery pack with appropriate mating connector for easy attachment. Batteries are sold separately. When using a battery pack, a jumper must be connected between J3.1 and J3.2 as shown in Table 2.

2.1.3 Packet Trace Port (J8)

The EM35x Breakout Board can also be powered from a Debug Adapter (ISA3). To enable this power supply, simply connect the Debug Adapter (ISA3) to the Packet Trace Port (J8) and connect the power selection jumper between J2 and J3.2 as shown in Table 2. In addition, the Debug Adapter (ISA3) selection toggle switch must be put in the INT position. The Debug Adapter (ISA3) provides a target voltage of 3.3 V and sources as much as 250 mA. See document [420-2040-000TS7](#), *Ember Debug Adapter (ISA3) Technical Specification*, for more details on the Debug Adapter (ISA3).

Note: If the Debug Adapter (ISA3) is connected directly to the Packet Trace Port on the Module, the jumper at J4 must be connected as well as the jumper across J2 and J3.2.

2.1.4 USB Host (J5)

The EM35x Breakout Board can also be powered by a USB Host (PC or Silicon Labs-supplied USB power supply). To operate in this mode, a USB Host must be connected to J5 and the power selection jumper must be connected between J3.2 and J3.3 as shown in Table 2.

2.2 Deep Sleep Testing of the Ember Module

To allow for accurate deep sleep current measurements, the EM35x Breakout Board isolates the module VDD power supply from the regulated power domain on the EM35x Breakout Board. The only connection point between the module power supply and the EM35x Breakout Board supply is through the VMOD_EN header (J4).

By isolating the module power supply in this manner, an ammeter can be placed across J4 to monitor the current sourced to the module. To perform accurate deep sleep measurements, configure the EM35x Breakout Board as follows:

- Remove J4 and place ammeter across this jumper.
- Remove J6 so the V_MOD LED DS4 is not driven. If supplying voltage by J8 battery connector, also remove J7 so the V_BATT LED DS5 is not driven.
- Remove J33 jumpers to isolate DataFlash IC from circuit.
- Issue "shutdown" in nodetest.
- Once command is issued and node is asleep, remove J22 and J24-J26 (UART jumpers).
- Make sure the Packet Trace Port cable, DEI cable, and RS-232 cable are all detached from the EM35x Breakout Board.

This connection scheme offers the highest degree of power supply flexibility. Wake the EM35x from deep sleep by pressing either Button 0 or Button 1.

Note: The use of virtual UART port 4900 is not recommended when interfacing to nodetest for deep sleep testing, because this does not allow for proper configuration of the EM35x for deep sleep measurements. Therefore, use either pass-through UART port 4901, USB, or RS-232 to interface to the nodetest application.

2.3 ZigBee Application Peripherals

As previously mentioned, the EM35x Breakout Board offers six peripherals to assist in ZigBee application development including:

- Temperature sensor
- Two (2) “normally open” buttons
- 4 kHz piezo buzzer
- Two (2) LEDs
- External DataFlash

Each peripheral connects to an EM35x GPIO through a two-pin peripheral header. Because each peripheral header on the EM35x Breakout Board ships with a jumper in place, the peripherals default to “HW Enabled.” If application development does not require the peripheral, simply remove the jumper.

Note: Each peripheral consumes power. Be sure to factor this into the current consumption equations when testing the module in deep sleep mode or if using the battery pack to power the EM35x Breakout Board.

2.3.1 Temperature Sensor (U4)

The temperature sensor is an off-the-shelf component from National Semiconductor (MFG P/N: LM20BIM7). The temperature sensor requires an enable signal to be asserted (active high) prior to generating an analog voltage proportional to the ambient temperature of the EM35x Breakout Board. Therefore, two EM35x GPIO signals, PC7 and PB5, are routed to pin 2 of peripheral headers J13 and J15, respectively.

- PC7 enables the temperature sensor when asserted (active high), when a jumper is installed at J13.
- PB5 contains the analog temperature information from the sensor, when it is enabled and a jumper is installed at J15.

Due to the EM35x ADC voltage reference at 1.2 V, the temperature sensor output is scaled to between 0 and 1.2 V through a resistive voltage divider. If you want to connect a temperature sensor from a different manufacturer, scale the output in a similar manner.

The EM35x Breakout Board is shipped with a jumper installed at J13 and J15. If the jumpers are removed, a different compatible device can be attached to pin 2 of both J13 and J15.

For more information on the temperature sensor, refer to its datasheet (<http://www.ti.com/product/LM20>).

2.3.2 Buttons (EM1, EM2)

Two programmable, normally-open buttons are provided for software debugging and application development. When either button is pressed, the connected net is driven low. A single-pole RC filter minimizes the effects of switching noise.

These buttons map to the backchannel button commands as follows:

- EM2: controlled by the `button 0` command
- EM1: controlled by the `button 1` command

For information about the `button` command, see document ~~429-4030-000~~[UG1110](#), *EM35x Development Kit User Guide*.

Two EM35x GPIO signals, PB6 and PC6, are routed from the EM35x Module to pin 2 of peripheral headers J9 and J10, respectively. In the default configuration of the EM35x Breakout Board, jumpers are positioned across J9 and J10 to enable buttons EM1 and EM2, respectively. If the jumpers are removed, different compatible devices can be attached to pin 2 of breakout headers J9 and J10 instead of the buttons.

2.3.3 Buzzer (SPK1)

A programmable buzzer is provided for software debugging and application development. An EM35x GPIO signal, PB7, is routed to pin 2 of peripheral header J17. In the default configuration of the EM35x Breakout Board, a jumper is positioned across J17 to enable use of the buzzer. The buzzer installed on the EM35x Breakout Board is from CUI (MFG P/N: CEP-1160). For more information on the buzzer, refer to its datasheet (<http://www.cui.com/Product/Resource/PDFRedirect/110/CEP-1160.pdf>).

2.3.4 LEDs (DS6 and DS7)

The EM35x Breakout Board contains two LEDs for software debugging and application development. Each LED is buffered (non-inverting) to allow for connection to any EM35x GPIO. One EM35x GPIO, PC5, is routed to pin 2 of header J12. To turn on DS7 (RED) from the EM35x RCM, install a jumper at J12, configure PC5 as an output and drive it low. To turn on DS6 (GREEN), make a physical connection between an unused EM35x GPIO and J11 (single pin header). Once this is done, then that GPIO must be configured as an output and driven low to illuminate DS6.

2.3.5 External DataFlash (U7)

The external DataFlash is an off-the-shelf component from Atmel (MFG P/N: AT45DB021D-SSH-B). The DataFlash is used in cases where ZigBee OTA Profile application bootloader is required. Note that the DataFlash is included on EM35x Breakout Board revision B0 and later. The DataFlash is connected to the EM35x RCM through jumper block J33. The EM35x Breakout Board is shipped with jumpers installed in a disconnected state at J33. If all five jumpers are installed properly at J33, the external DataFlash connects to the EM35x for application bootloading. Figure 2 illustrates the J33 jumper configuration for the DataFlash interface.

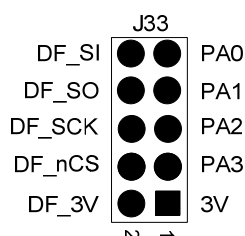


Figure 2. Jumper for DataFlash Connections

For more information on the DataFlash, refer to its datasheet (<http://www.atmel.com/Images/doc3638.pdf>).

2.4 Serial Communication for EM35x SC1 UART

To enhance the software development experience, access to the EM35x SC1 UART is available directly from the EM35x Breakout Board or by telnetting into port 4901 of an ISA3 connected to an Ethernet network. On the EM35x Breakout Board, it is available as RS-232, USB and TTL-compliant signal levels.

To minimize current consumption and allow for the different configuration options, the EM35x Breakout Board individually routes the EM35x SC1 UART signals TXD (EM35x PB2), RXD (EM35x PB1), nRTS (EM35x PB3), and nCTS (EM35x PB4) to pin 2 of headers J22, J24, J25 and J26 respectively. TTL-level access to these UART signals is available at pin 2 of these headers. To route the UART signals to the USB transceiver, connect the jumpers between pins 3 and 2 to each of the headers. Placing the jumpers across pins 1 and 2 route the UART

signals to the RS-232 transceiver. To access the EM35x UART SC1 with an ISA3, remove the jumpers on J22, J24, J25 and J26 and place them on the DEI jumper connector (J27) as summarized below and shown in Figure 3.

- TXD: J27.1 to J27.2
- RXD: J27.5 to J27.6
- nRTS: J27.7 to J27.8
- nCTS: J27.9 to J27.10

Each jumper configuration is shown in Table 3.

Table 3: Serial Communication Selection Jumpers

UART Path	Selection Scheme (J22, J25, J24 and J26)
EM35x SC1 to USB	
EM35x SC1 to TTL	
EM35x SC1 to RS-232	
EM35x SC1 over Debug Adapter (ISA3) <i>Connect DEI cable to J28.</i>	

Note To connect to the EM35x UART over a Debug Adapter (ISA3), the Debug Adapter (ISA3) must be connected to an Ethernet connection. It can be accessed by issuing “Serial 1” within the Console view of the Ember Desktop or by telnetting to Port 4901.

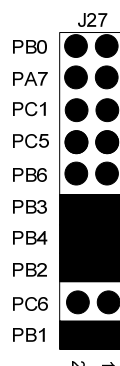


Figure 3. Jumper Settings Required for EM35x SC1 UART Access by Debug Adapter

2.5 Data Emulation Interface (J28)

The 12-pin, dual-row, data emulation interface contains 10 EM35x GPIO signals, as well as voltage (VBRD) and ground (GND) connections. When connected to the Debug Adapter (ISA3), the connector provides additional debug features to software developers.

One feature involves the port 4901 UART connection via Debug Adapter (ISA3). To enable the UART connection to the EM35x UART signals, install four jumpers on J27 as shown in Figure 3.

Another feature involves manipulation of BUTTON0 and BUTTON1 GPIO signals. To enable GPIO manipulation of BUTTON0 and BUTTON1, install jumpers on J27 at PB6 and PC6, respectively.

2.6 EM35x Module Interface Connector (J21)

Two single-row, 0.05" pitch, connectors make up the EM35x module interface to the EM35x Breakout Board. In addition, two single-row, guide connectors assist with connecting the EM35x module to the EM35x Breakout Board. The board-to-board connector scheme allows access to all EM35x GPIO as well as nRESET and the JCLK signals. The connector is illustrated in Figure 4, while the dimensions are listed in Figure 5.

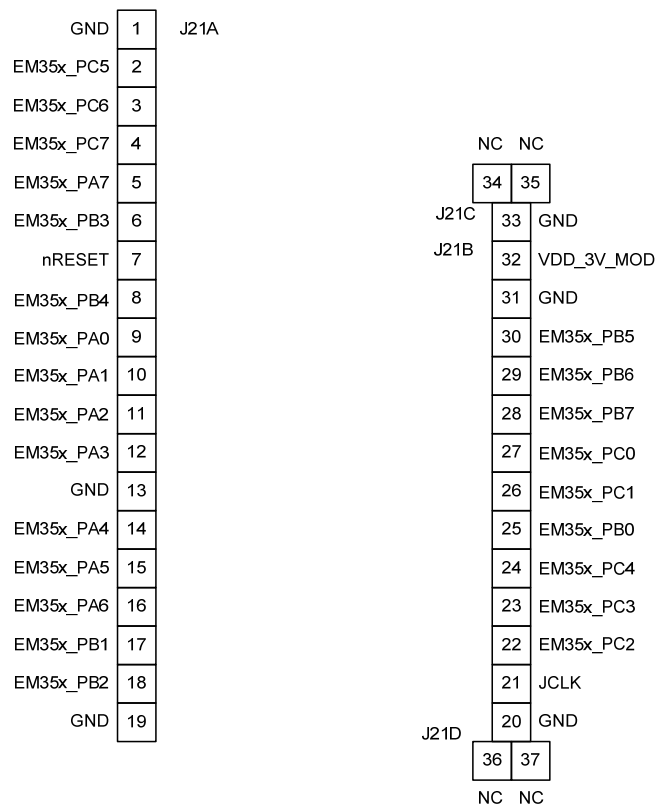


Figure 4. Board-to-Board Connector for the EM35x Module

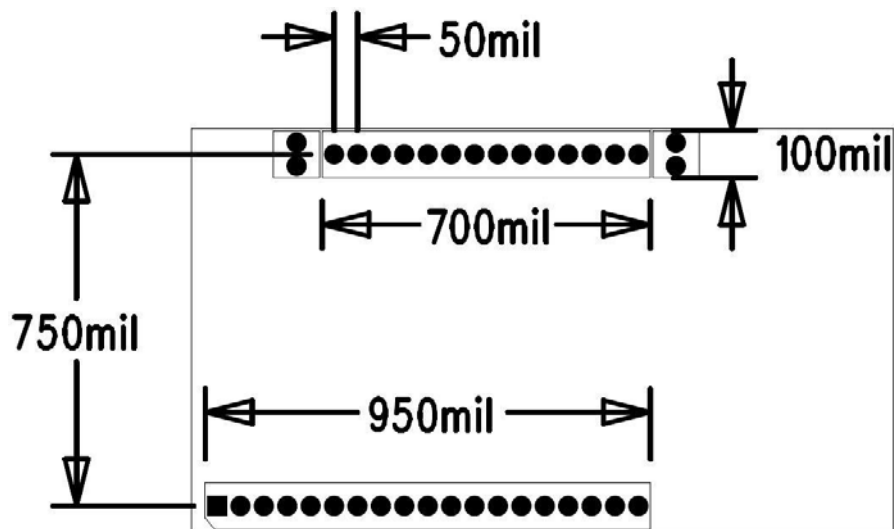


Figure 5. Board-to-Board Connector Dimensions for the EM35x Module

Table 4 describes the pinout and signal names at both J21. The EM35x GPIOs are exposed on the EM35x Breakout Board at the 26-pin, dual row, 0.1" pitch GPIO connector (J23) for application development. For more information on the alternate functions of the GPIO connector, refer to document 120-035X-000, *EM35x Data Sheet*.

Table 4. Pinout and Signal Names of the Interface Connector

Pin #	Signal name	Direction ²	Connector	Description
1	GND	Power	J21A	Ground Connection
2	PC5	I/O	J21A	EM35x GPIO
3	PC6	I/O	J21A	EM35x GPIO
4	PC7	I/O	J21A	EM35x GPIO
5	PA7	I/O	J21A	EM35x GPIO
6	PB3	I/O	J21A	EM35x GPIO
7	nRESET	I/O	J21A	Active low chip reset (internal pull-up on EM35x)
8	PB4	I/O	J21A	EM35x GPIO
9	PA0	I/O	J21A	EM35x GPIO
10	PA1	I/O	J21A	EM35x GPIO
11	PA2	I/O	J21A	EM35x GPIO
12	PA3	I/O	J21A	EM35x GPIO
13	GND	Power	J21A	Ground connection
14	PA4	I/O	J21A	EM35x GPIO
15	PA5	I/O	J21A	EM35x GPIO
16	PA6	I/O	J21A	EM35x GPIO
17	PB1	I/O	J21A	EM35x GPIO
18	PB2	I/O	J21A	EM35x GPIO
19	GND	Power	J21A	Ground connection
20	GND	Power	J21B	Ground connection
21	JCLK	Input	J21B	JTAG interface, serial clock
22	PC2	I/O	J21B	EM35x GPIO
23	PC3	I/O	J21B	EM35x GPIO
24	PC4	I/O	J21B	EM35x GPIO
25	PB0	I/O	J21B	EM35x GPIO
26	PC1	I/O	J21B	EM35x GPIO
27	PC0	I/O	J21B	EM35x GPIO
28	PB7	I/O	J21B	EM35x GPIO
29	PB6	I/O	J21B	EM35x GPIO
30	PB5	I/O	J21B	EM35x GPIO
31	GND	Power	J21B	Ground connection
32	VDD	Power	J21B	2.1 to 3.6V Module Power Domain
33	GND	Power	J21B	Ground connection
34	NC	N/A	J21C	Not connected; guide pin
35	NC	N/A	J21C	Not connected; guide pin
36	NC	N/A	J21D	Not connected; guide pin
37	NC	N/A	J21D	Not connected; guide pin

² with respect to the RCM

2.7 Prototyping Area

The 2.8" x 2" (0.1" pitch) prototyping area on the EM35x Breakout Board offers software developers an extra degree of flexibility. As shown in Figure 4, it allows access to VBRD, GND, and each of the 24 EM35x GPIOs. Therefore, you can solder any sensor or input device to the prototyping area and connect it to the EM35x GPIO for development and debugging.

As shown in [Figure 6](#), the leftmost column is connected to GND and the rightmost column to VBRD. The top row is connected to the EM35x GPIOs. Included in the top row are additional GND and JCLK connections. The remainder of the array is available for application development.

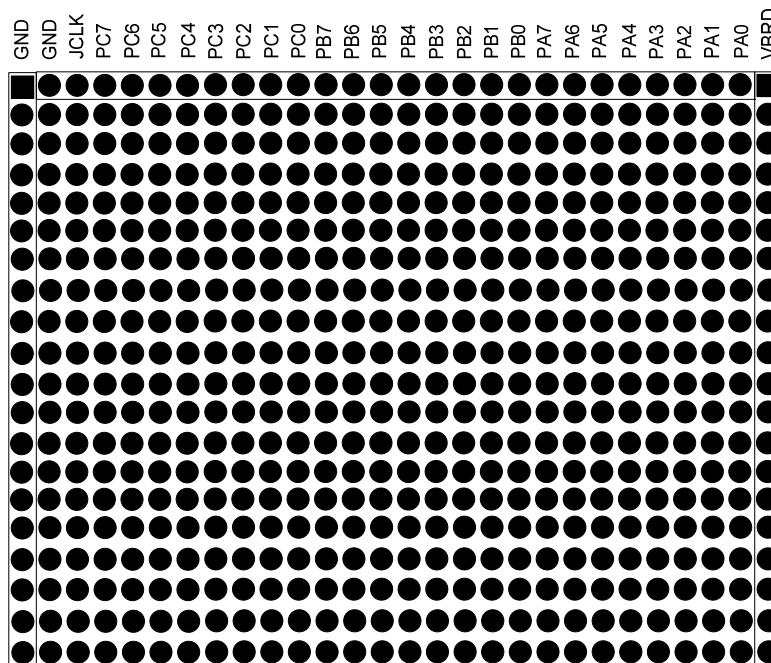


Figure 6. EM35x Breakout Board Prototyping Area

3 EM35x Breakout Board Schematic

The EM35x Breakout Board schematic is included at the end of this document.

CONTACT INFORMATION

Silicon Laboratories Inc.

400 West Cesar Chavez
Austin, TX 78701
Tel: 1+(512) 416-8500
Fax: 1+(512) 416-9669
Toll Free: 1+(877) 444-3032

Please visit the Silicon Labs Technical Support web page for ZigBee products:
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Patent Notice

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EM35x Development Kit Breakout Board

Sheet 1 - Cover Sheet


Sheet 2 - Power Management, Manufacturing

Sheet 3 - Applications Circuits, Breadboard

Sheet 4 - EM35x RCM Connectors, User Interfaces, DataFlash

Sheet 5 - EM358-Specific USB and ETM Circuitry

Sheet 6 - Notes



SILICON LABS
25 Thomson Place, 2nd Floor
Boston, MA 02210, USA
www.silabs.com
617-951-0200

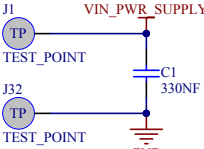
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Title: EM35x Development Kit Breakout Board	
Part Number: 130-0680-000	Rev: C1
Sheet Name: 0680_1_Cover.SchDoc	
Date: 9/30/2012	Time: 5:25:58 PM
Sheet: 1	of 6
Size: B	

POWER SUPPLY, DC, 4V to 20V

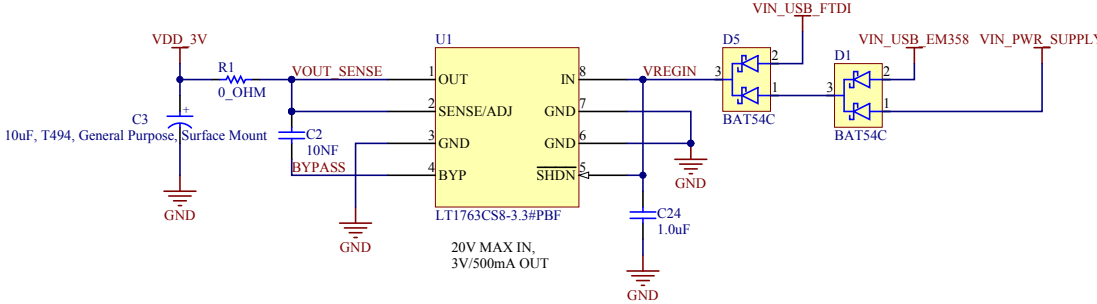
* Place J1 close to J32



FTDI USB / EM358 USB / VIN_PWR_JACK DOWN CONVERSION

U1 Regulator Power Priority:

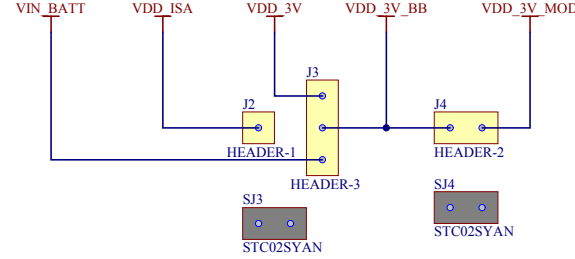
- VIN_USB_FTDI has priority over VIN_USB_EM358
- VIN_PWR_SUPPLY has priority over both VIN_USB_FTDI and VIN_USB_EM358
- If VIN_USB_EM358 is the only power source, the EM358 is self-powered
- Power selection jumper J2/J3 still determines source of power for VDD_3V_BB and VDD_3V_MOD



BREAKOUT BOARD AND MODULE POWER SELECTOR

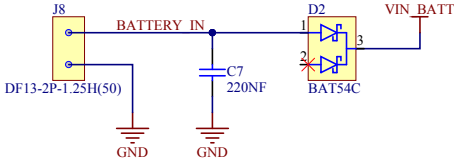
Headers J2 and J3 allow the user to select the power source. There are five (5) possible power sources for the Breakout Board:

1. External 3V Supply (connect directly to J3.2)
2. External Regulated Supply from 4V to 20V (connect shorting jumper between J3.2 and J3.3)
3. Ember battery pack with 2 AAA (connect shorting jumper between J3.1 and J3.2)
4. USB Bus Powered (connect shorting jumper between J3.2 and J3.3)
5. InSight Adapter (connect shorting jumper between J2 and J3.2)

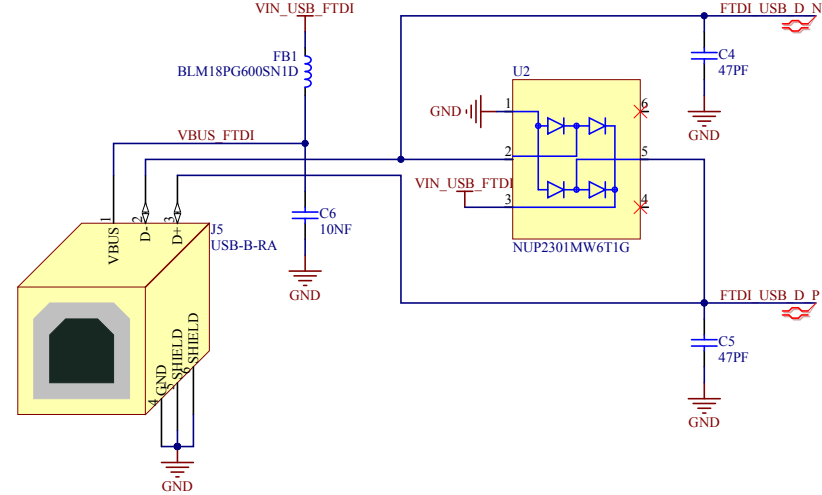


The shorting jumper at J4 can be replaced with a current meter to measure module current draw

BATTERY CONNECTOR (2 AAA), 1.5V to 3.1V

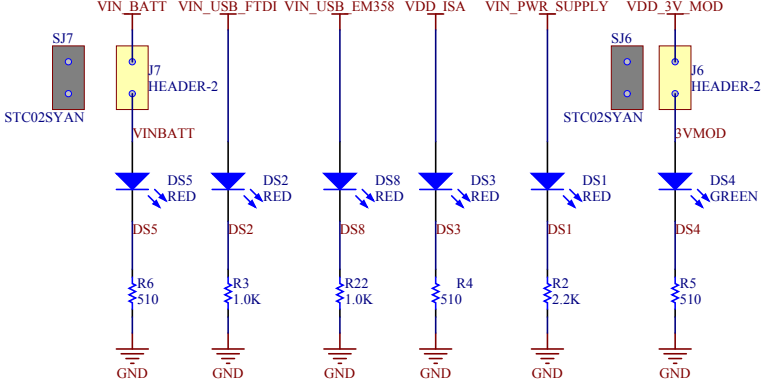


USB FTDI UART CONNECTION (TYPE B) 5V



POWER INDICATOR LEDS

* Remove J6 when performing a Deep Sleep Test



PCB INFORMATION

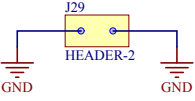
Top Side
Board Logo
PCB1
710-0680-000
SN Bar Code Label
SN1
130-0680-000

FIDUCIALS

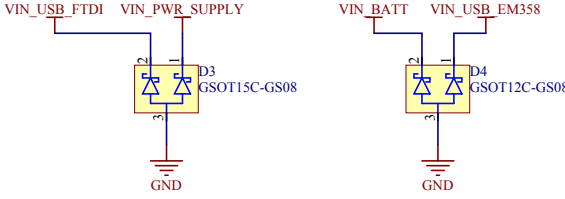


TEST EQUIPMENT GND CONNECTION

* Place J29 close to J23



ESD SUPPRESSION (POWER SUPPLY)



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Title: EM35x Development Kit Breakout Board

Part Number: 130-0680-000

Rev: C1

Sheet Name: 0680_2_Power_Mfg_SchDoc

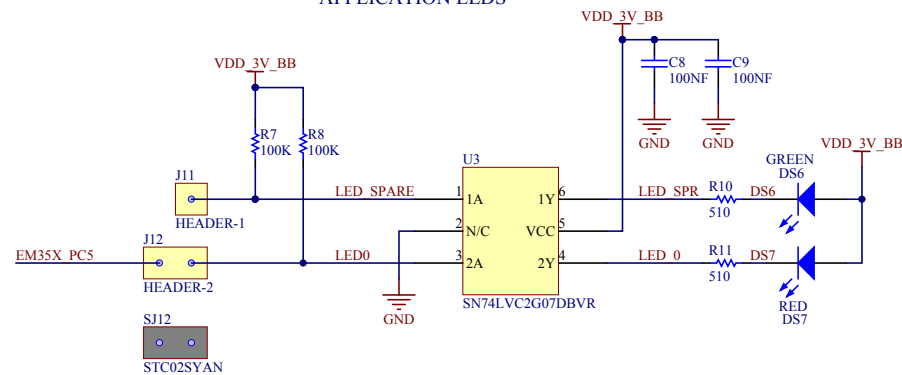
Date: 9/30/2012

Time: 5:25:58 PM

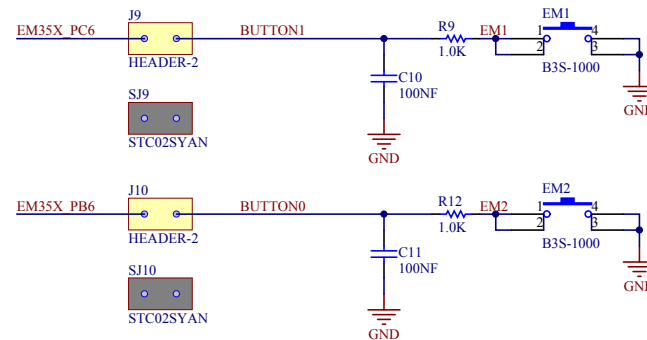
Sheet: 2 of 6

Size: B

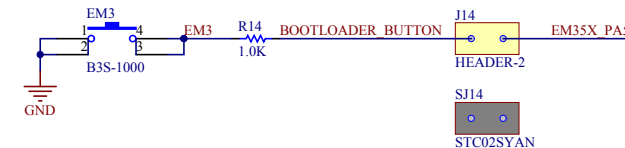
APPLICATION LEDS



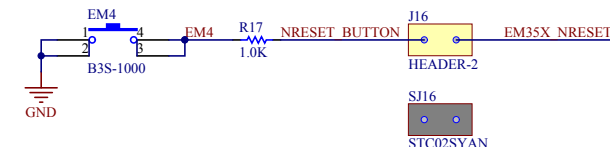
APPLICATION BUTTONS



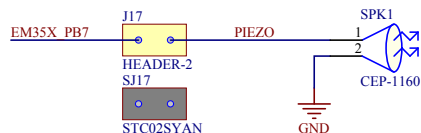
EM35X BOOTLOADER BUTTON



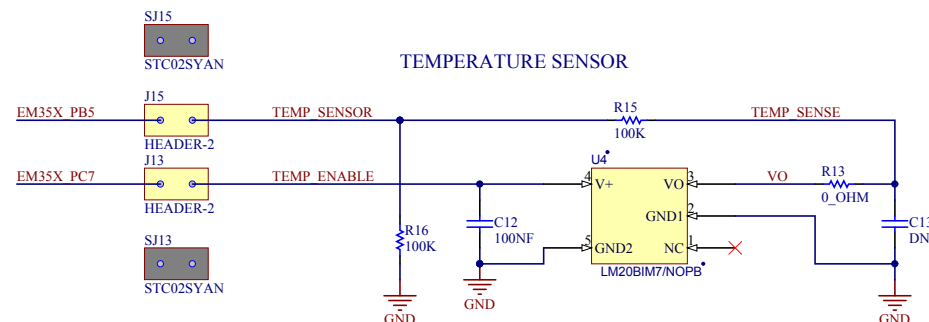
EM35X RESET BUTTON (ACTIVE LOW)



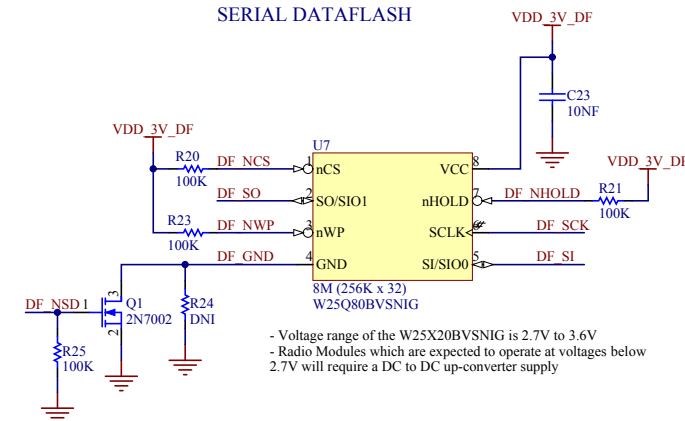
PIEZO SPEAKER



TEMPERATURE SENSOR

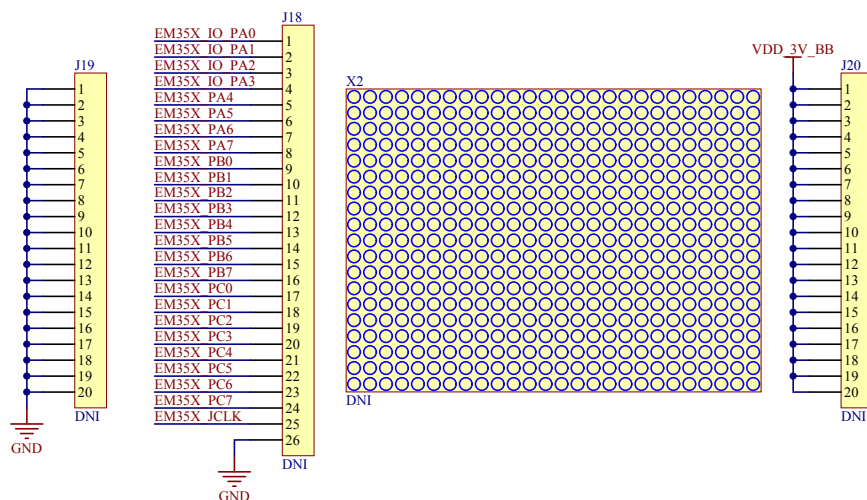


SERIAL DATAFLASH



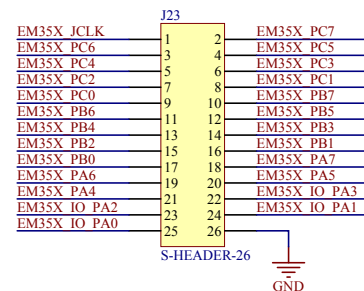
PERIPHERAL SCRATCH PAD (0.1" PITCH)

* Note: J18 EM35X_PA0 through EM35X_PA3 are switched IO not available when EM358 USB is enabled via J40

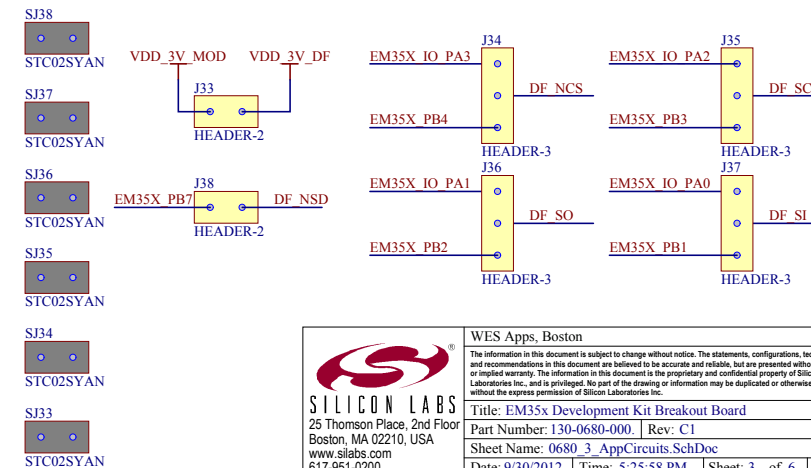


26-PIN DUAL ROW TEST CONNECTOR

* Note: J23 EM35X_PA0 through EM35X_PA3 are switched IO not available when EM358 USB is enabled via J40

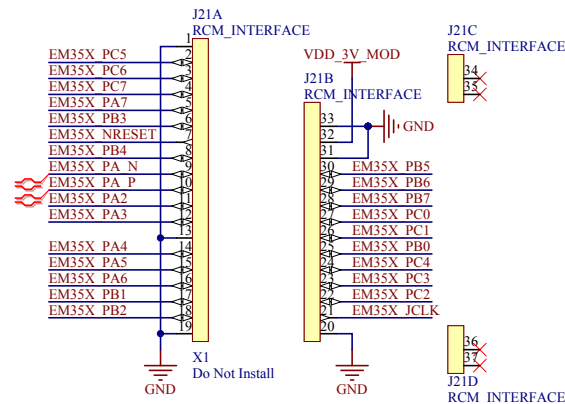


SERIAL DATAFLASH SC1/SC2 SELECTION



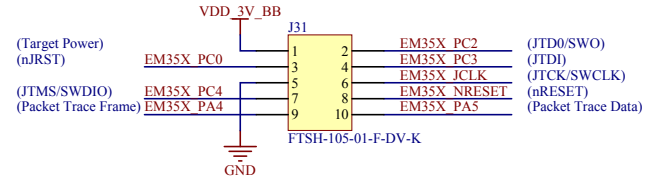
WES Apps, Boston	
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Title: EM35x Development Kit Breakout Board	
Part Number: 130-0680-000 Rev: C1	
Sheet Name: 0680_3_AppCircuits.SchDoc	
Date: 9/30/2012	Time: 5:25:58 PM
Sheet: 3	of 6
Size: B	

EM35X RCM INTERFACE HEADERS

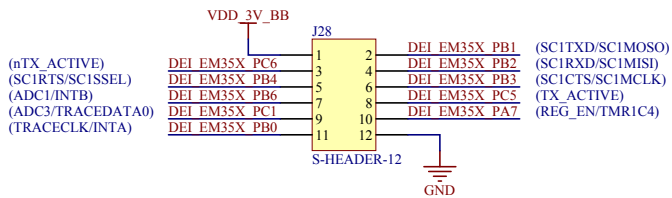


* EM35X_PA_N is PA0 (USB_DM), EM35X_PA_P is PA0 (USB_DP)

INSIGHT PORT (ISP)

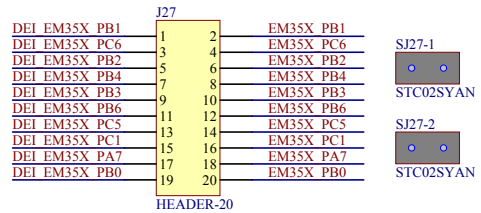


EM35X DEI CONNECTOR

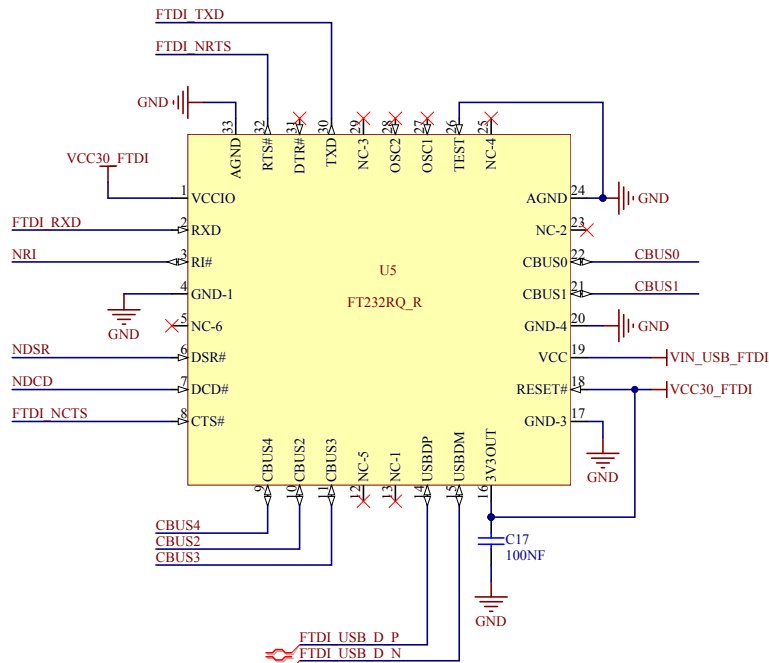


EM35X DEI HEADER

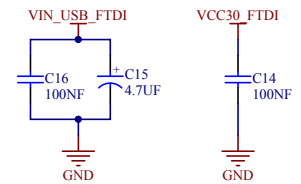
* Place near J28



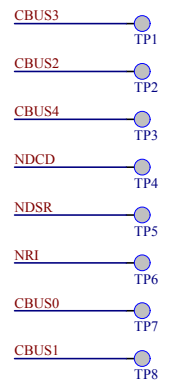
USB / UART IC - EM35X SC1 UART INTERFACE



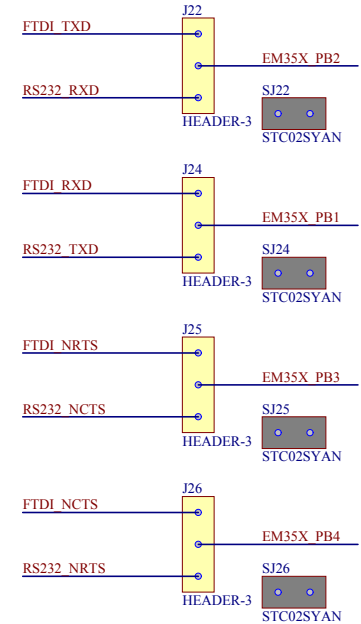
USB IC DECOUPLING



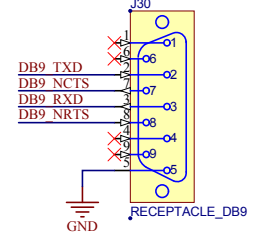
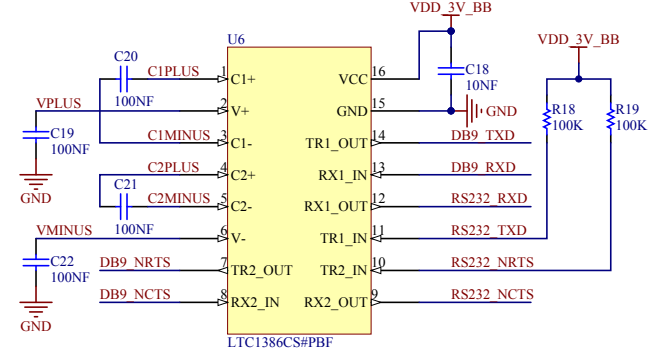
FTDI GPIO TEST POINTS



USB/RS-232 SELECTOR



TTL TO RS-232 TRANSCEIVER



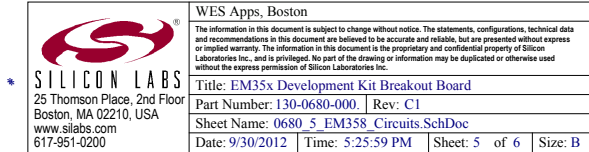
A

B

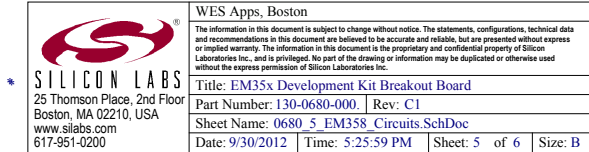
- C



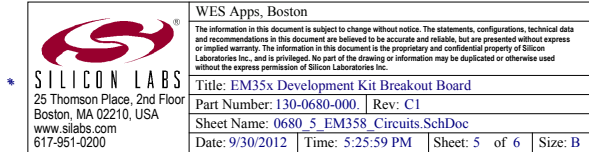
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	Part: EM35x Development Kit Breakout Board			
	Title Number: 130-0680-000		Rev: C1	
	Sheet Name: 0680 5 EM358S Circuits.SchDoc			
	Date: 9/30/2012	Time: 5:25:59 PM	Sheet: 5 of 6	Size: B

	1	2	3	4	5	6
A	SCHEMATIC NOTES:		PCB LAYOUT NOTES:			
	-- Version A0 -- *Released: December 8, 2009 *Initial version released, Version A0 (initial draft)		-- Version A0 -- *Released: December 8, 2009 *Initial version released, Version A0 (initial draft)			
	-- Version B0 -- *Released: January 13, 2010 *Changes from A0 include: 1. Added U7, DataFlash 2. Added C23, 10nF 3. Added R20, 0 Ohm 4. Added R21, 0 Ohm 5. Added J33, 10 pin shorting plug header 6. Added SJ19 - SJ23, Shorting Plugs		-- Version B0 -- *Released: January 13, 2010 *Changes from A0 include: 1. Added U7, DataFlash, plus routes 2. Added C23, 10nF, plus routes 3. Added R20, 0 Ohm, plus routes 4. Added R21, 0 Ohm, plus routes 5. Added J33, 10 pin shorting plug header, plus routes 6. Changed silkscreen text at J22 to display "RXD (PB2)" 7. Changed silkscreen text at J24 to display "TXD (PB1)" 8. Changed silkscreen text at J25 to display "nCTS (PB3)" 9. Changed silkscreen text at J26 to display "nRTS (PB4)" 10. Reduced size of Ember logo, Moved it and rotated it 90 degrees			
B	-- Version B2 -- *Released: August 19, 2011 *Changes from B0 schematic and B1 BOM include: 1. Added C24 1uF cap to U1 regulator, incorporating ECO C00273 into design 2. Changed D3 from GSOT12C to GSOT15C 3. Updated shorting jumper notes 4. Updated C3 to a lower ESR cap for better regulator stability		-- Version B2 -- *Released: August 19, 2011 *Changes from B0 layout and B1 BOM include: 1. Added bump-on outlines for bottom-side assembly 2. Added C24 1uF cap to U1 regulator 3. Updated drill sizes for some through-hole decals 4. Updated DB-9 connector footprint from vertical to right-angle 5. Fixed board outline dimensioning error			
C	-- Version C0 -- *Released: September 20, 2012 *Changes from B2 schematic and B3 BOM include: 1. Recreated schematic in Altium Designer. 2. Removed MH1-4, since rubber bump-ons used instead of stand-offs. 3. Added dedicated USB connector for EM358 USB. 4. Added support for external DataFlash on SC1 as well as SC2. 5. Added USB switches for PA0-3 switching between USB mode and IO/DF mode. 6. Replaced Atmel AT45 DataFlash with 8MBit WinBond DataFlash W25Q80BVSNIg. 7. Added DataFlash Shutdown control circuit, controlled by EM35X_PB7. 8. Added VIN_USB_EM358 power path through regulator and power LED. 9. Various net name changes to distinguish between FTDI USB and EM358 USB. 10. Added ARM Compatible ETM header.		-- Version B3 -- *Released: August 27, 2012 *Changes from B2 layout and B2 BOM include: 1. Replaced Ember logo with Silicon Labs logo			
	-- Version C1 -- *Released: September 29, 2012 *Changes from C0 include: 1. Fixed error with R29 PA2 pull-up control. 2. Added decoupling caps to U9 and U10. 3. Updated J41 to correct part (610-0097-000).		-- Version C0 -- *Released: September 20, 2012 *Changes from B3 layout and B3 BOM include: 1. Imported layout from PADS into Altium Designer. 2. Removed MH1-4, since rubber bump-ons used instead of stand-offs. 3. Added dedicated USB connector for EM358 USB. 4. Added support for external DataFlash on SC1 as well as SC2. 5. Added USB switches for PA0-3 switching between USB mode and IO/DF mode. 6. Replaced Atmel DataFlash with WinBond DataFlash W25Q80BVSNIg. 7. Added DataFlash Shutdown control circuit. 8. Added BAT54C for EM358 USB power. 9. Added VIN_USB_EM358 power LED. 10. Added ARM Compatible ETM header.			
D			-- Version C1 -- *Released: September 29, 2012 *Changes from C0 include: 1. Fixed error with R29 PA2 pull-up control. 2. Added decoupling caps to U9 and U10. 3. Added SST outline to U1. 4. Fixed SSB polarity issue with J5. 5. Moved peripheral headers away from DataFlash headers. 6. Moved buttons and LEDs closer together to make more room for DataFlash and USB_EN headers.			
				<div><div><div><div>SILICON LABS</div><div>25 Thomson Place, 2nd Floor Boston, MA 02210, USA www.silabs.com 617-951-0200</div></div><div><div>WES Apps, Boston</div><div>The information in this document is subject to change without notice. The statements, configurations, technical data and recommendations in this document are believed to be accurate and reliable, but are presented without express or implied warranty. The information in this document is the proprietary and confidential property of Silicon Laboratories Inc., and is privileged. No part of this drawing or information may be duplicated or otherwise used without the express permission of Silicon Laboratories Inc.</div><div><div>Title: EM35x Development Kit Breakout Board</div><div>Part Number: 130-0680-000 Rev: C1</div><div>Sheet Name: 0680_6_Notes.SchDoc</div><div>Date: 9/30/2012 Time: 5:25:59 PM Sheet: 6 of 6 Size: B</div></div></div></div></div>		
1	2	3	4	5	6	