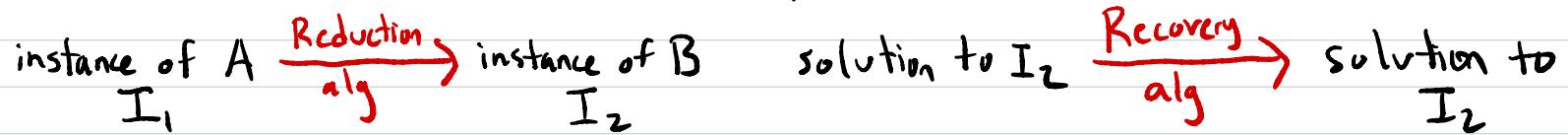


Reductions II

Admin Corner

Reductions Recap

Def : "A reduces to B" aka $A \leq_p B$ if



Thm:

- Zero-Sum \leq_p LP
- Rukrata Cycle \leq_p Min-TSP

Reduction notes

- Reduction & Recovery algs must be efficient (poly-time)
- $A \leq_p B$ and $B \leq_p C \Rightarrow A \leq_p C$
- Can prove $A \leq_p B$, even if A & B not known to be efficient
- "A \leq_p B" and "A reduces to B" most confusing thing in Algos

#1 Most Common Mistake:

"Prove" $A \leq_p B$ by taking

Instance
of B

reduction

Instance
of A

XX

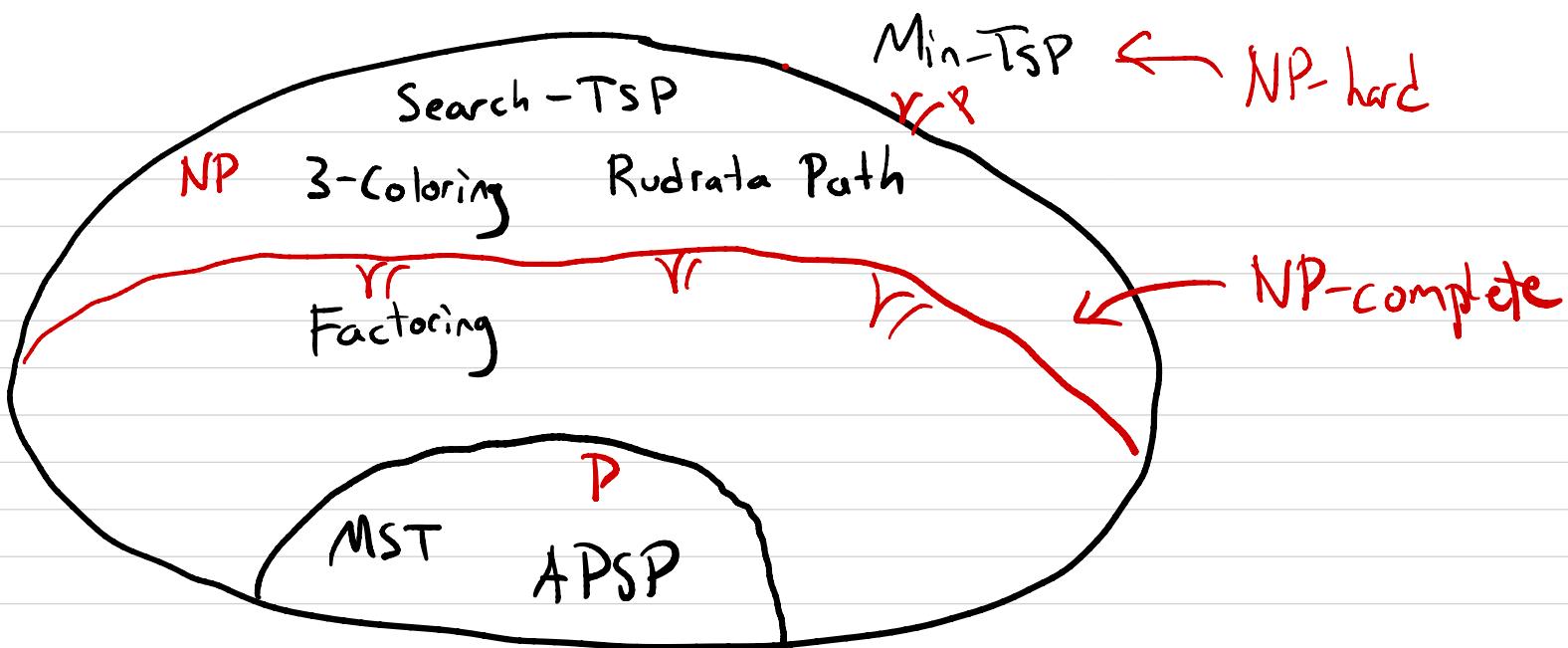
Quiz: We have seen

$A = LP$, $B = \text{Max Flow}$
 $A = \text{Max Flow}$, $B = \text{Bipartite matching}$

$A \leq_p B$

$B \leq_p A$

X



Def: A problem A is **NP-hard** if every problem $B \in \text{NP}$ reduces to A
 $(B \leq_p A)$

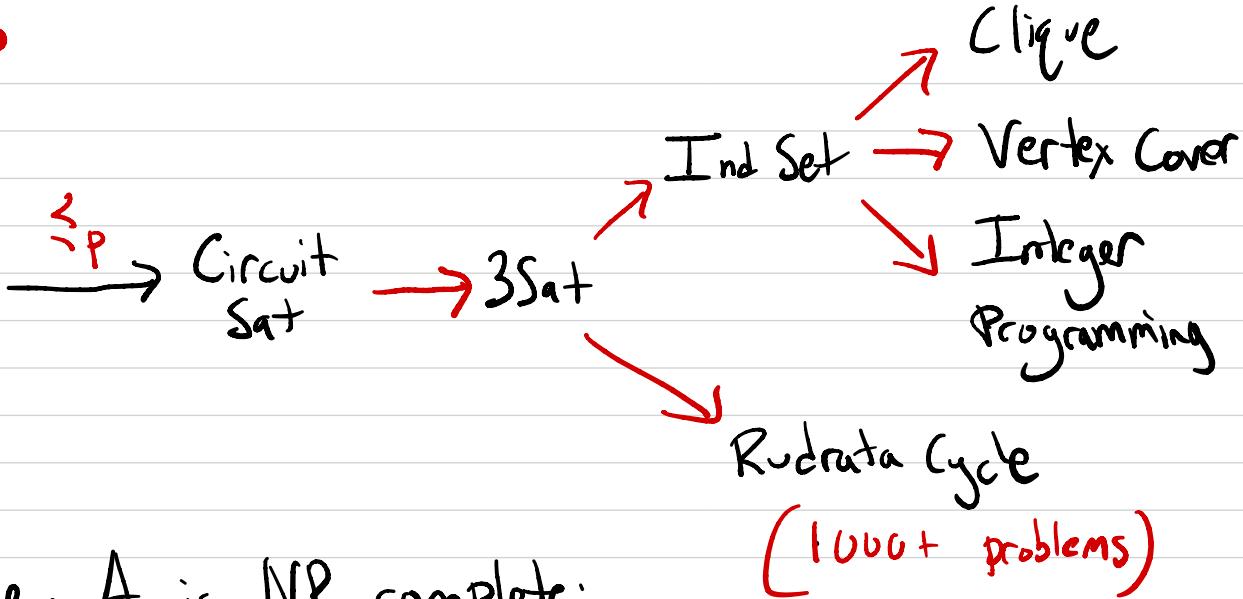
Def: A is **NP-complete** if $A \in \text{NP}$ & A is NP-hard.

Fact: If A & B are NP-complete, $A \leq_p B$ and $B \leq_p A$.

Fact: \exists poly-time alg for NP-complete problem $\Rightarrow P = NP$.

NP-completeness

Every problem
in NP



To show Problem A is NP-complete:

1.) $A \in \text{NP}$ (show verification algorithm)

2.) Pick some (usually well-known) NP-complete problem B.
and show $B \leq_p A$. (Example: $3\text{Sat} \leq_p A$)

Circuit Sat

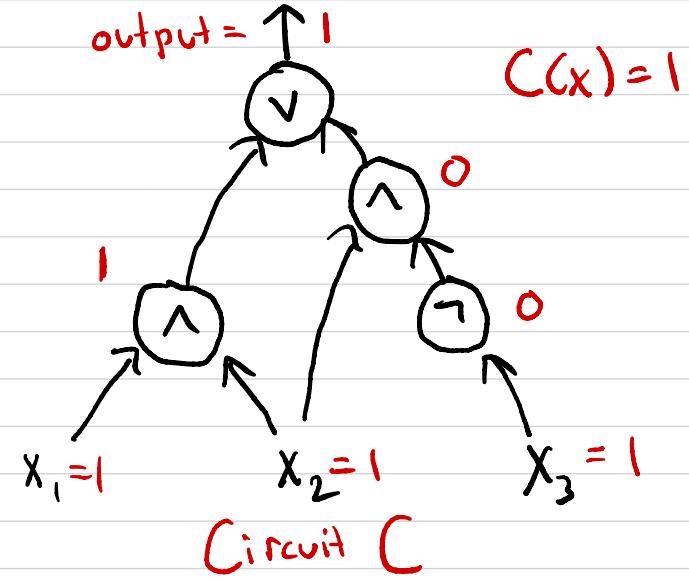
Def: A Boolean circuit is a directed acyclic graph (DAG)

- input nodes x_1, \dots, x_n
- one output node
- gates marked
AND, OR, NOT

Input: A circuit C

w/ n inputs and m gates

Solution: An assignment $x \in \{0, 1\}^n$
s.t. $C(x) = 1$.



Cook-Levin Theorem: Circuit Sat is NP-complete

$\text{3Sat} = \underline{\text{THE}}$ NP-complete problem

Input: 1.) n Boolean variables $x_1, \dots, x_n \in \{0, 1\}$

2.) $m \leq 3$ -variable clauses $(x_1 \vee \overline{x}_2 \vee x_3)$

$$\wedge (x_5 \vee \overline{x}_6 \vee \overline{x}_9)$$

$$\wedge (x_7 \vee x_{10}) \wedge \dots$$

Solution: An assignment $x_1, \dots, x_n \rightarrow \{0, 1\}$ satisfying all the clauses

Thm: $\cancel{\text{3Sat}}^k$ is NP-complete, for all $k \geq 3$ (see textbook)

Pf: • $\text{3Sat} \in \text{NP}$.

• Circuit-Sat $\leq_p \text{3Sat}$. (next slide)

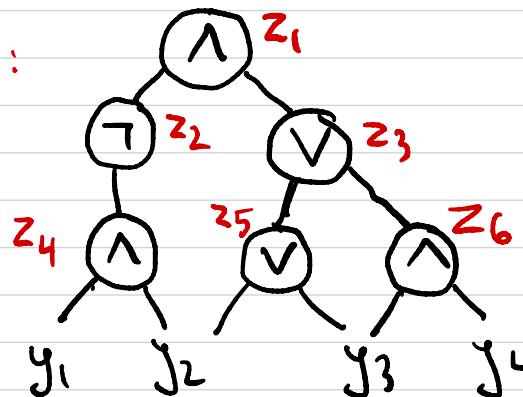
□

Fun facts: • $\text{2Sat} \in \text{P}$

• \exists heuristic algs (e.g. DPLL) which do well in practice

Thm: Circuit Sat \leq_p 3Sat

Circuit \neg Sat input:



n variables
 m gates

Poly-time reduction:

$$\begin{aligned} &z_1 \wedge \boxed{(z_1 = z_2 \wedge z_3)} \\ &\quad \wedge (z_2 = \bar{z}_4) \\ &\quad \wedge (z_3 = z_5 \vee z_6) \wedge \dots \end{aligned}$$

3Sat clauses

forbidden
assignments

3Sat
clauses

$z_1 \ z_2 \ z_3$

0	1	1	$(z_1 \vee \bar{z}_2 \vee \bar{z}_3)$
1	0	0	$\wedge (\bar{z}_1 \vee z_2 \vee z_3)$
1	0	1	$\wedge (\bar{z}_1 \vee z_2 \vee \bar{z}_3)$
1	1	0	$\wedge (\bar{z}_1 \vee \bar{z}_2 \vee z_3)$

Poly-time recovery: $y_1, \dots, y_n, z_1, \dots, z_m \in \{0, 1\}$ satisfying 3Sat inst.
 $\rightarrow y_1, \dots, y_n$ satisfying Circuit Sat inst.

Independent Set

Input: Graph $G = (V, E)$, integer k

Solution: Independent set of size k (k vertices in V w/ no edges between them)

Thm: 3Sat \leq_p Ind-Set (\therefore Ind-Set is NP-complete)

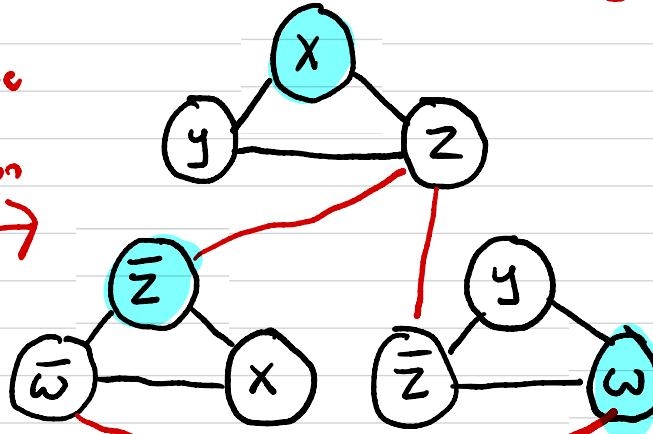
Ind Set instance G

3Sat instance +

poly-time

Reduction

$$\begin{array}{l} (x \vee y \vee z) \\ \wedge (\bar{z} \vee \bar{w} \vee x) \\ \wedge (y \vee \bar{z} \vee w) \end{array}$$



poly-time

Recovery

$$x = 1$$

$$y = 0 \text{ or } 1$$

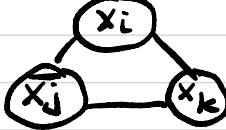
$$z = 0$$

$$w = 1$$

Sat assign: $x=y=1, z=w=0$

$K = m = \# \text{ of Clauses}$

Proof part 1 : If 3Sat inst x has sat assign $A: \{x_1, \dots, x_m\} \rightarrow \{0, 1\}$, then graph G has ind set I of size k

- For each clause $x_i \vee \bar{x}_j \vee x_k$, A sets either $x_i=1, \bar{x}_j=1, x_k=1$.
- Pick one (say x_i). Add $\circlearrowleft x_i$ of  to I .
- So I is of size $k=m$.
- I contains no $\circlearrowleft x_i - \circlearrowleft \bar{x}_i$. \therefore No edges, so independent set.

Proof part 2 : Let I be ind set in G of size K .

Then Recovery alg outputs satisfying assignment.

Recovery alg

$$\text{variable } x_L = \begin{cases} 1 & \text{if some } \circlearrowleft x_i \in I \\ 0 & \text{if some } \circlearrowleft \bar{x}_i \in I \\ \text{arbitrary o.w.} & \end{cases}$$

- For each i , only $\circlearrowleft x_i$ or $\circlearrowleft \bar{x}_i \in I$. So Recovery alg well-defined.

- For each , $x_i=1, x_j=1$, or $x_k=1$. So all clauses satisfied.

Integer Programming (IP)

Input: A linear program

Solution: An integer solution to LP

Thm: Ind-Set \leq_p Integer Programming

Pf: Ind Set instance

- $G = (V, E)$
- $V = \{1, 2, \dots, n\}$
- integer k

poly-time
Reduction

IP instance

$$0 \leq x_i \leq 1$$
$$\sum_{i=1}^n x_i = k$$

$$\forall (i, j) \in E, x_i + x_j \leq 1$$

Proof part 1: If \exists ind set I of size k ,
 \exists solution to IP instance. (Just set $x_i = \begin{cases} 1 & \text{if } i \in I \\ 0 & \text{if } i \notin I \end{cases}$)

Proof part 2: If (x_1, \dots, x_n) is solution to IP,
can "recover" and ind set of size k . (Just put $i \in I$ if $x_i = 1$)