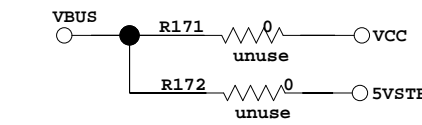
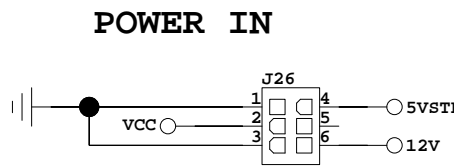
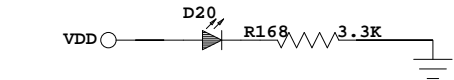
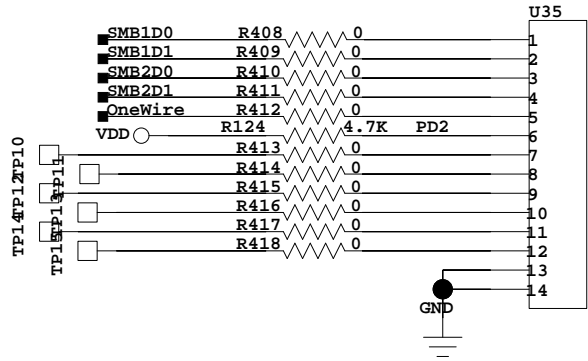
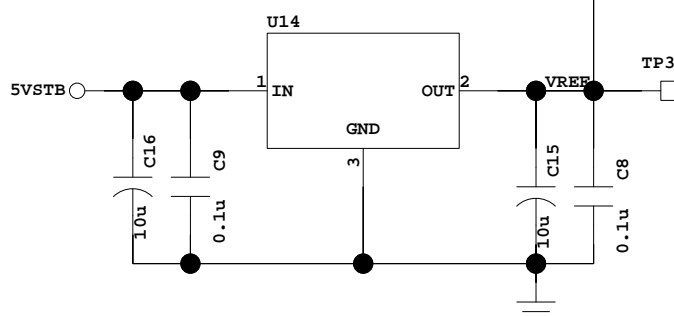
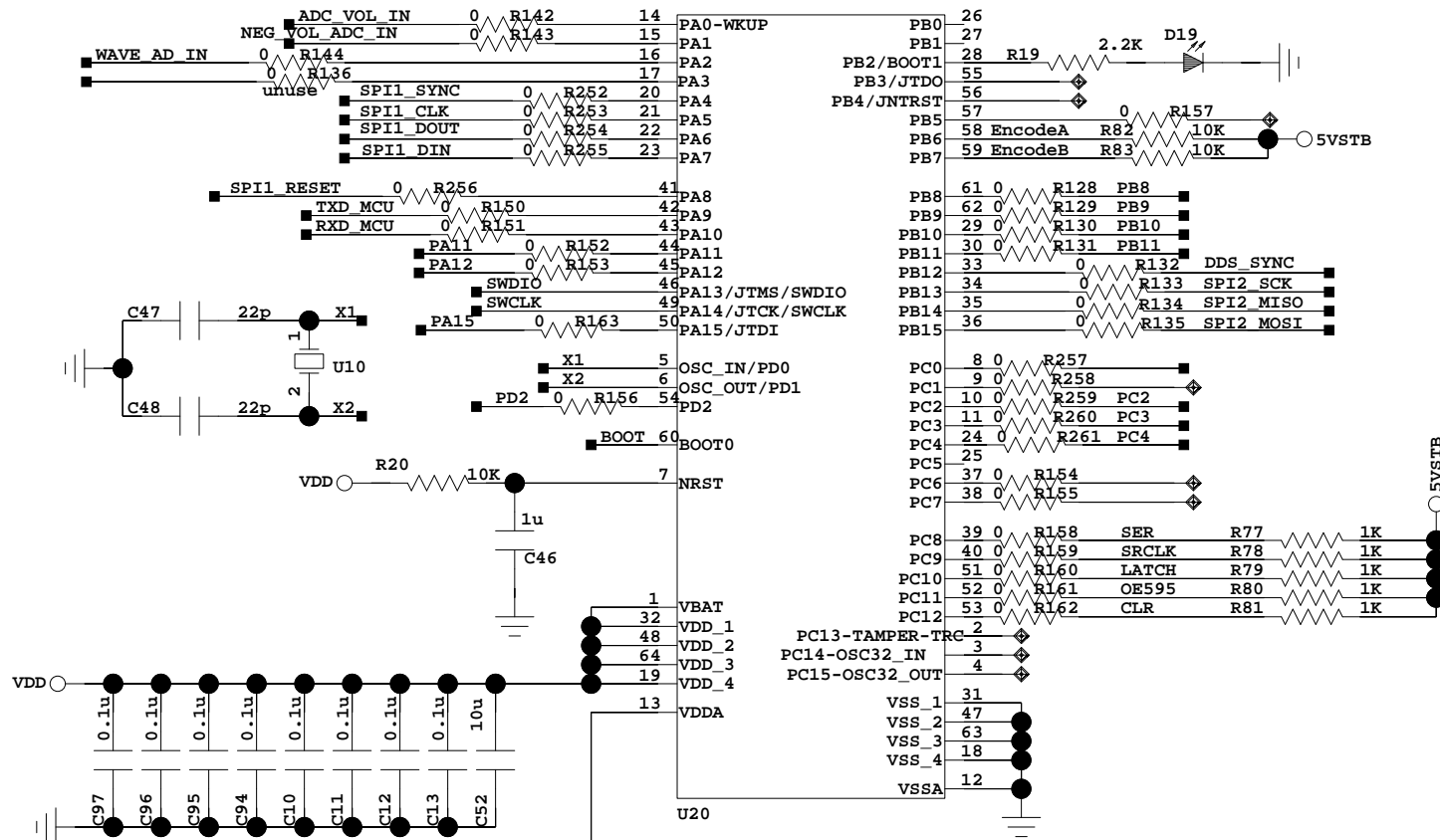
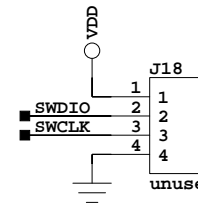
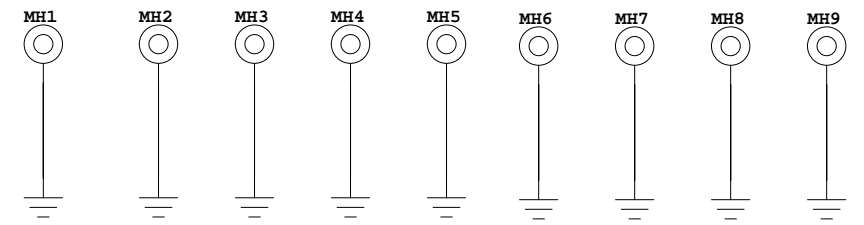
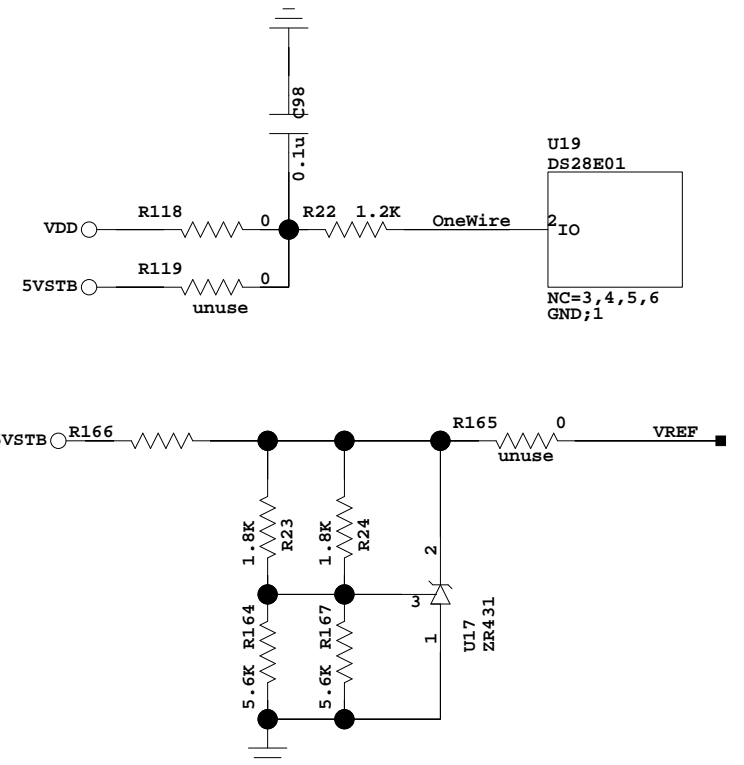
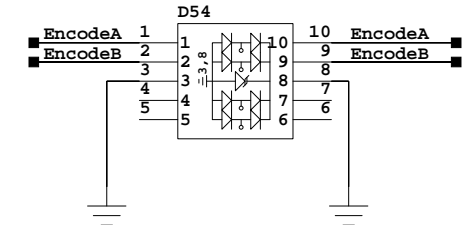
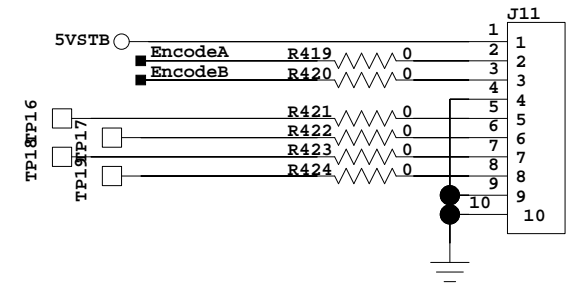
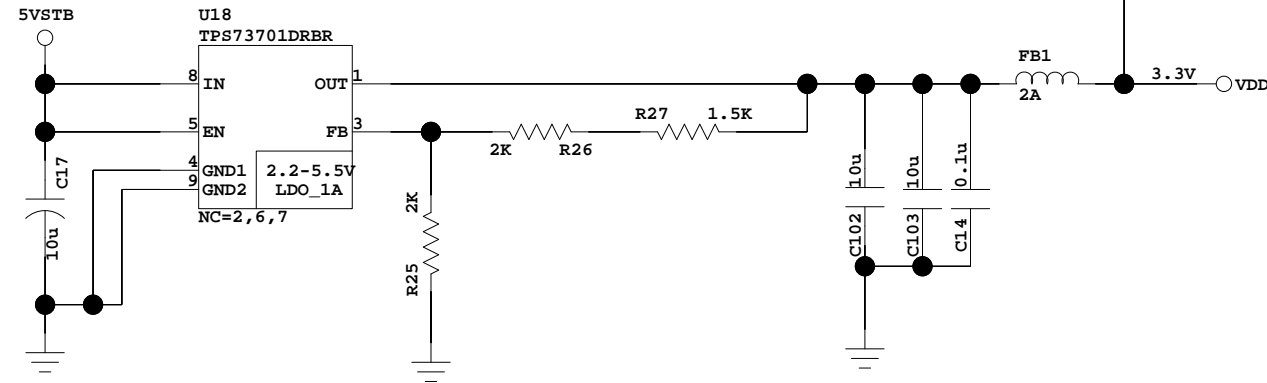


Security Classification:
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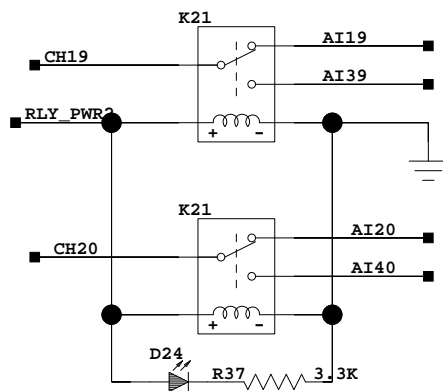
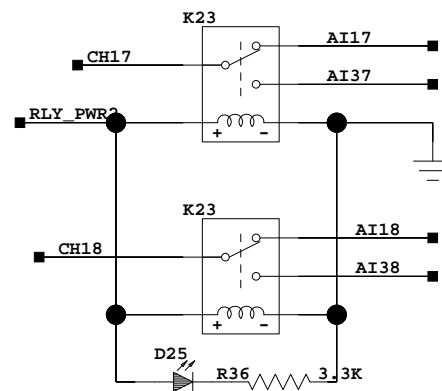
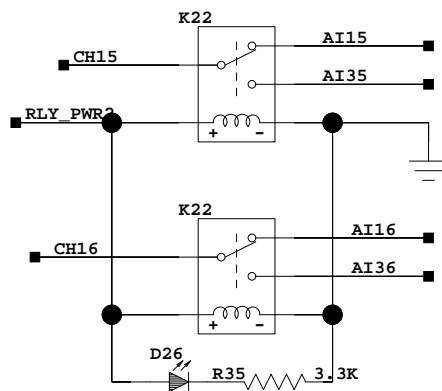
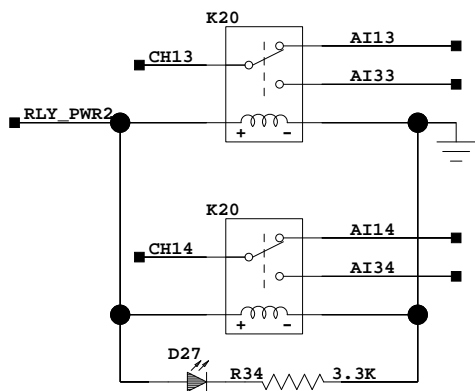
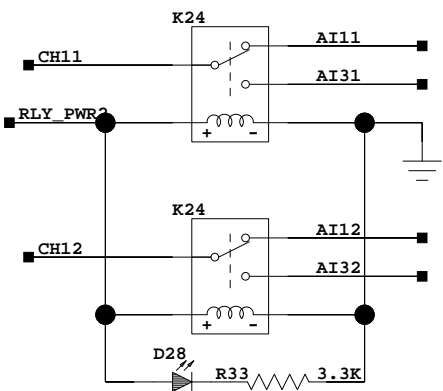
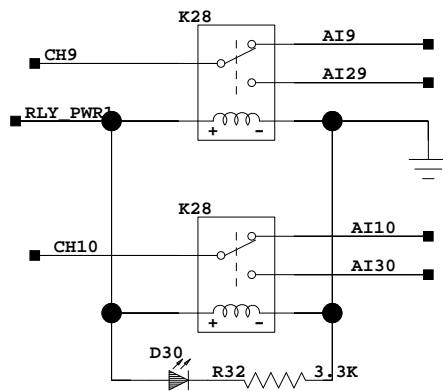
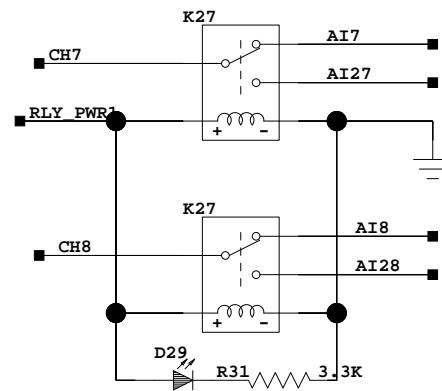
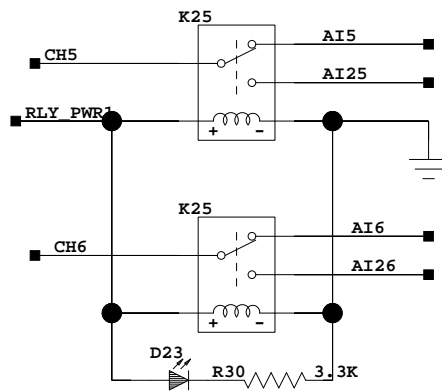
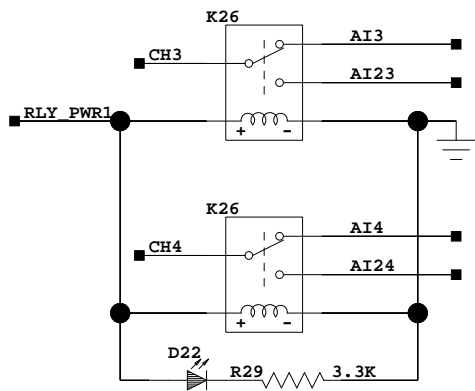
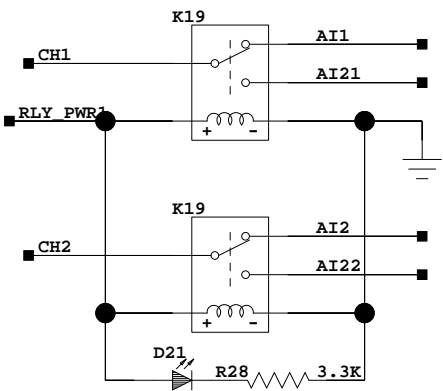
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TITLE	Name
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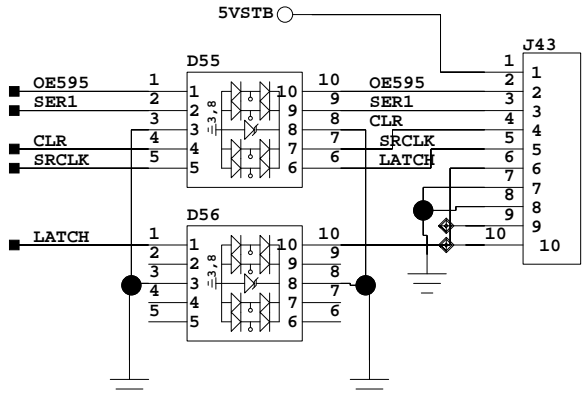
Designed by

Author

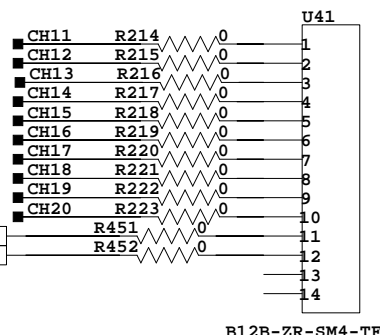
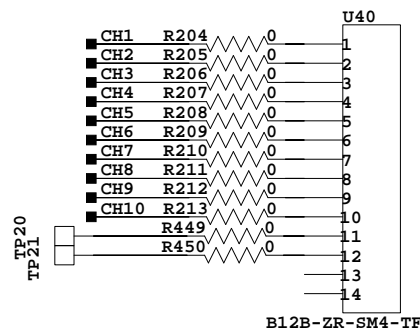
Security Classification:



TO RelayMatrix board



TO DMM

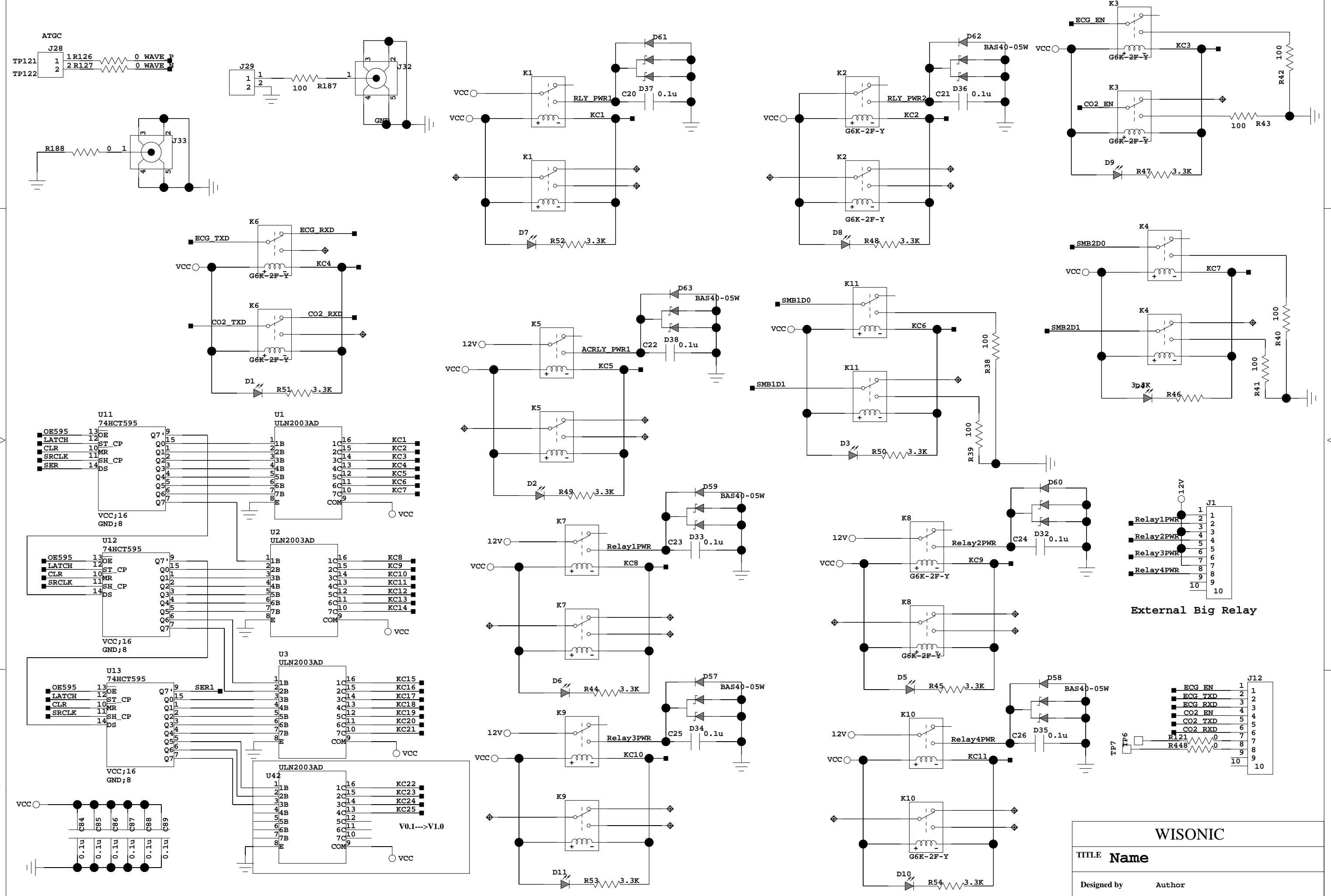


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TITLE Name

Designed by Author

Security Classification:
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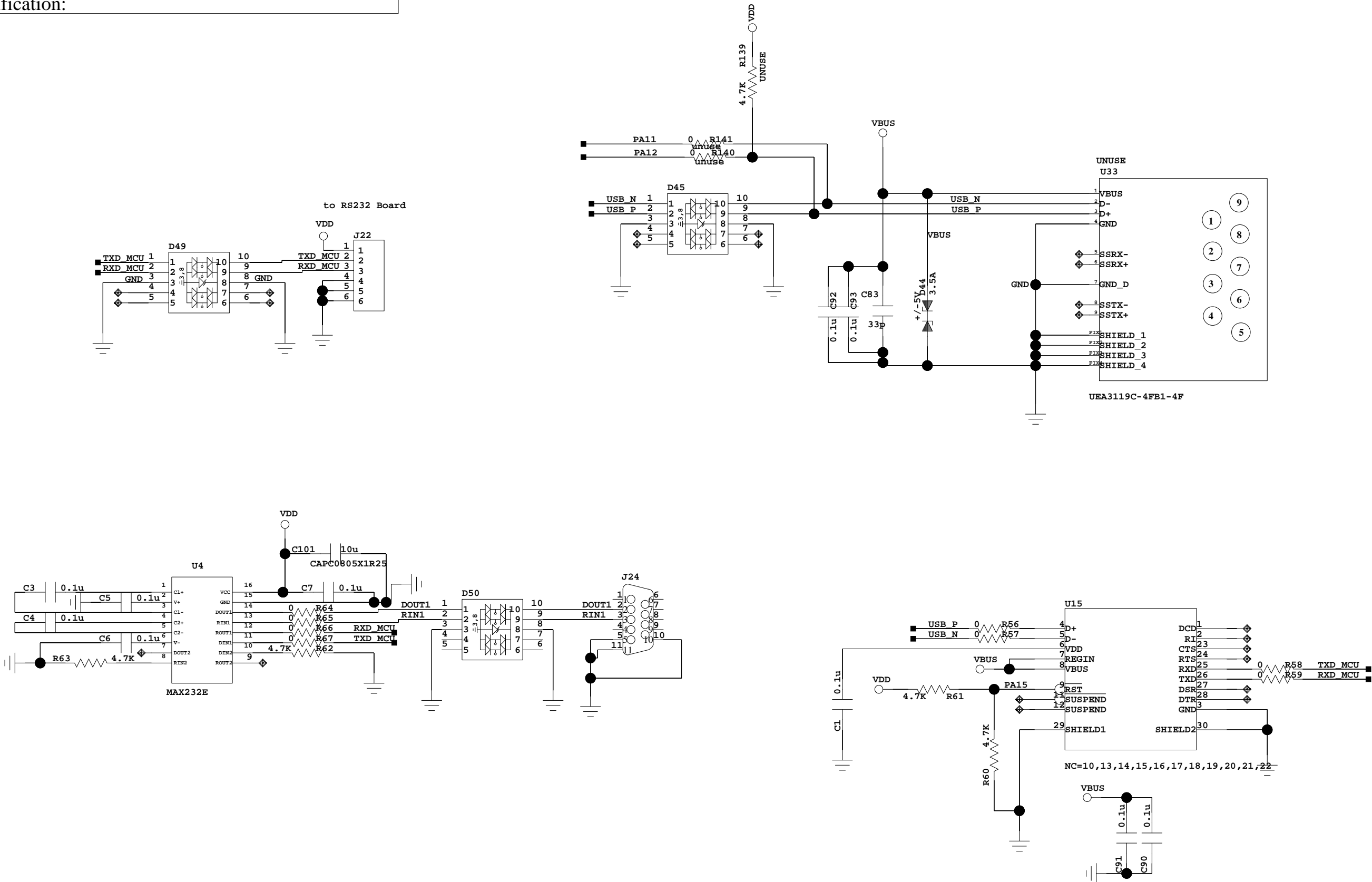
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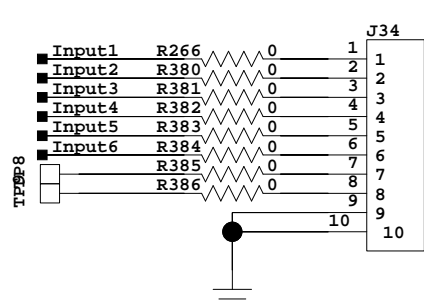
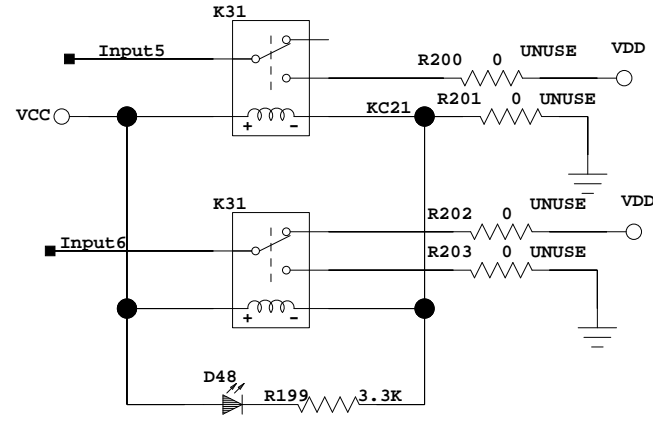
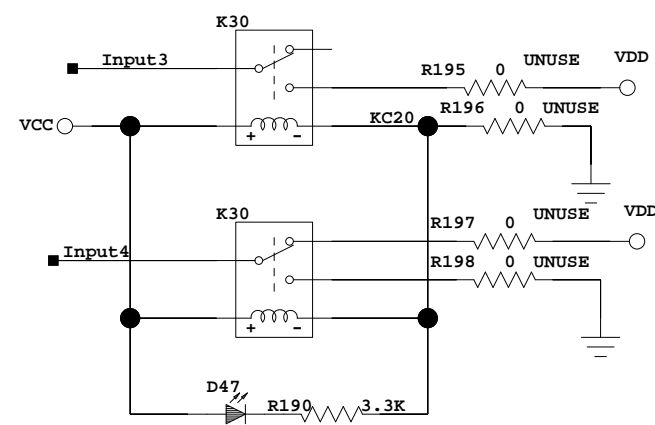
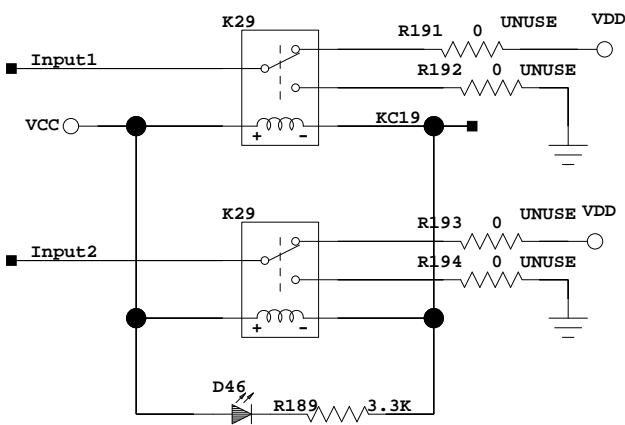
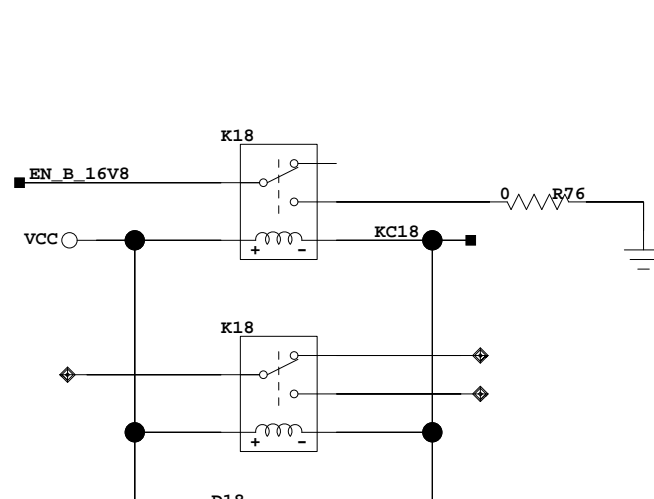
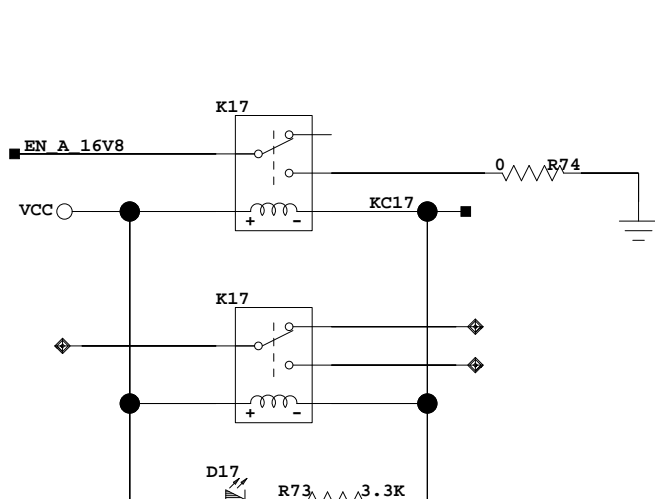
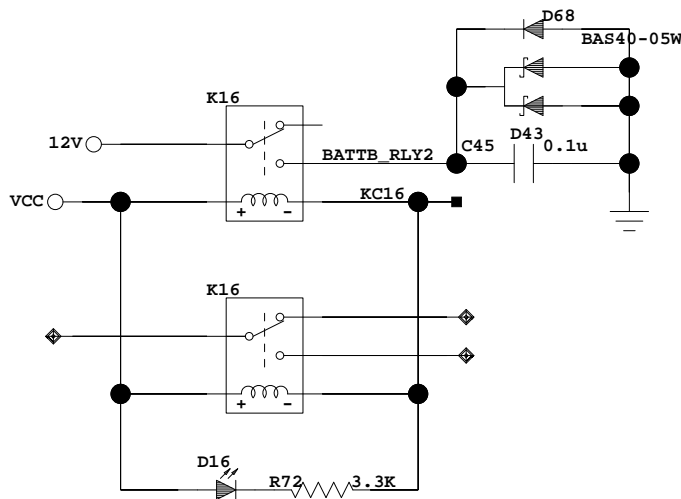
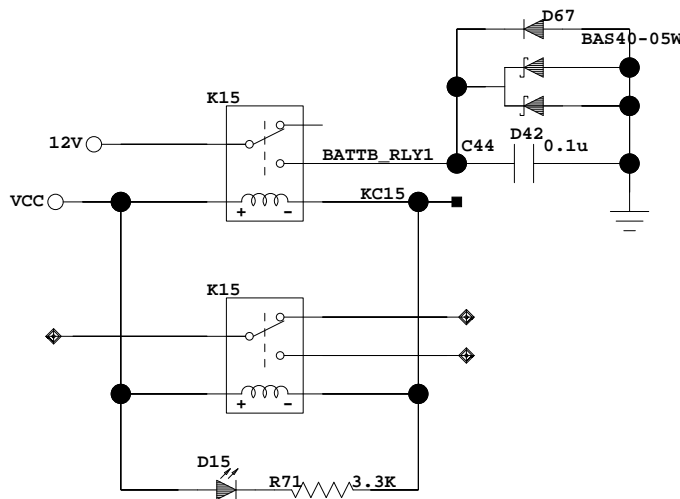
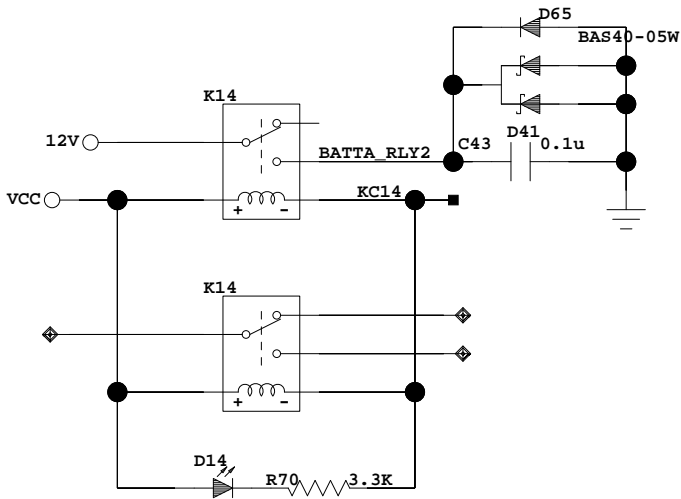
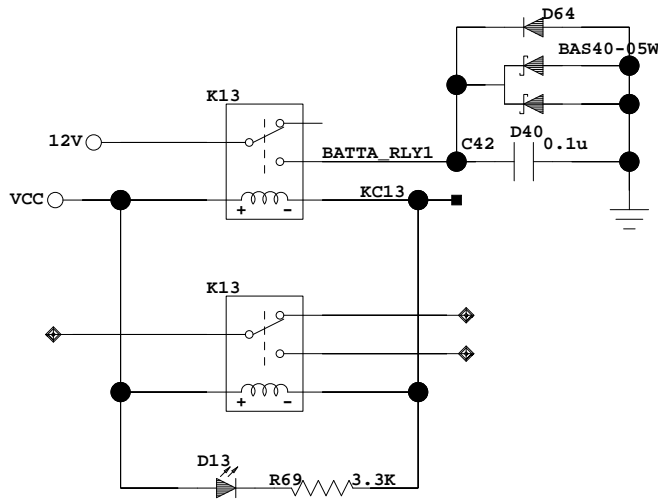
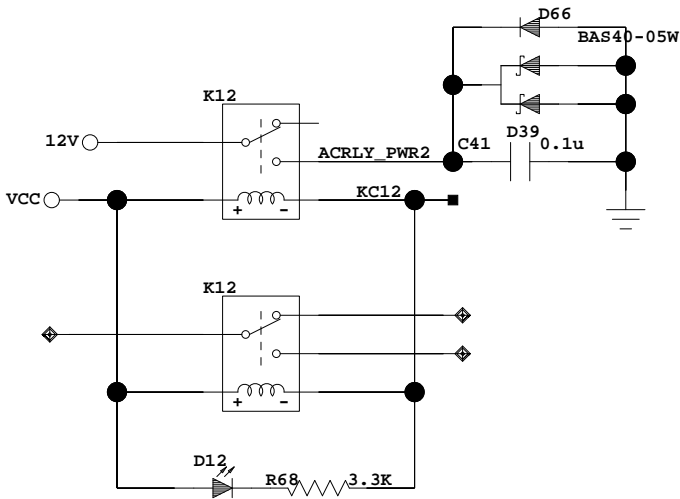
TITLE	Name
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Security Classification:

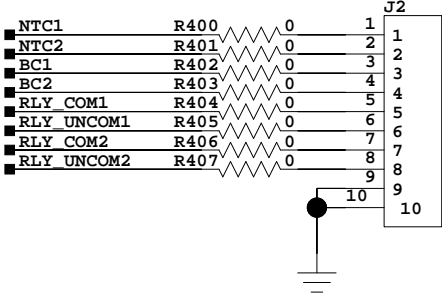
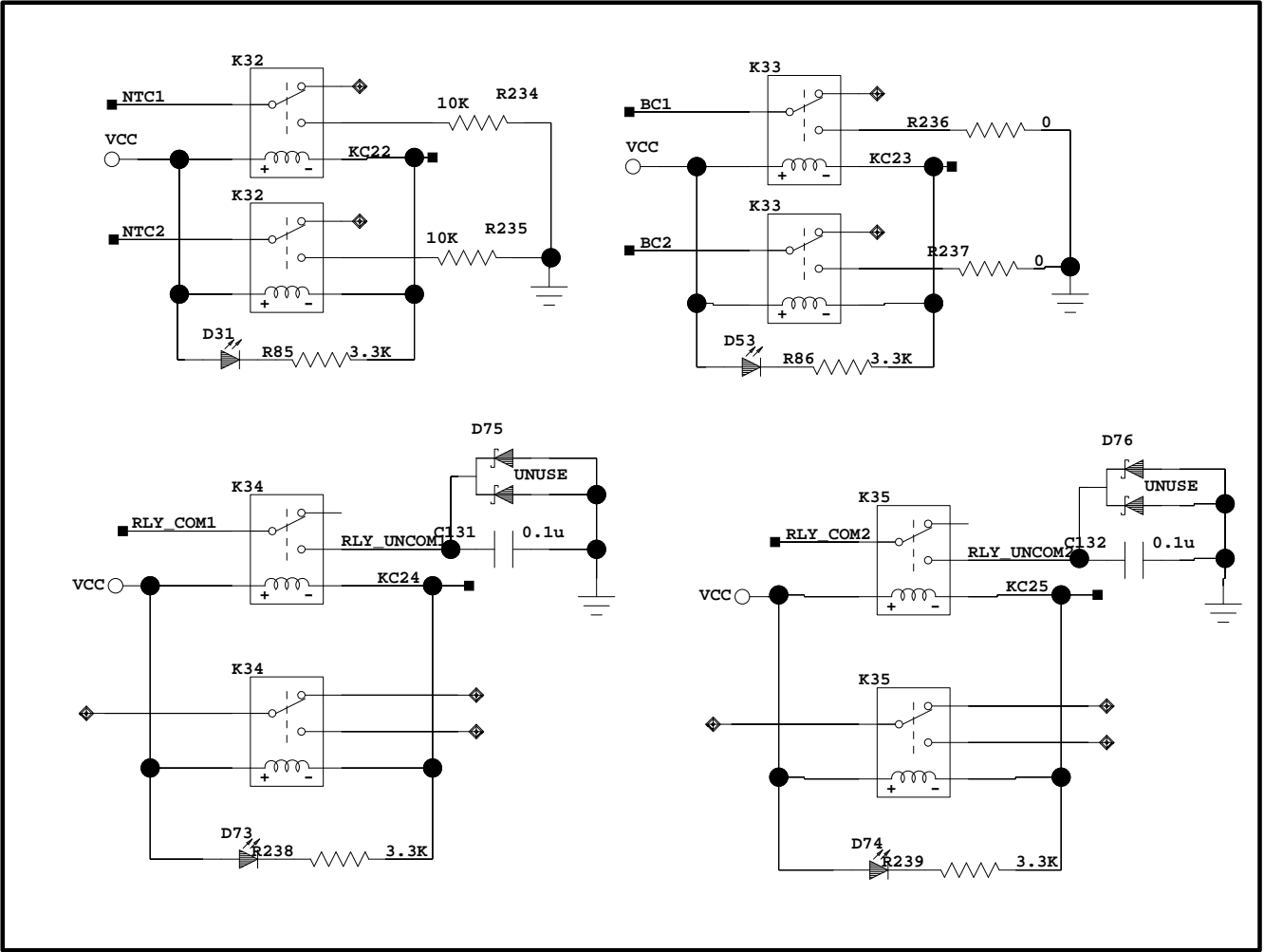


Security Classification:



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TITLE	Name
Designed by	Author

Security Classification:

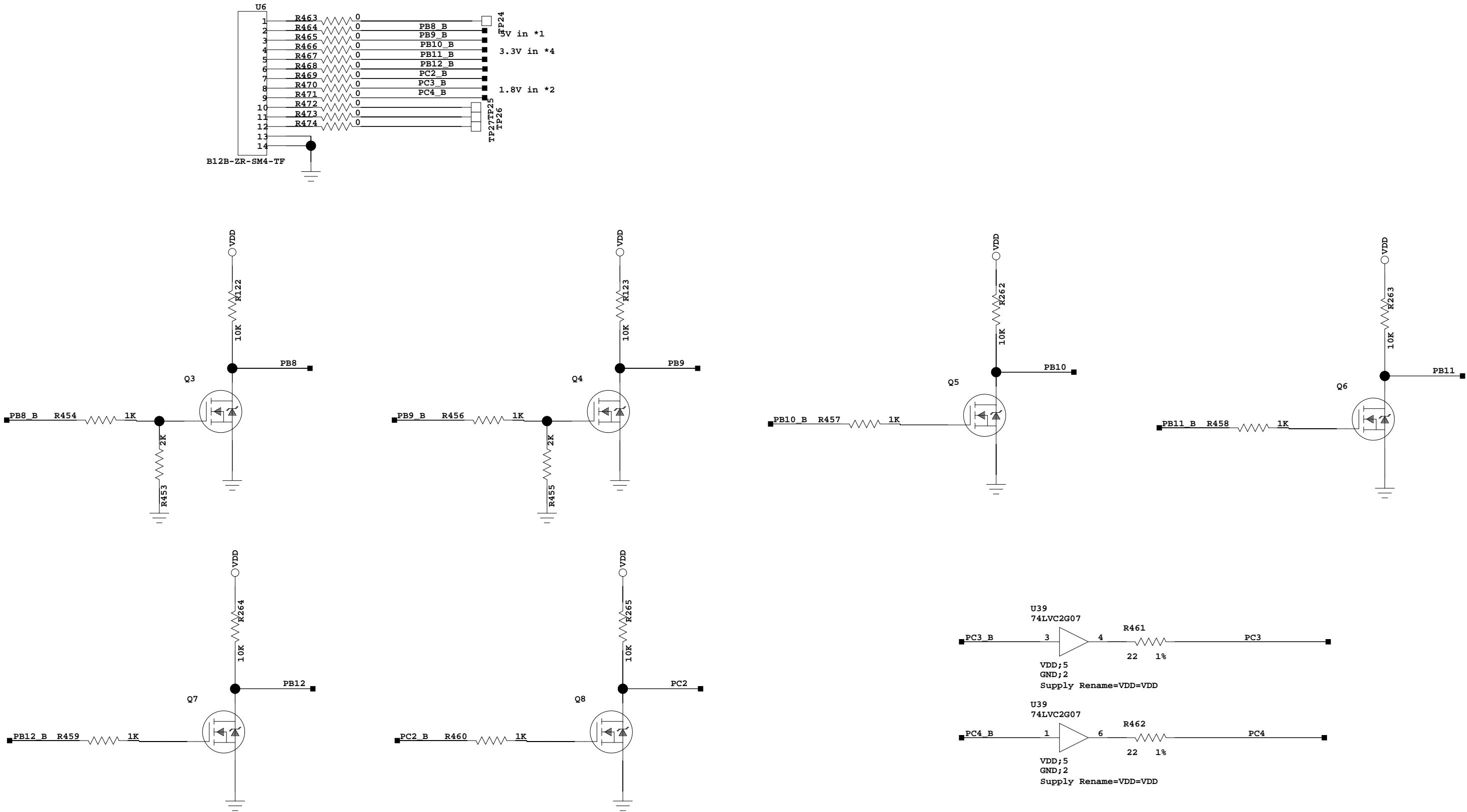


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TITLE Name	
Doc No.	REV 0.1
Designed by	P.Code

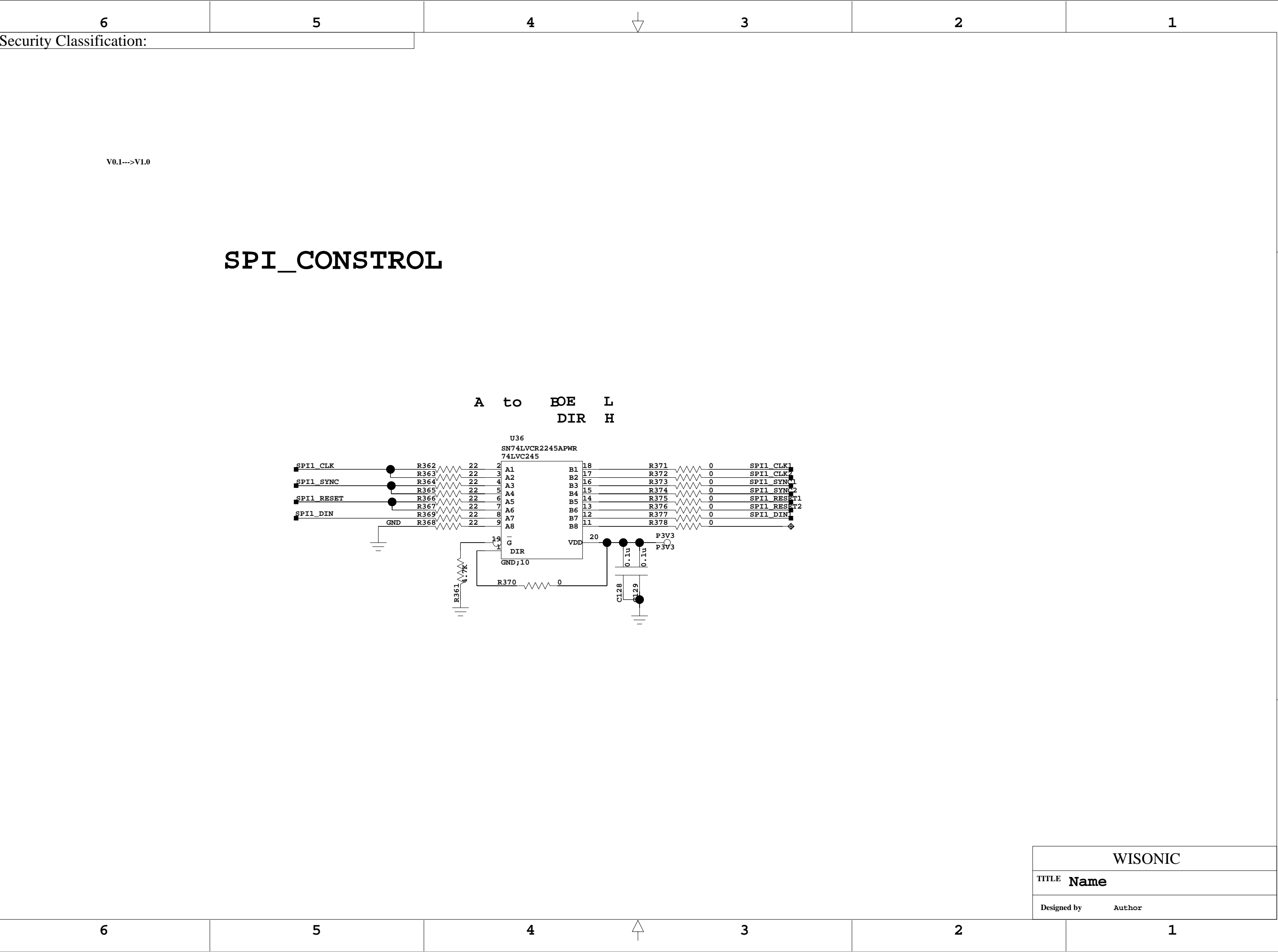
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V0.1--->V1.0

Digital Signal In

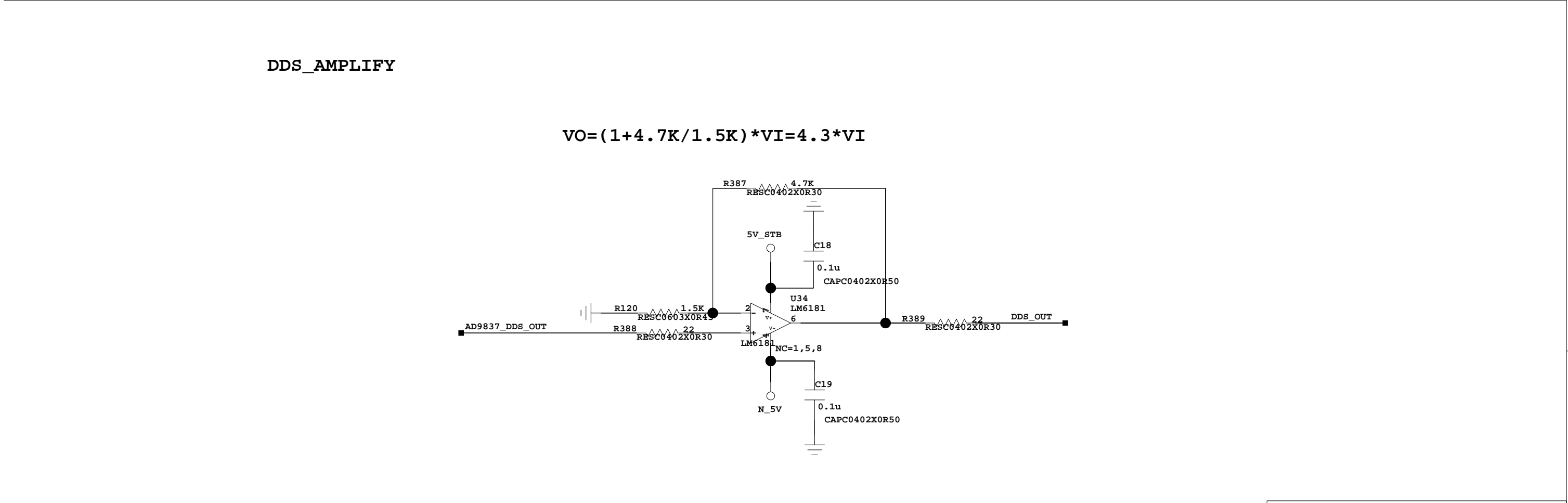
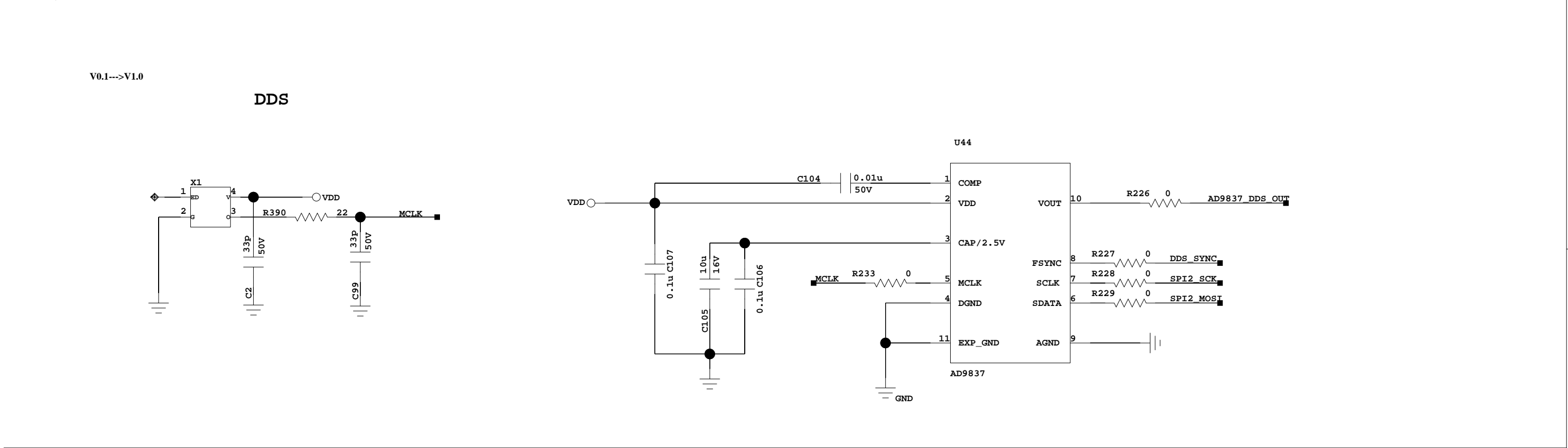


WISONIC		
TITLE Name		
Doc No.	REV 0.1	
Designed by	AUTHOR	P.Code





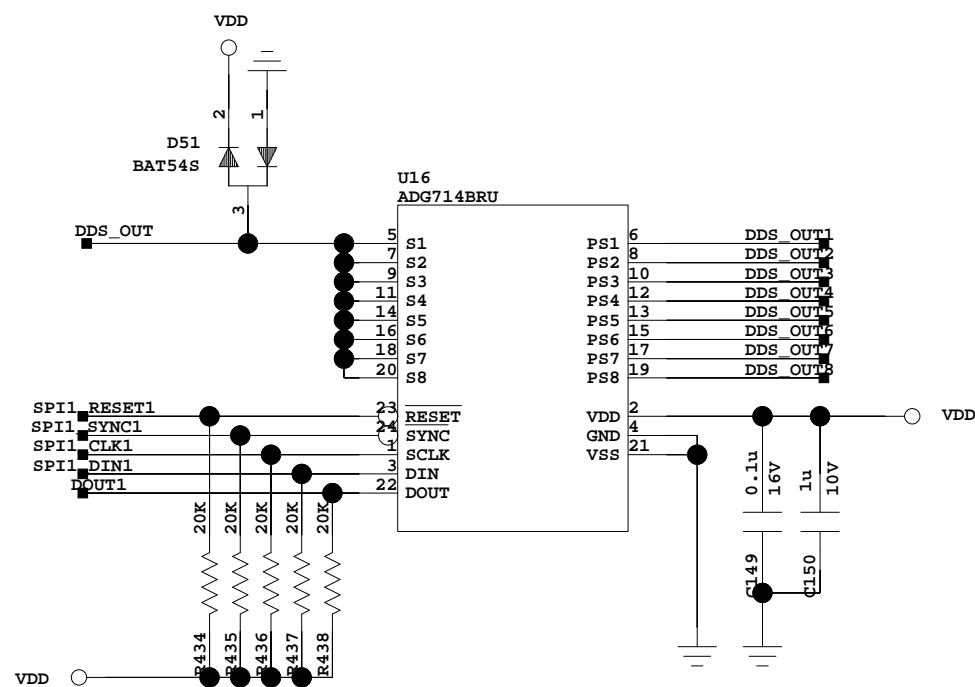
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WISONIC	
TITLE Name	
Doc No.	REV 0.1
Designed by	P.Code

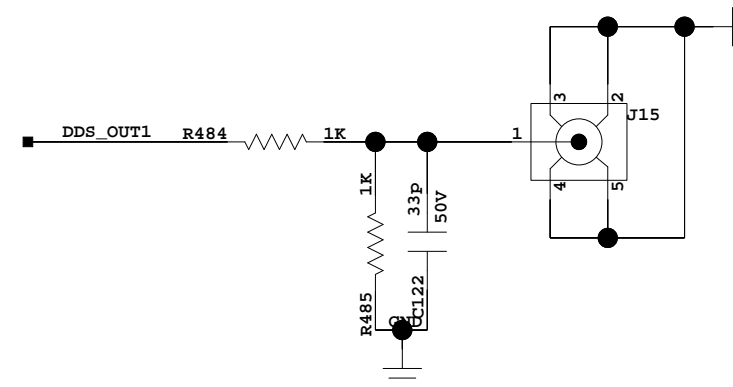
**Security Classification:**

## DDS SWITCH

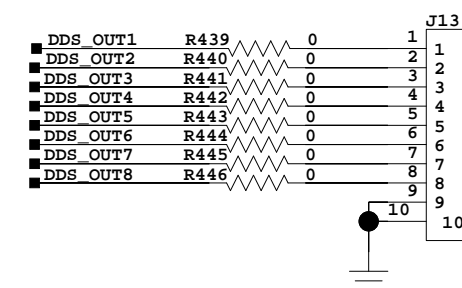


DDS OUT PUT

To Relay Board



To Test Board



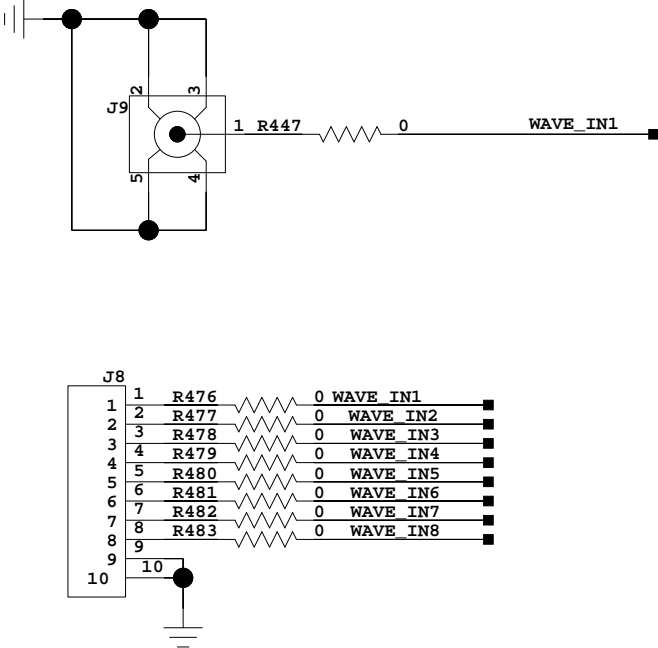
WISONIC	
TITLE <b>Name</b>	
Doc No.	REV 0.1
Designed by  AUTHOR	P.Code

**Security Classification:**

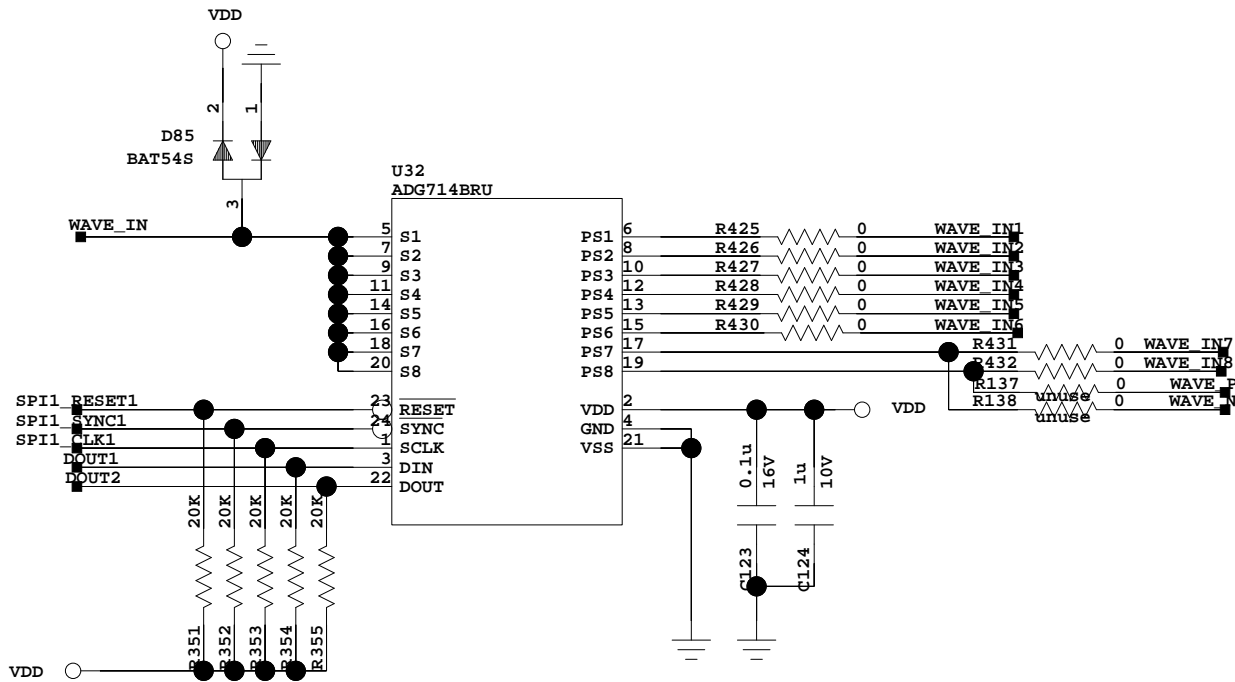
## V0.1--->V1.0

## TEST IN

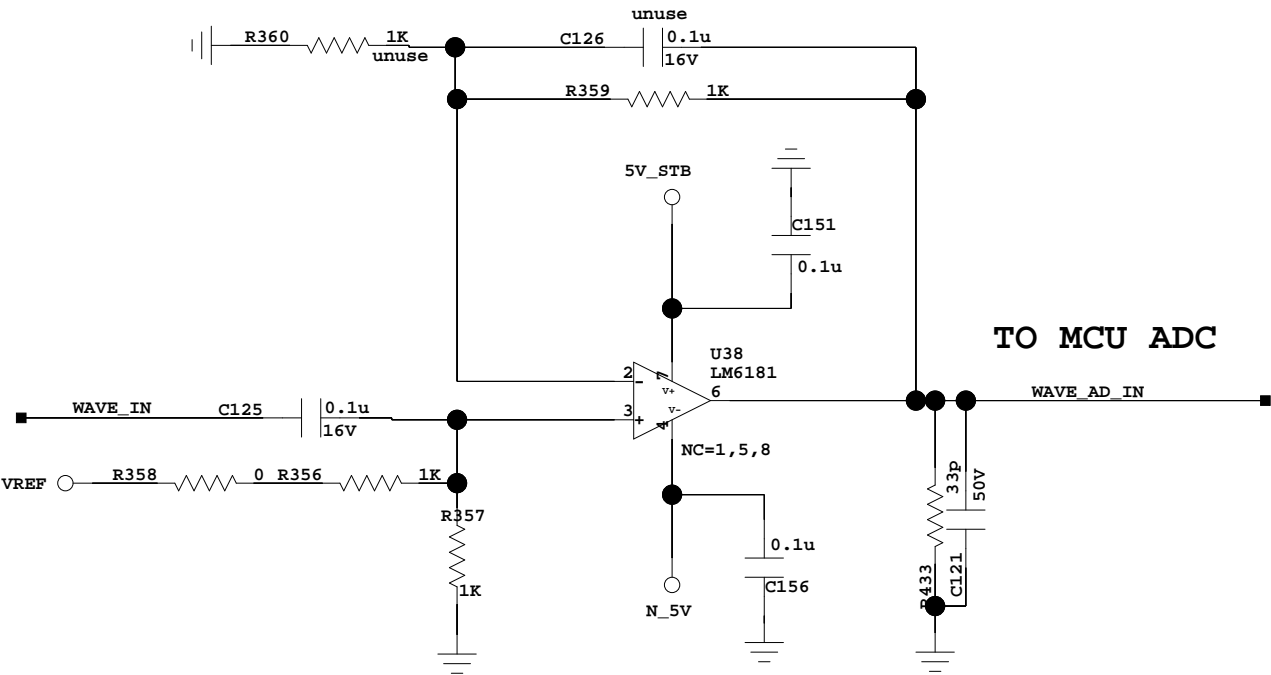
FROM RelayMatrix Board



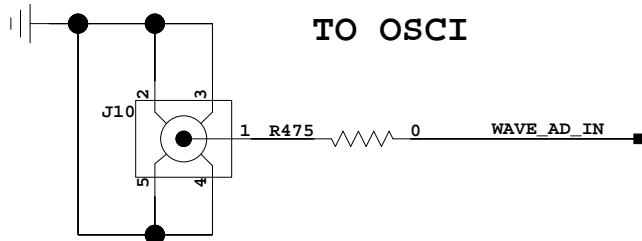
## TEST WAVE SWITCH



## AD AMPLIFY



TO OSCI

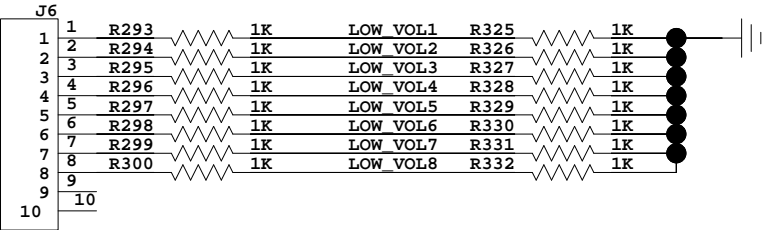


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<b>TITLE   Name</b>	
<b>Doc No.</b>	<b>REV</b> <div style="text-align: right;">0.1</div>
<b>Designed by</b>	<b>P.Code</b>
<b>AUTHOR</b>	

Security Classification:

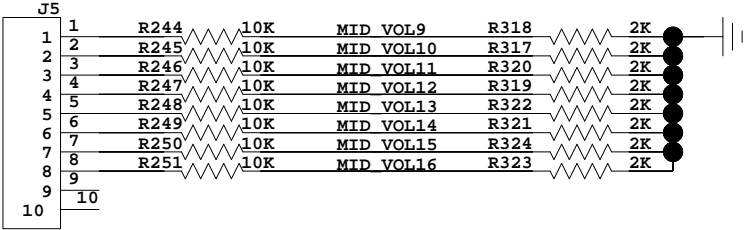
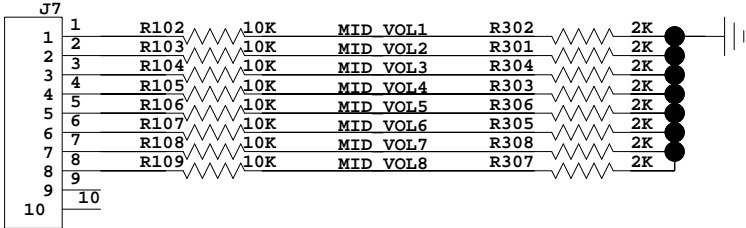
V0.1--->V1.0

LOW\_VOL TEST IN (0-5V)



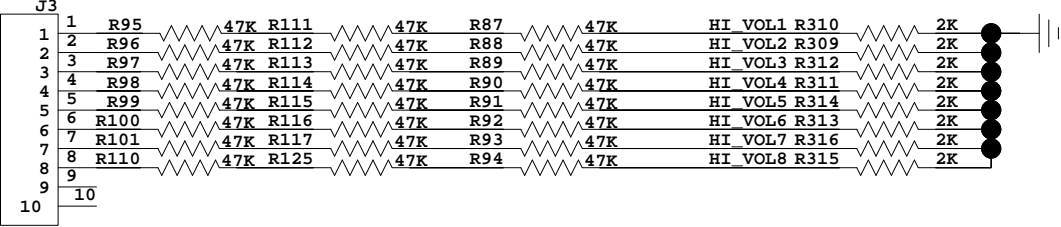
$V=LOW\_VOL*4096*(1/2)/3.3$

MID\_VOL TEST IN (5-15V)



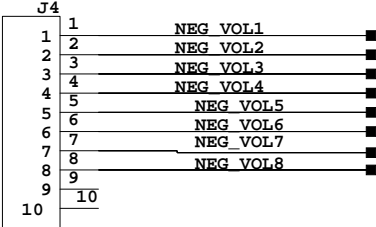
$V=VOL*4096*(2/12)/3.3$

HI\_VOL TEST IN (15-100V)



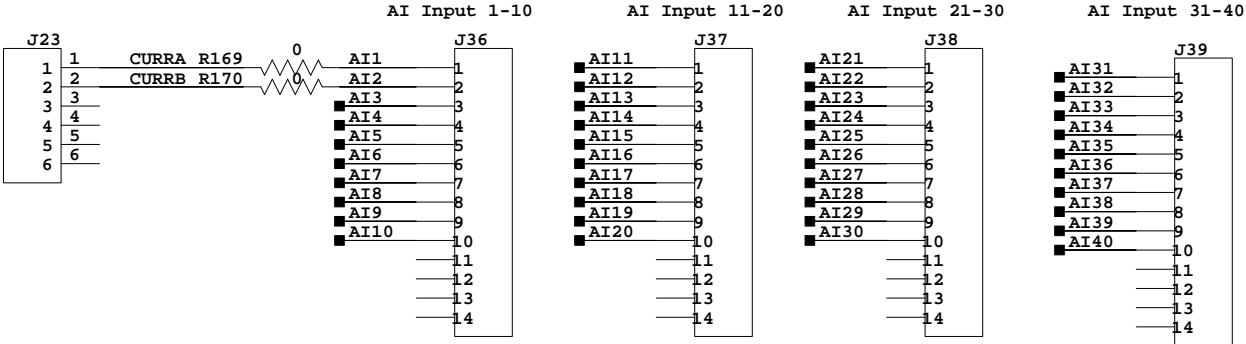
$V=VOL*4096*(2/143)/3.3$

NEG\_VOL TEST IN(-15V-0V)



$V=VOL*4096*(2/12)/3.3$

$V=VOL*4096*(2/143)/3.3$

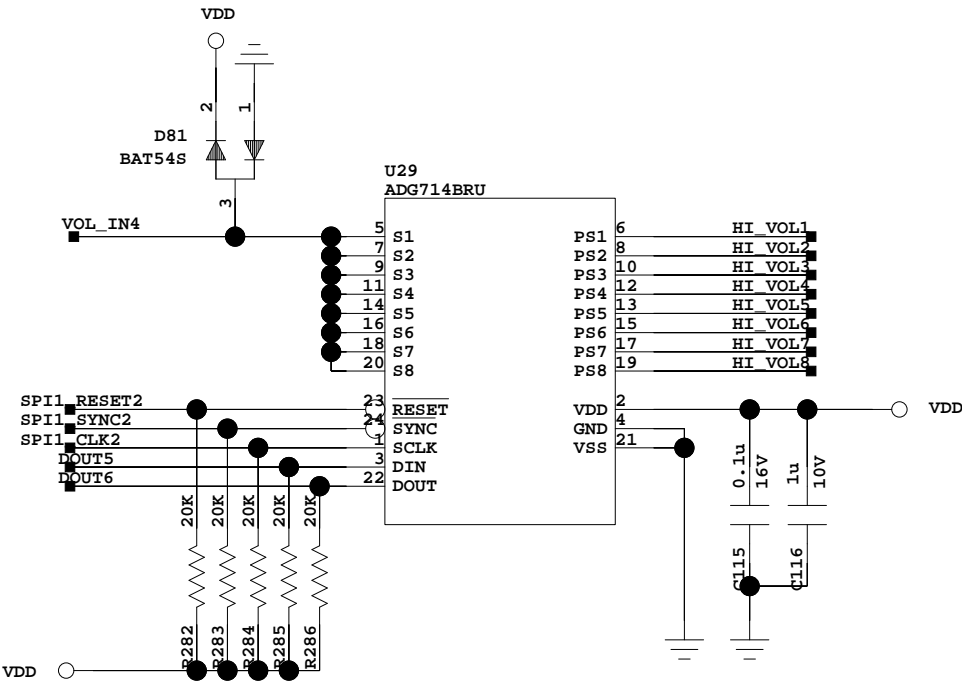
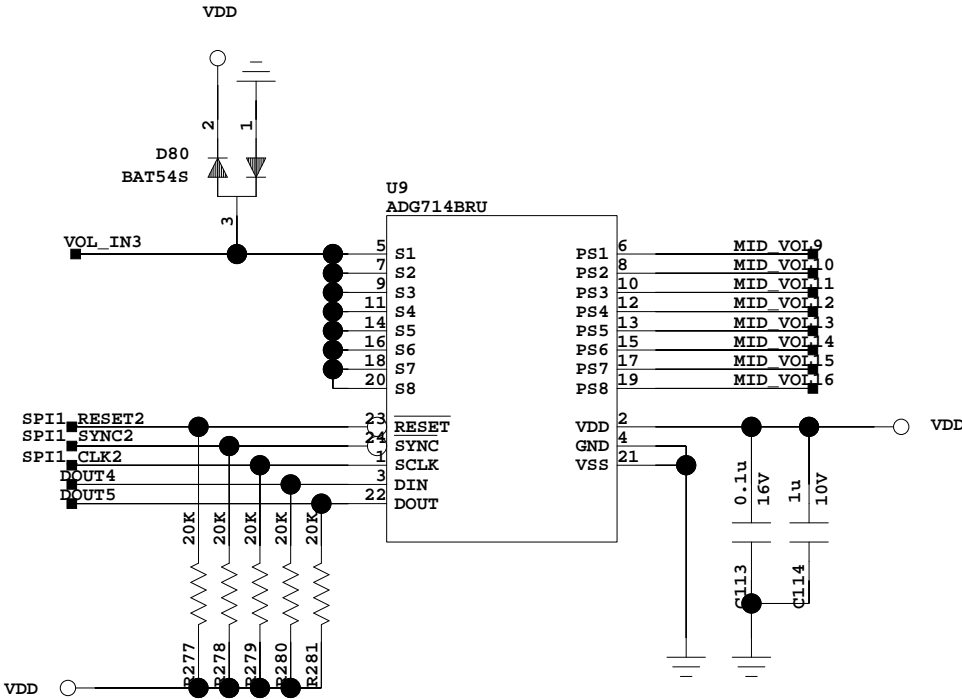
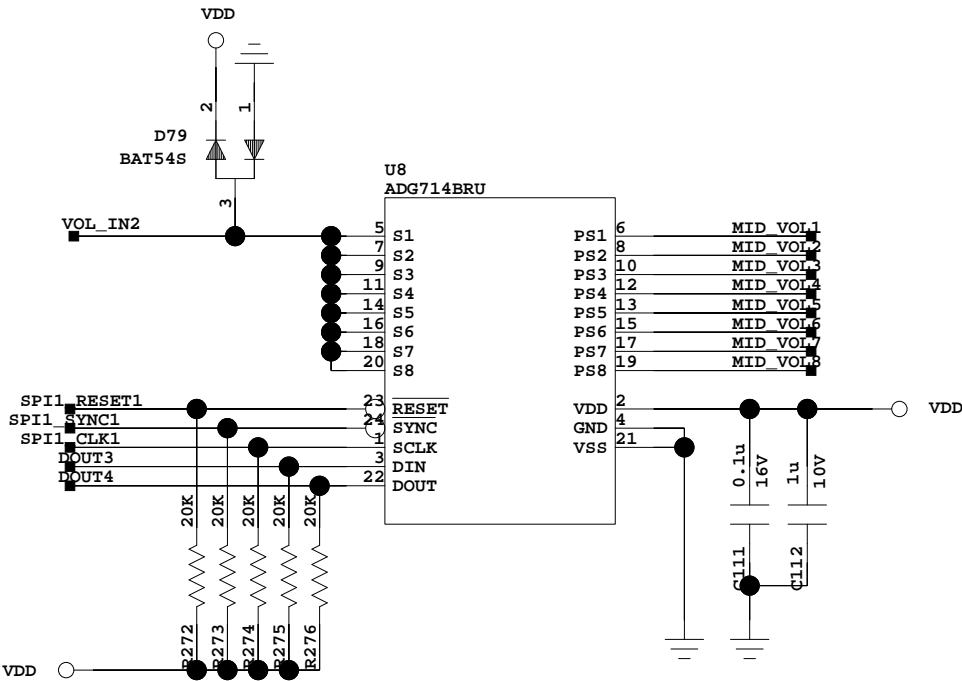
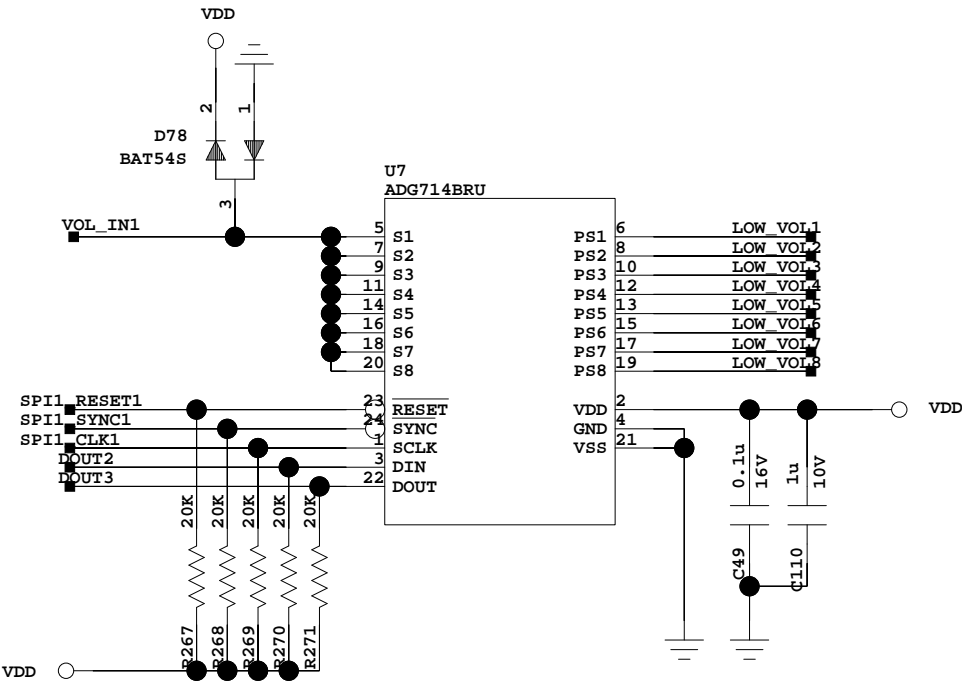


WISONIC	
TITLE <b>Name</b>	
Doc No.	REV 0.1
Designed by	P.Code AUTHOR

Security Classification:

V0.1--->V1.0

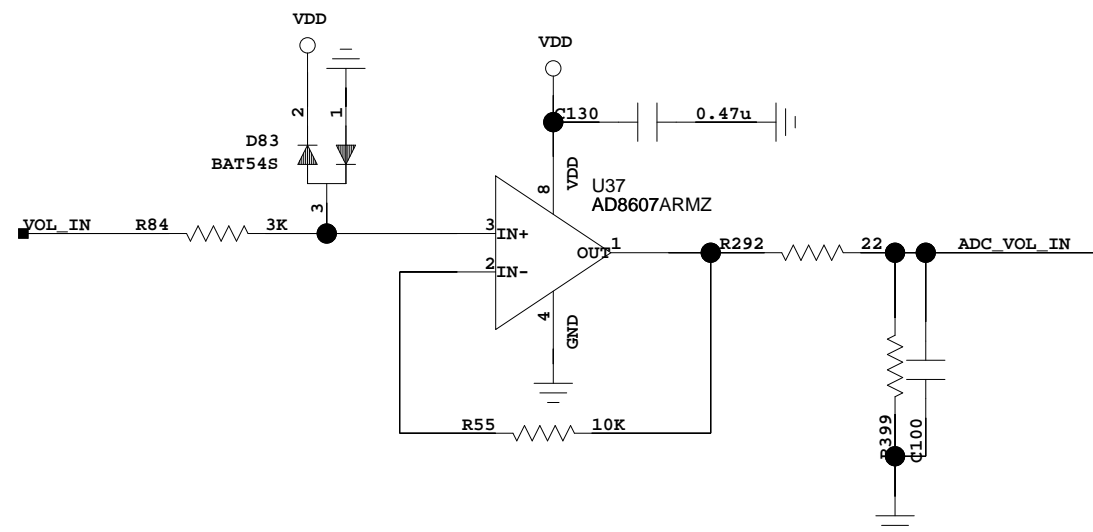
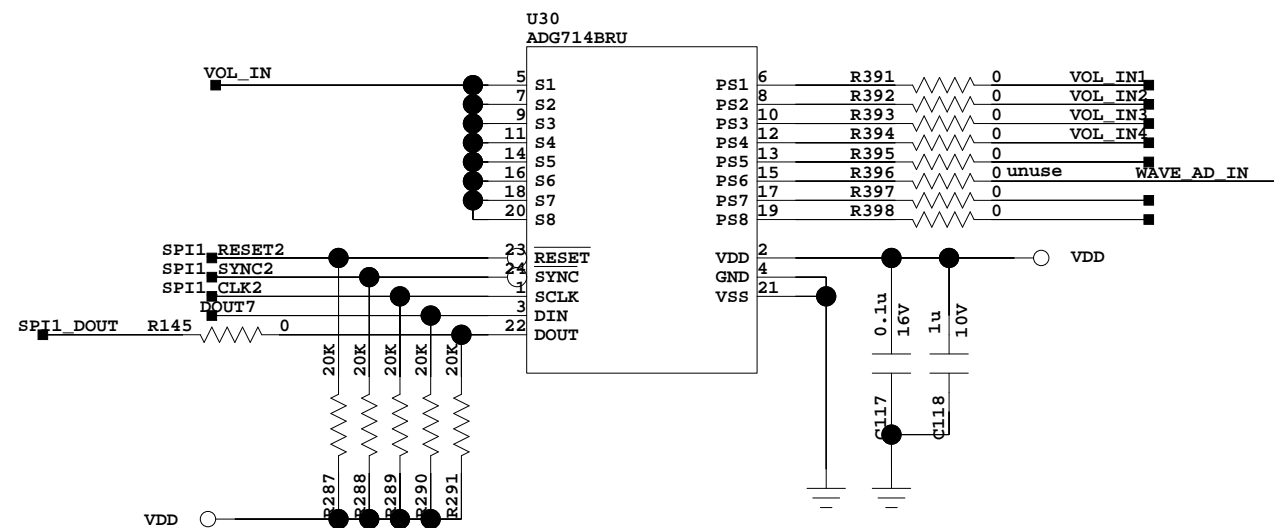
VOLTAGE TEST SWITCH



WISONIC		
TITLE      Name		
Doc No.		REV 0.1
Designed by      AUTHOR		P.Code

Security Classification:

**V0.1--->V1.0**



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TITLE	Name
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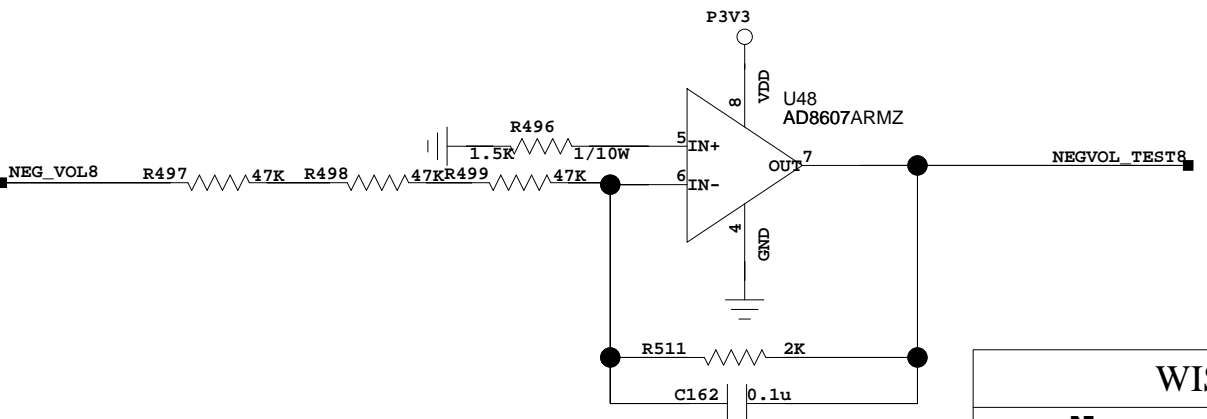
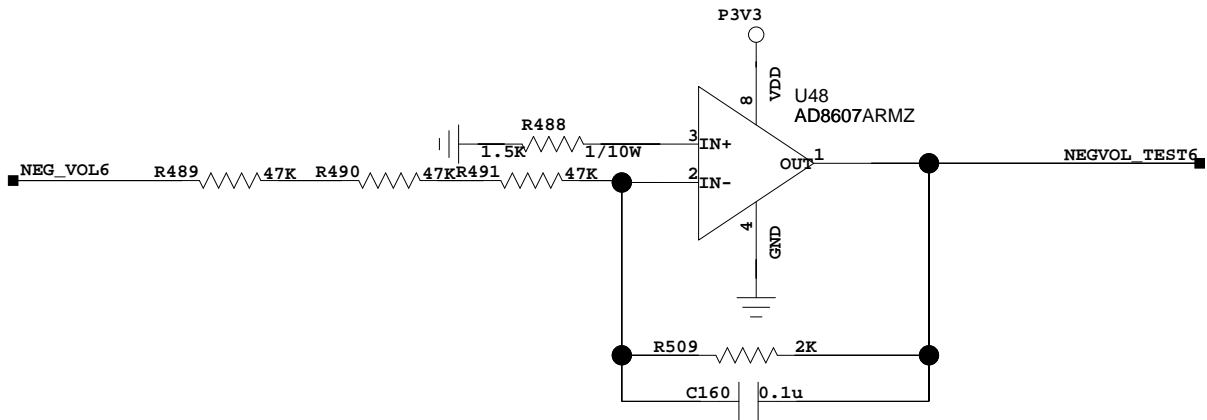
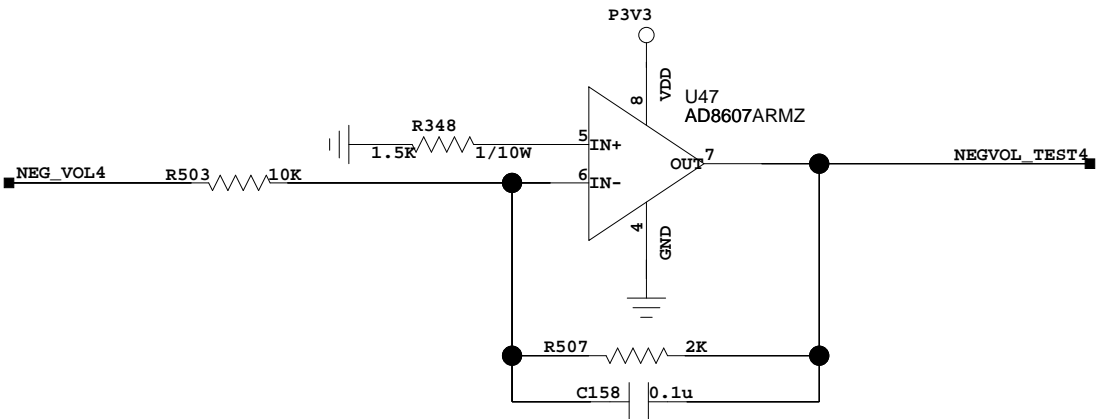
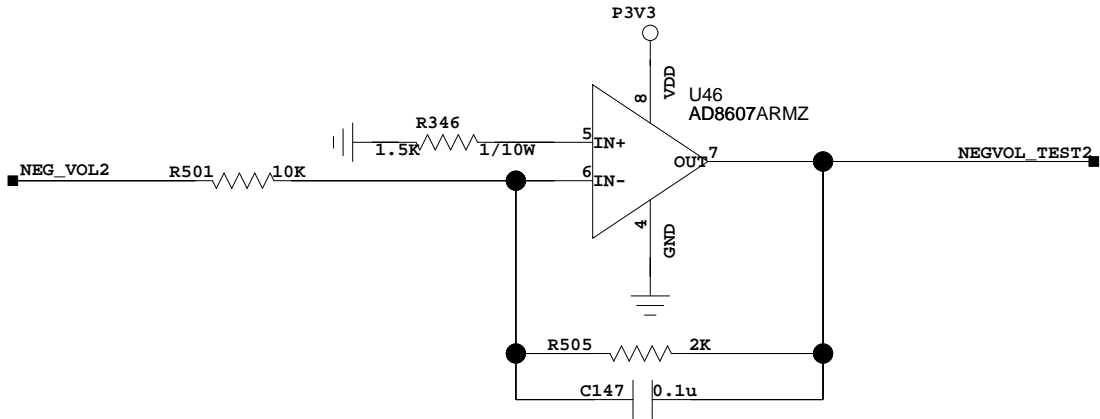
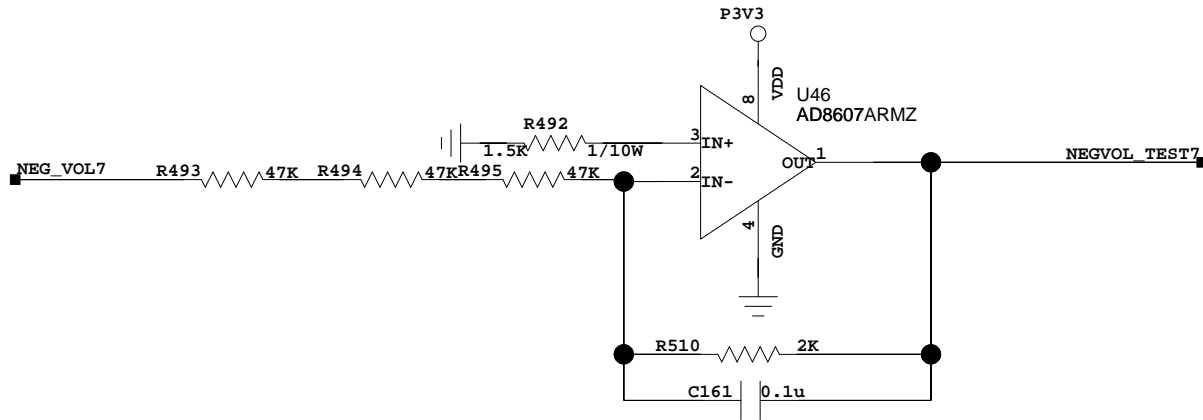
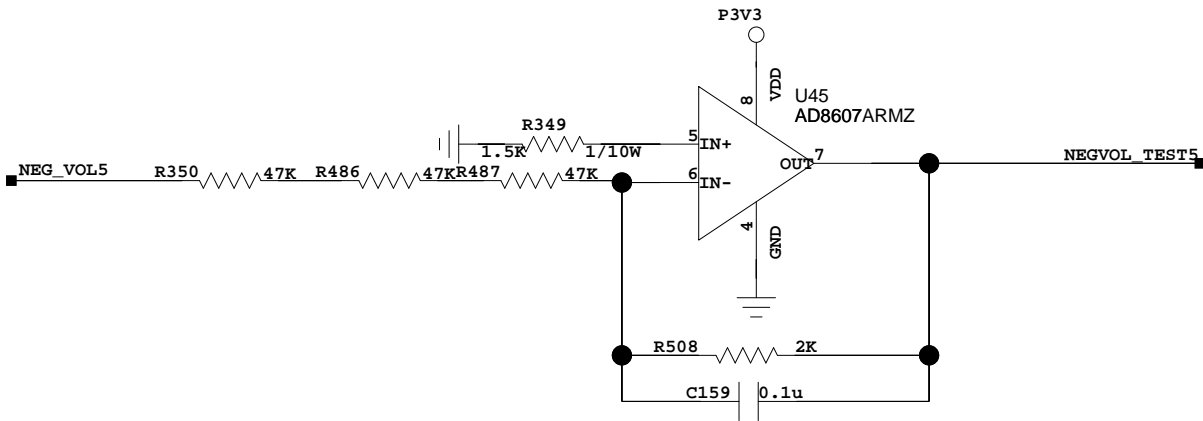
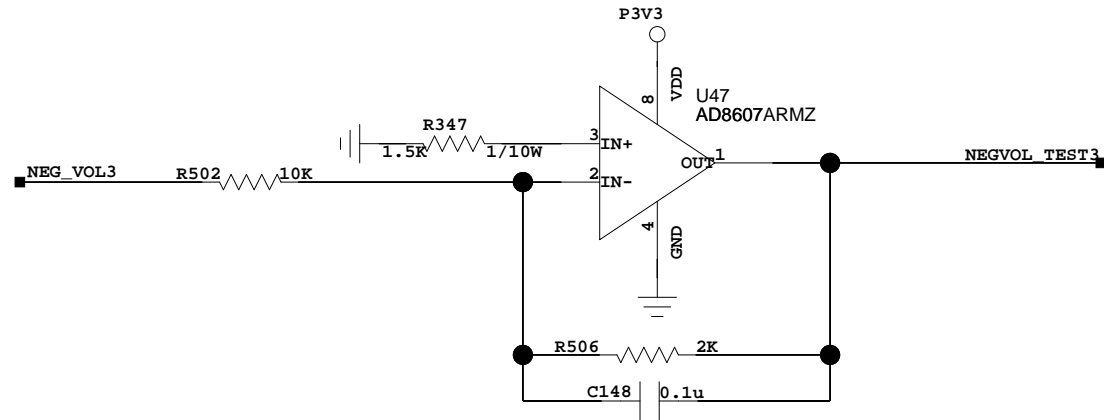
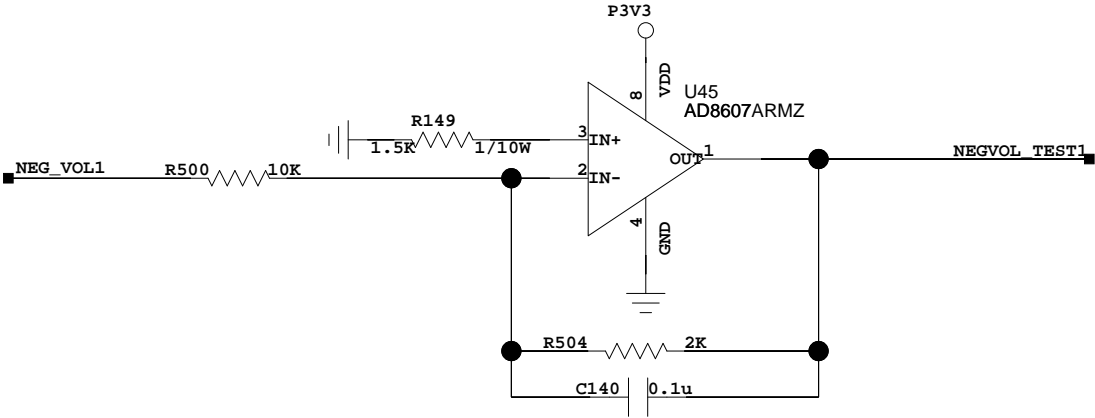
Designed by

**AUTHOR**

P.Code
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Security Classification:

V0.1--->V1.0

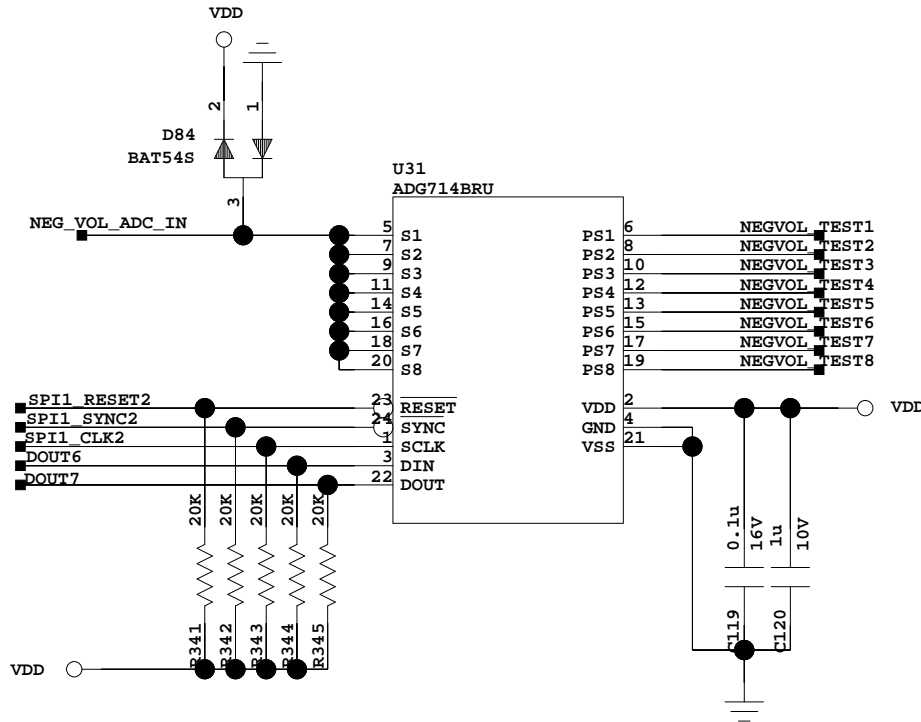


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TITLE	Name
Doc No.	REV 0.1
Designed by	P.Code

### Security Classification:

**V0.1--->V1.0**

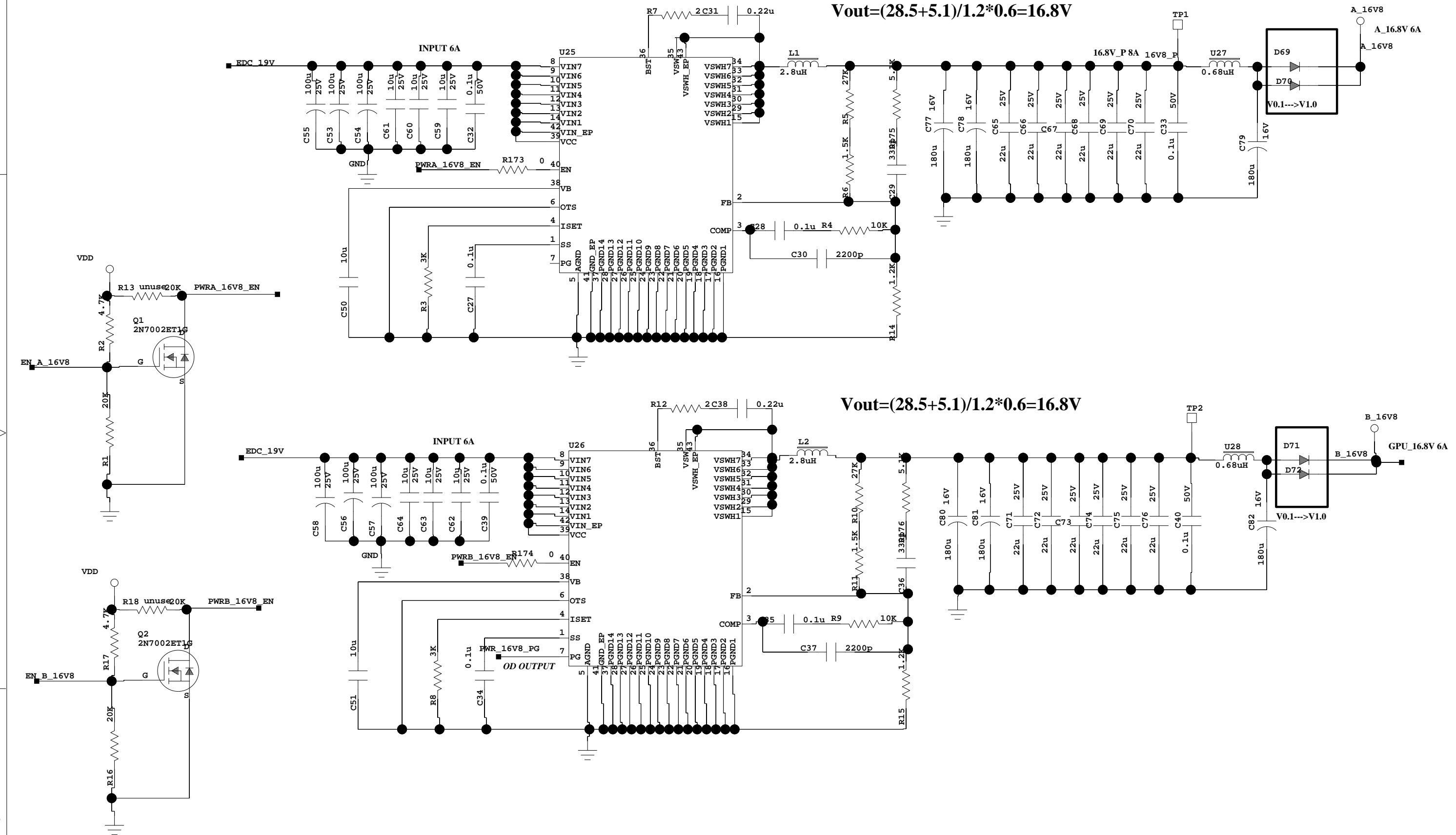
## VOLTAGE TEST SWITCH



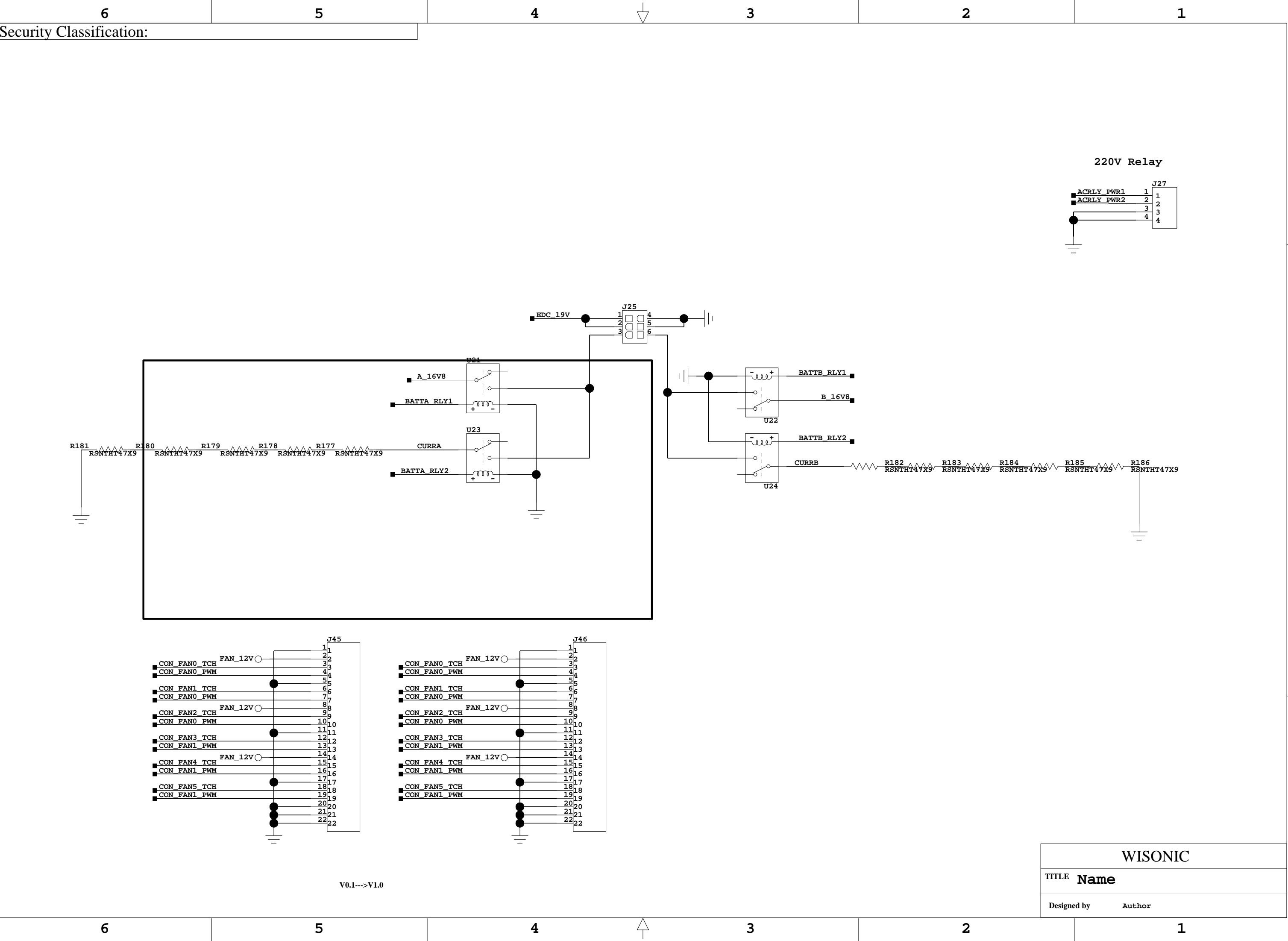
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<b>TITLE   Name</b>	
<b>Doc No.</b>	<b>REV</b> <div style="text-align: right;">0.1</div>
<b>Designed by</b>	<b>P.Code</b>
<b>AUTHOR</b>	



Security Classification:
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WISONIC	
TITLE	<b>Name</b>
Designed by	Author



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