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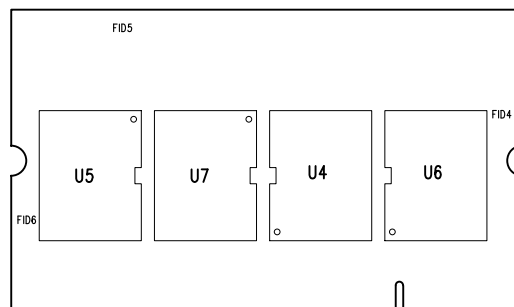
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PRELIMINARY

REVISION RECORD			
LTR	ECO NO:	APPROVED:	DATE:



LAYER: ASSEMBLY BOTTOM		COMPANY: CARNEGIE MELLON		
DESIGN BY: CiREXX 		PART NUMBER: XXXXX	ASSEMBLY NUMBER: XXXXX	REV. 1
TITLE: ASSEMBLY DRAWING BOTTOM NAND + DDR 1V8IO SODIMM				
DESIGN: Gene McKenna	DATE: 11-9-09			
CHECKED BY:	DATE: XX-XX-XX			
ENGINEERING:	DATE: XX-XX-XX	SIZE A	CIREXX DOCUMENT NUMBER: 83232	
RELEASED:	DATE:	CIREXX FILE NAME: 83232-A.pcb	SHEET 2 OF 2	SCALE: 1/1

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