S - System state		c1	c2	c3	c4	
λ -		1	0.5	0.15	0.2	
μ -						
c1 -						
c2 -						
c3 -						
c4 -						
Handling Counter						
Rejecting Counter						
C - Idle Counter						
E - Efficienty						
SS	**λ**	**μ**	**HC**	**RC**	**IC**	**E**
free	0.5	0.001	0	12	75	-16.8
free	0.5	0.01	0	12	75	-16.8
free	0.5	0.02	0	12	75	-16.8
free	0.5	0.04	0	12	75	-16.8
free	0.5	0.05	1	11	84	-17.95
free	0.5	0.1	2	10	82	-16.9
free	0.5	0.15	3	9	233	-46.45
free	0.5	0.2	4	8	230	-45.2
free	**0.5**	**0.25**	**6**	**6**	**80**	**-13.9**
free	0.5	0.3	6	6	230	-43.9
free	0.5	0.35	6	6	338	-65.5
free	0.5	0.4	6	6	416	-81.1
free	0.5	0.45	6	6	476	-93.1
free	0.5	0.5	11	1	150	-24.65
free	0.5	0.6	12	0	218	-37.6
free	0.5	0.7	12	0	326	-59.2
free	0.5	0.8	12	0	410	-76
free	0.5	0.9	12	0	470	-88
free	0.5	1	12	0	518	-97.6
free	0.5	2	12	0	746	-143.2