H6 STD GPIO

GPIO口	名字	功能复用（功能0为输入，功能1为输出）					功能定义	功能分类	有效电平	说明	PIN电源	PIN 电源范围 (V)	注意
		功能2	功能3	功能4	功能5	功能6							
PC	PC0	NAND_WE		SPI0_CLK			NAND_WE	NAND/EMMC	–		VCC_PC	1.8V/3.3V	
	PC1	NAND_ALE	SDC2_DS				NAND_ALE / SDC2_DS		–				
	PC2	NAND_CLE		SPI0_MOSI			NAND_CLE		–				
	PC3	NAND_CE0		SPI0_MISO			NAND_CE0		–				
	PC4	NAND_RE	SDC2_CLK				NAND_RE / SDC2_CLK		–				
	PC5	NAND_RB0	SDC2_CMD	SPI0_CS			NAND_RB0 / SDC2_CMD		–				
	PC6	NAND_DQ0	SDC2_D0	SPI0_HOLD			NAND_DQ0 / SDC2_D0		–				
	PC7	NAND_DQ1	SDC2_D1	SPI0_WP			NAND_DQ1 / SDC2_D1		–				
	PC8	NAND_DQ2	SDC2_D2				NAND_DQ2 / SDC2_D2		–				
	PC9	NAND_DQ3	SDC2_D3				NAND_DQ3 / SDC2_D3		–				
	PC10	NAND_DQ4	SDC2_D4				NAND_DQ4 / SDC2_D4		–				
	PC11	NAND_DQ5	SDC2_D5				NAND_DQ5 / SDC2_D5		–				
	PC12	NAND_DQ6	SDC2_D6				NAND_DQ6 / SDC2_D6		–				
	PC13	NAND_DQ7	SDC2_D7				NAND_DQ7 / SDC2_D7		–				
	PC14	NAND_DQS	SDC2_RST				NAND_DQS / SDC2_RST		–				
	PC15	NAND_CE1					NAND_CE1		–				
	PC16	NAND_RB1					NAND_RB1		–				
PD	PD0	LCD0_D2	TS0_CLK	CSI_PCLK	RGMII_RXD3/RMI		GND	–	–		VCC_PD	3.3/2.5V	
	PD1	LCD0_D3	TS0_ERR	CSI_MCLK	RGMII_RXD2/RMI		GND	–	–				
	PD2	LCD0_D4	TS0_SYNC	CSI_HSYNC	RGMII_RXD1/RMI		GND	–	–				
	PD3	LCD0_D5	TS0_DVLD	CSI_VSYNC	RGMII_RXD0/RMI		GND	–	–				
	PD4	LCD0_D6	TS0_D0	CSI_D0	RGMII_RXCK/RM		GND	–	–				
	PD5	LCD0_D7	TS0_D1	CSI_D1	RGMII_RXCTL/R		GND	–	–				
	PD6	LCD0_D10	TS0_D2	CSI_D2	RGMII_NULL/RMI		GND	–	–				
	PD7	LCD0_D11	TS0_D3	CSI_D3	RGMII_TXD3/RMI		GND	–	–				
	PD8	LCD0_D12	TS0_D4	CSI_D4	RGMII_TXD2/RMI		GND	–	–				
	PD9	LCD0_D13	TS0_D5	CSI_D5	RGMII_TXD1/RMI		GND	–	–				
	PD10	LCD0_D14	TS0_D6	CSI_D6	RGMII_TXD0/RMI		GND	–	–				
	PD11	LCD0_D15	TS0_D7	CSI_D7	RGMII_TXCK/RMI		GND	–	–				
	PD12	LCD0_D18	TS1_CLK	CSI_SCK	RGMII_TXCTL/R		GND	–	–				
	PD13	LCD0_D19	TS1_ERR	CSI_SDA	RGMII_CLKIN/RM		GND	–	–				
	PD14	LCD0_D20	TS1_SYNC	DMIC_CLK	CSI_D8		GND	–	–				
	PD15	LCD0_D21	TS1_DVLD	DMIC_DATA0	CSI_D9		GND	–	–				
	PD16	LCD0_D22	TS1_D0	DMIC_DATA1			GND	–	–				
	PD17	LCD0_D23	TS2_CLK	DMIC_DATA2			GND	–	–				
	PD18	LCD0_CLK	TS2_ERR	DMIC_DATA3			GND	–	–				
	PD19	LCD0_DE	TS2_SYNC	UART2_TX	MDC		GND	–	–				
	PD20	LCD0_HSYNC	TS2_DVLD	UART2_RX	MDIO		GND	–	–				
	PD21	LCD0_VSYNC	TS2_D0	UART2_RTS			GND	–	–				
	PD22	PWM0	TS3_CLK	UART2_CTS			GND	–	–				
	PD23	TWI2_SCK	TS3_ERR	UART3_TX	JTAG_MS		GND	–	–				
	PD24	TWI2_SDA	TS3_SYNC	UART3_RX	JTAG_CK		GND	–	–				
	PD25	TWI0_SCK	TS3_DVLD	UART3_RTS	JTAG_DO		GND	–	–				
	PD26	TWI0_SDA	TS3_D0	UART3_CTS	JTAG_DI		GND	–	–				
PF	PF0	SDC0_D1	JTAG_MS1			PF_EINT0	SDC0_D1	TF-CARD	–		VCC_IO	3.3V	
	PF1	SDC0_D0	JTAG_DI1			PF_EINT1	SDC0_D0		–				
	PF2	SDC0_CLK	UART0_TX			PF_EINT2	SDC0_CLK		–				
	PF3	SDC0_CMD	JTAG_DO1			PF_EINT3	SDC0_CMD		–				
	PF4	SDC0_D3	UART0_RX			PF_EINT4	SDC0_D3		–				
	PF5	SDC0_D2	JTAG_CK1			PF_EINT5	SDC0_D2		–				
	PF6					PF_EINT6	SDC0_DET		–				
PG	PG0	SDC1_CLK				PG_EINT0	WL-SDIO-CLK	WIFI-SDIO	–		VCC_PG	1.8V/3.3V	
	PG1	SDC1_CMD				PG_EINT1	WL-SDIO-CMD		–				
	PG2	SDC1_D0				PG_EINT2	WL-SDIO-D0		–				
	PG3	SDC1_D1				PG_EINT3	WL-SDIO-D1		–				
	PG4	SDC1_D2				PG_EINT4	WL-SDIO-D2		–				
	PG5	SDC1_D3				PG_EINT5	WL-SDIO-D3		–				
	PG6	UART1_TX				PG_EINT6	BT-UART-TX	BT-UART	–				
	PG7	UART1_RX				PG_EINT7	BT-UART-RX		–				
	PG8	UART1_RTS	PLL_STA_DB	SIM0_VPPEN		PG_EINT8	BT-UART-RTS		–				
	PG9	UART1_CTS	PLL_TEST_GPIO	SIM0_VPPPP		PG_EINT9	BT-UART-CTS		–				
	PG10	PCM2_SYNC	H_PCM2_SYNC	SIM0_PWREN		PG_EINT10	BT-PCM-SYNC	BT-PCM	–				
	PG11	PCM2_CLK	H_PCM2_CLK	SIM0_CLK	BIST_RESULT0	PG_EINT11	BT-PCM-CLK		–				
	PG12	PCM2_DOUT	H_PCM2_DOUT	SIM0_DATA	BIST_RESULT1	PG_EINT12	BT-PCM-DOUT		–				
	PG13	PCM2_DIN	H_PCM2_DIN	SIM0_RST	BIST_RESULT2	PG_EINT13	BT-PCM-DIN						
	PG14	PCM2_MCLK	H_PCM2_MCLK	SIM0_DET	BIST_RESULT3	PG_EINT14			–				
	PH0	UART0_TX	PCM0_SYNC	H_PCM0_SYNC	SIM1_VPPEN	PH_EINT0	CPUX-UTX	UART	–				

								UART				
PH	PH1	UART0_RX	PCM0_CLK	H_PCM0_CLK	SIM1_VPPPP	PH_EINT1	CPUX-URX		–		VCC_IO	3.3V
	PH2	IR_TX	PCM0_DOUT	H_PCM0_DOUT	SIM1_PWREN	PH_EINT2	DDC_EN	HDMI	1	GPIO的有效电平和定义不能更改		
	PH3	SPI1_CS	PCM0_DIN	H_PCM0_DIN	SIM1_CLK	PH_EINT3	FLOATING		–			
	PH4	SPI1_CLK	PCM0_MCLK	H_PCM0_MCLK	SIM1_DATA	PH_EINT4	FLOATING		–			
	PH5	SPI1_MOSI	SPDIF_MCLK	TWI1_SCK	SIM1_RST	PH_EINT5	FLOATING		–			
	PH6	SPI1_MISO	SPDIF_IN	TWI1_SDA	SIM1_DET	PH_EINT6	FLOATING		–			
	PH7		SPDIF_OUT			PH_EINT7	SPDIF_OUT		–			
	PH8	HSCL				PH_EINT8	HSCL		–			
	PH9	HSDA				PH_EINT9	HSDA	HDMI	–			
	PH10	HCEC				PH_EINT10	HCEC		–			
PL(CPUs)	PL0		S_TWI_SCK			S_PL_EINT0	PMU-SCK	PMU	–		VCC_PL	3.3V
	PL1		S_TWI_SDA			S_PL_EINT1	PMU-SDA		–			
	PL2	S_UART_TX				S_PL_EINT2	RECOVERY		0	GPIO的有效电平和定义不能更改		
	PL3	S_UART_RX				S_PL_EINT3	LINK-LED		1	GPIO的有效电平和定义不能更改		
	PL4	S_JTAG_MS				S_PL_EINT4	PWR-LED		1	GPIO的有效电平和定义不能更改		
	PL5	S_JTAG_CK				S_PL_EINT5	USB0-DRVVBUS		1	GPIO的有效电平和定义不能更改		
	PL6	S_JTAG_DO				S_PL_EINT6	MUTE		1	GPIO的有效电平和定义不能更改		
	PL7	S_JTAG_DI				S_PL_EINT7	STATUS-LED		1	GPIO的有效电平和定义不能更改		
	PL8	S_PWM0				S_PL_EINT8	BT-WIFI-ON		1	GPIO的有效电平和定义不能更改		
	PL9	S_IR_RX				S_PL_EINT9	IR-RX		–			
	PL10	S_OWC	S_PWM1			S_PL_EINT10			–			
PM(CPUs)	PM0					S_PM_EINT0	WL-WAKE-AP	WIFI-BT-CTR	1	GPIO的有效电平和定义不能更改	VCC_PM	1.8V/3.3V
	PM1					S_PM_EINT1	BT-WAKE-AP		1	GPIO的有效电平和定义不能更改		
	PM2					S_PM_EINT2	AP-WAKE-BT		1	GPIO的有效电平和定义不能更改		
	PM3					S_PM_EINT3	WL-REG-ON		1	GPIO的有效电平和定义不能更改		
	PM4					S_PM_EINT4	BT-REG-ON		1	GPIO的有效电平和定义不能更改		