1. Description

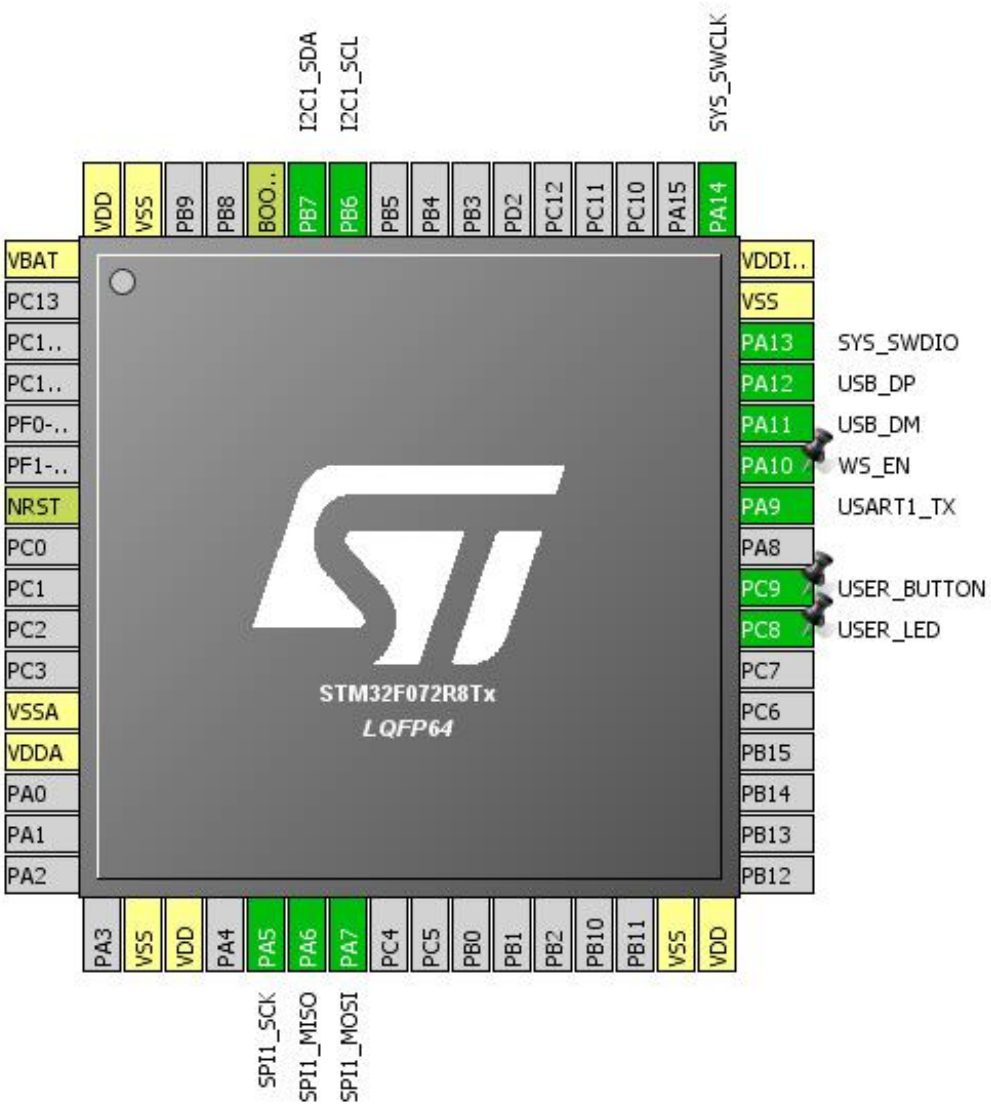
1.1. Project

Project Name	lul
Board Name	lul
Generated with:	STM32CubeMX 4.22.0
Date	08/10/2017

1.2. MCU

MCU Series	STM32F0
MCU Line	STM32F0x2
MCU name	STM32F072R8Tx
MCU Package	LQFP64
MCU Pin number	64

2. Pinout Configuration

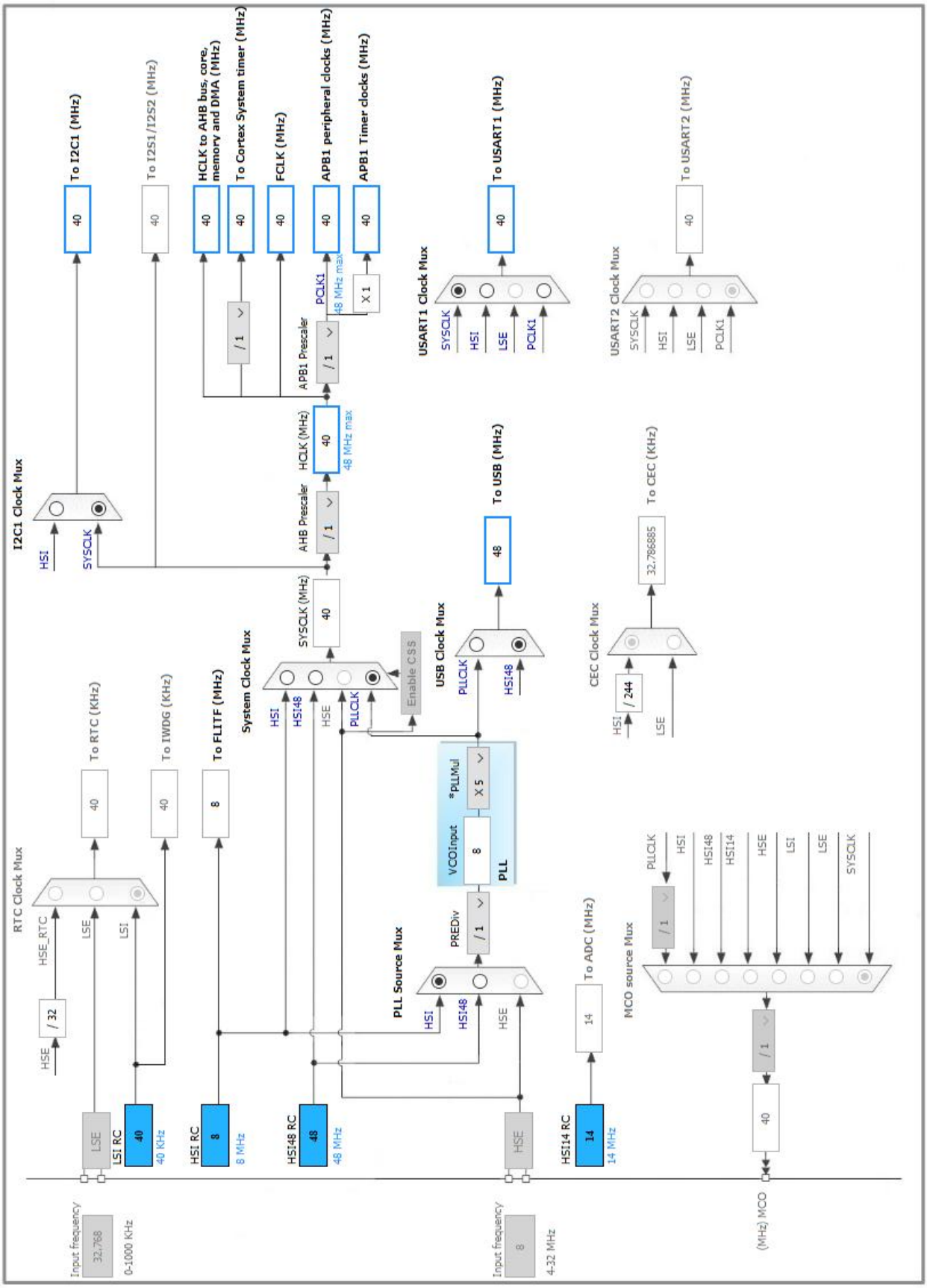


3. Pins Configuration

Pin Number LQFP64	Pin Name (function after reset)	Pin Type	Alternate Function(s)	Label
1	VBAT	Power		
7	NRST	Reset		
12	VSSA	Power		
13	VDDA	Power		
18	VSS	Power		
19	VDD	Power		
21	PA5	I/O	SPI1_SCK	
22	PA6	I/O	SPI1_MISO	
23	PA7	I/O	SPI1_MOSI	
31	VSS	Power		
32	VDD	Power		
39	PC8 *	I/O	GPIO_Output	USER_LED
40	PC9 *	I/O	GPIO_Input	USER_BUTTON
42	PA9	I/O	USART1_TX	
43	PA10 *	I/O	GPIO_Output	WS_EN
44	PA11	I/O	USB_DM	
45	PA12	I/O	USB_DP	
46	PA13	I/O	SYS_SWDIO	
47	VSS	Power		
48	VDDIO2	Power		
49	PA14	I/O	SYS_SWCLK	
58	PB6	I/O	I2C1_SCL	
59	PB7	I/O	I2C1_SDA	
60	BOOT0	Boot		
63	VSS	Power		
64	VDD	Power		

* The pin is affected with an I/O function

4. Clock Tree Configuration



5. IPs and Middleware Configuration

5.1. I2C1

I2C: I2C

5.1.1. Parameter Settings:

Timing configuration:

I2C Speed Mode	**Fast Mode ***
I2C Speed Frequency (KHz)	400
Rise Time (ns)	0
Fall Time (ns)	0
Coefficient of Digital Filter	0
Analog Filter	Enabled
Timing	**0x00301347 ***

Slave Features:

Clock No Stretch Mode	Disabled
General Call Address Detection	Disabled
Primary Address Length selection	7-bit
Dual Address Acknowledged	Disabled
Primary slave address	0

5.2. SPI1

Mode: Full-Duplex Master

5.2.1. Parameter Settings:

Basic Parameters:

Frame Format	Motorola
Data Size	**8 Bits ***
First Bit	MSB First

Clock Parameters:

Prescaler (for Baud Rate)	**4 ***
Baud Rate	**10.0 MBits/s ***
Clock Polarity (CPOL)	Low
Clock Phase (CPHA)	1 Edge

Advanced Parameters:

CRC Calculation	Disabled
NSSP Mode	Enabled
NSS Signal Type	Software

5.3. SYS

mode: Debug Serial Wire

Timebase Source: SysTick

5.4. USART1

Mode: Single Wire (Half-Duplex)

5.4.1. Parameter Settings:

Basic Parameters:

Baud Rate	**115200 ***
Word Length	**8 Bits (including Parity) ***
Parity	None
Stop Bits	1

Advanced Parameters:

Data Direction	**Transmit Only ***
Over Sampling	16 Samples
Single Sample	Disable

Advanced Features:

Auto Baudrate	Disable
TX Pin Active Level Inversion	Disable
RX Pin Active Level Inversion	Disable
Data Inversion	Disable
TX and RX Pins Swapping	Disable
Overrun	Enable
DMA on RX Error	Enable
MSB First	Disable

5.5. USB

mode: Device (FS)

5.5.1. Parameter Settings:

Basic Parameters:

Speed	Full Speed 12MBit/s
Endpoint 0 Max Packet size	64 Bytes
Physical interface	Internal Phy

Power Parameters:

Low Power	Disabled
Link Power Management	Disabled

5.6. USB_DEVICE

Class For FS IP: Human Interface Device Class (HID)

5.6.1. Parameter Settings:

Basic Parameters:

USBD_MAX_NUM_INTERFACES (Maximum number of supported interfaces)	1
USBD_MAX_NUM_CONFIGURATION (Maximum number of supported configuration)	1
USBD_MAX_STR_DESC_SIZ (Maximum size for the string descriptors)	512
USBD_SUPPORT_USER_STRING (Enable user string descriptor)	Disabled
USBD_SELF_POWERED (Enabled self power)	Enabled
USBD_DEBUG_LEVEL (USBD Debug Level)	0: No debug message

5.6.2. Device Descriptor:

Device Descriptor:

VID (Vendor IDentifier)	1155
LANGID_STRING (Language Identifier)	English(United States)
MANUFACTURER_STRING (Manufacturer Identifier)	STMicroelectronics

Device Descriptor FS:

PID (Product IDentifier)	22315
PRODUCT_STRING (Product Identifier)	STM32 Human interface
SERIALNUMBER_STRING (Serial number)	00000000001A
CONFIGURATION_STRING (Configuration Identifier)	HID Config
INTERFACE_STRING (Interface Identifier)	HID Interface

*** User modified value**

6. System Configuration

6.1. GPIO configuration

IP	Pin	Signal	GPIO mode	GPIO pull/up pull down	Max Speed	User Label
I2C1	PB6	I2C1_SCL	Alternate Function Open Drain	Pull-up	**High ***	
	PB7	I2C1_SDA	Alternate Function Open Drain	Pull-up	**High ***	
SPI1	PA5	SPI1_SCK	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	
	PA6	SPI1_MISO	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	
	PA7	SPI1_MOSI	Alternate Function Push Pull	No pull-up and no pull-down	**High ***	
SYS	PA13	SYS_SWDIO	n/a	n/a	n/a	
	PA14	SYS_SWCLK	n/a	n/a	n/a	
USART1	PA9	USART1_TX	Alternate Function Open Drain	Pull-up	**High ***	
USB	PA11	USB_DM	n/a	n/a	n/a	
	PA12	USB_DP	n/a	n/a	n/a	
GPIO	PC8	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	USER_LED
	PC9	GPIO_Input	Input mode	**Pull-up ***	n/a	USER_BUTTON
	PA10	GPIO_Output	Output Push Pull	No pull-up and no pull-down	Low	WS_EN

6.2. DMA configuration

nothing configured in DMA service

6.3. NVIC configuration

Interrupt Table	Enable	Preenmption Priority	SubPriority
Non maskable interrupt	true	0	0
Hard fault interrupt	true	0	0
System service call via SWI instruction	true	0	0
Pendable request for system service	true	0	0
System tick timer	true	0	0
USB global interrupt / USB wake-up interrupt through EXTI line 18	true	0	0
PVD and VDDIO2 supply comparator interrupts through EXTI lines 16 and 31	unused		
Flash global interrupt	unused		
RCC and CRS global interrupts	unused		
I2C1 event global interrupt / I2C1 wake-up interrupt through EXTI line 23	unused		
SPI1 global interrupt	unused		
USART1 global interrupt / USART1 wake-up interrupt through EXTI line 25	unused		

*** User modified value**

7. Power Consumption Calculator report

7.1. Microcontroller Selection

Series	STM32F0
Line	STM32F0x2
MCU	STM32F072R8Tx
Datasheet	025004_Rev4

7.2. Parameter Selection

Temperature	25
Vdd	3.6

8. Software Project

8.1. Project Settings

Name	Value
Project Name	lul
Project Folder	C:\Users\Allen\Desktop\repos\lulboard\firmware
Toolchain / IDE	MDK-ARM V5
Firmware Package Name and Version	STM32Cube FW_F0 V1.8.0

8.2. Code Generation Settings

Name	Value
STM32Cube Firmware Library Package	Copy only the necessary library files
Generate peripheral initialization as a pair of '.c/.h' files	No
Backup previously generated files when re-generating	No
Delete previously generated files when not re-generated	Yes
Set all free pins as analog (to optimize the power consumption)	No