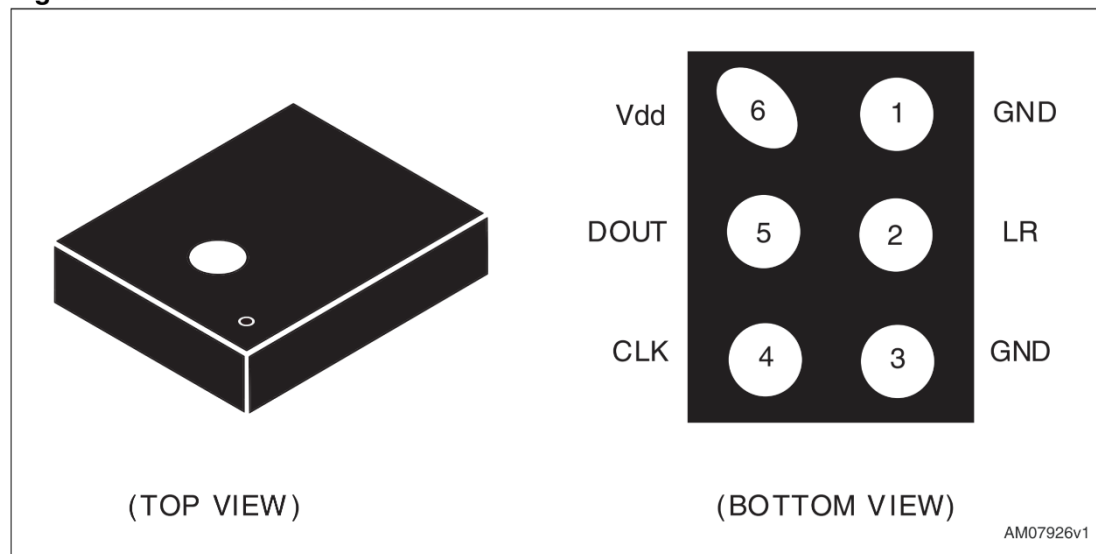


MP45DT02

Figure 1. Pin connections



- Pin description
 - #1 GND: 0V supply
 - #2 LR: Left / Right channel selection
 - #3 GND: 0V supply
 - #4 CLK: Clock
 - #5 DOUT: Left/right PDM data output
 - #6 Vdd: Power supply
- Functionality
 - L/R channel selection

Pin L/R must connect to GND or Vdd

Table 8. L/R channel selection

L/R	CLK low	CLK high
GND	Data valid	High impedance
Vdd	High impedance	Data valid

Figure 5. MP45DT02 electrical connections for stereo configuration

