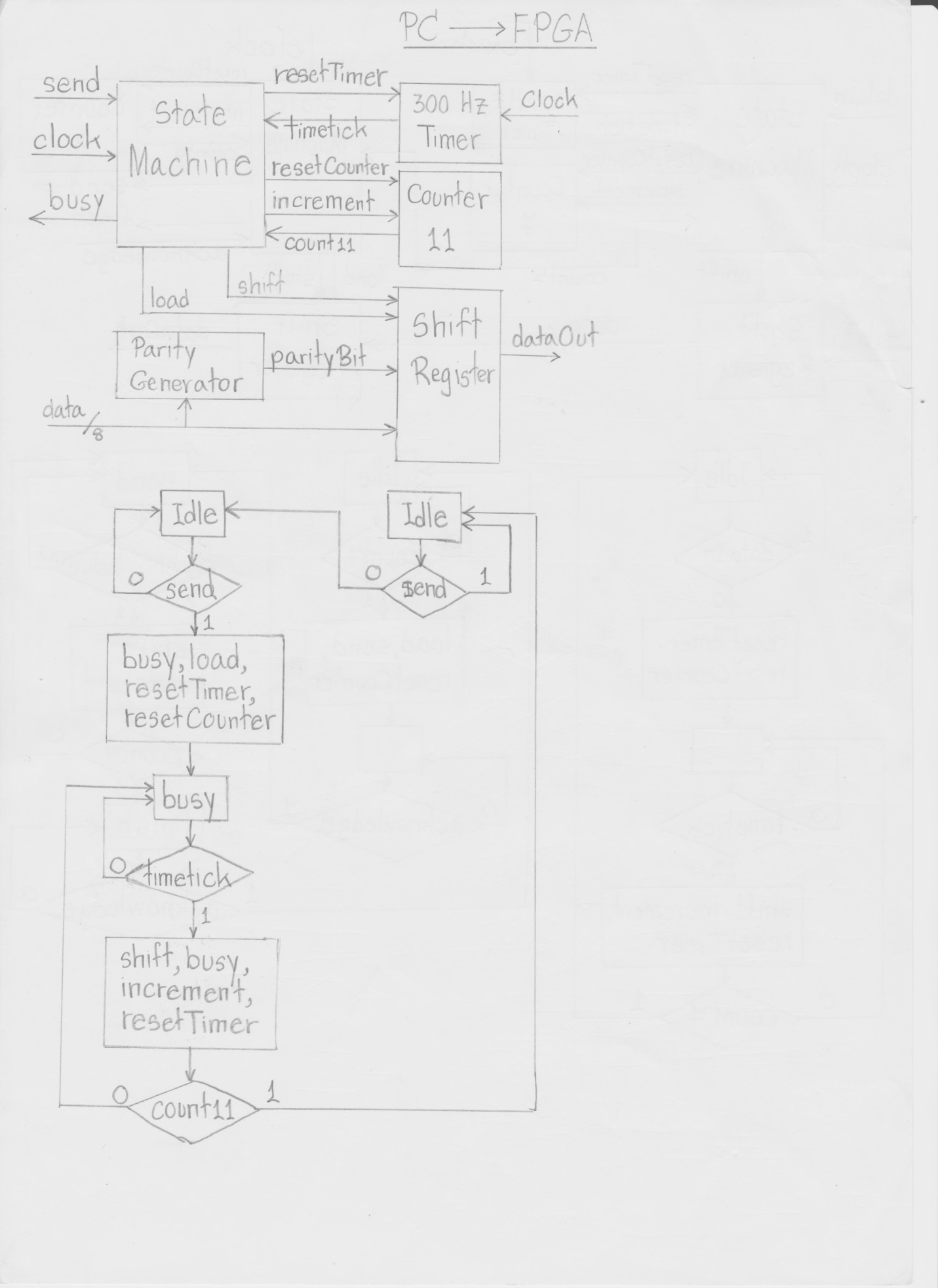
PC -> FPGA



FPGA(Transmitter) -> FPGA(Receiver)

