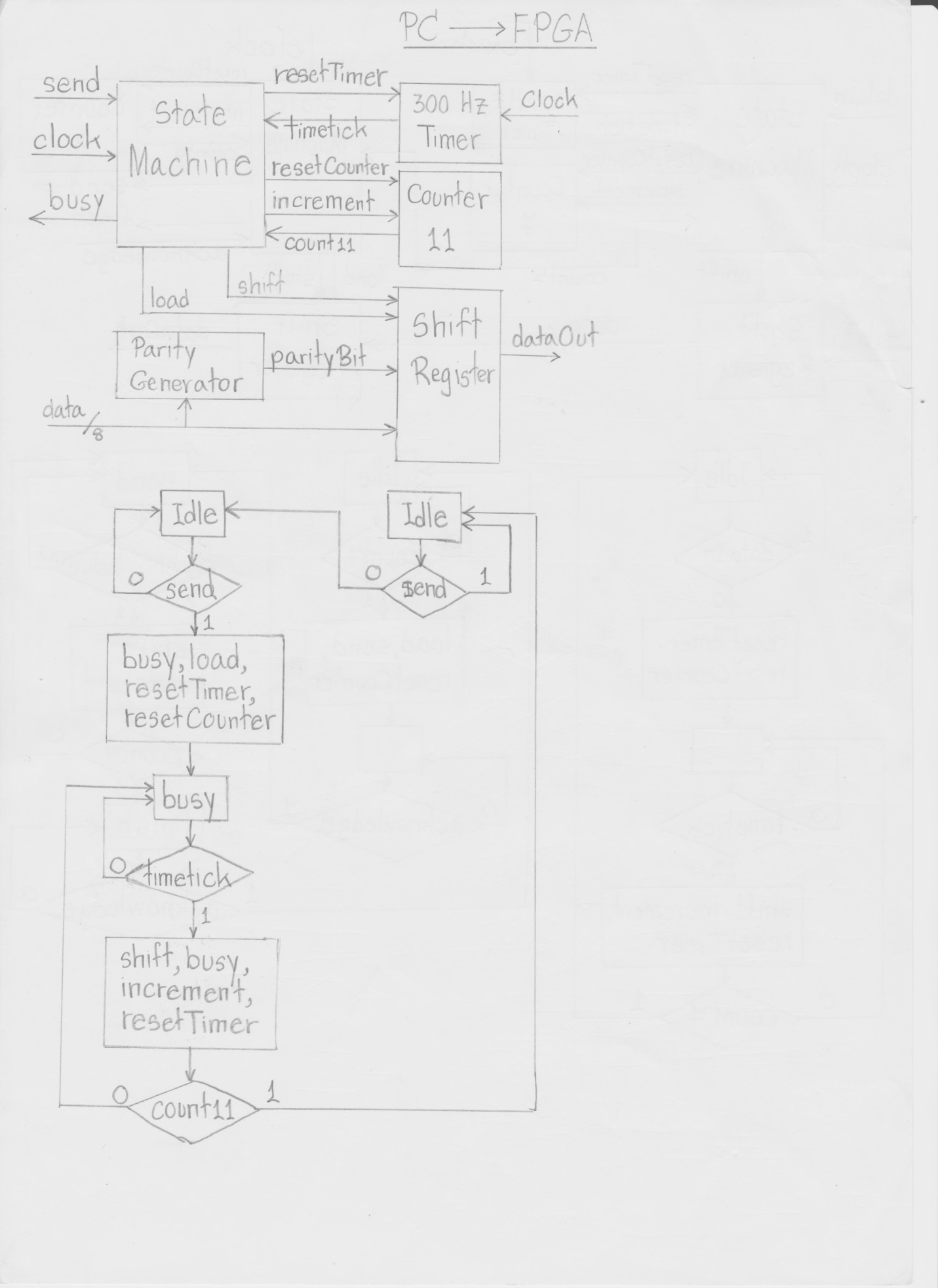
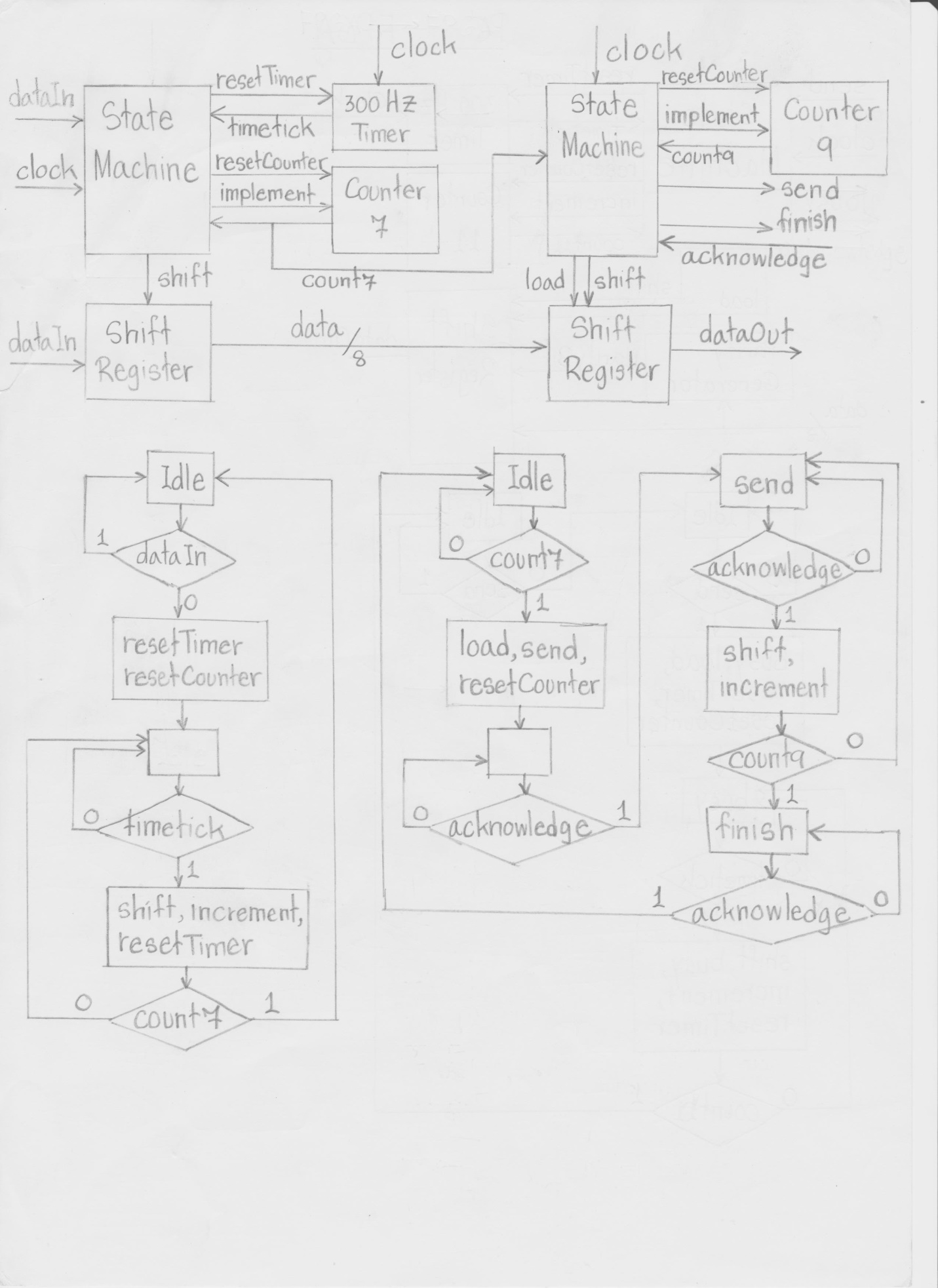
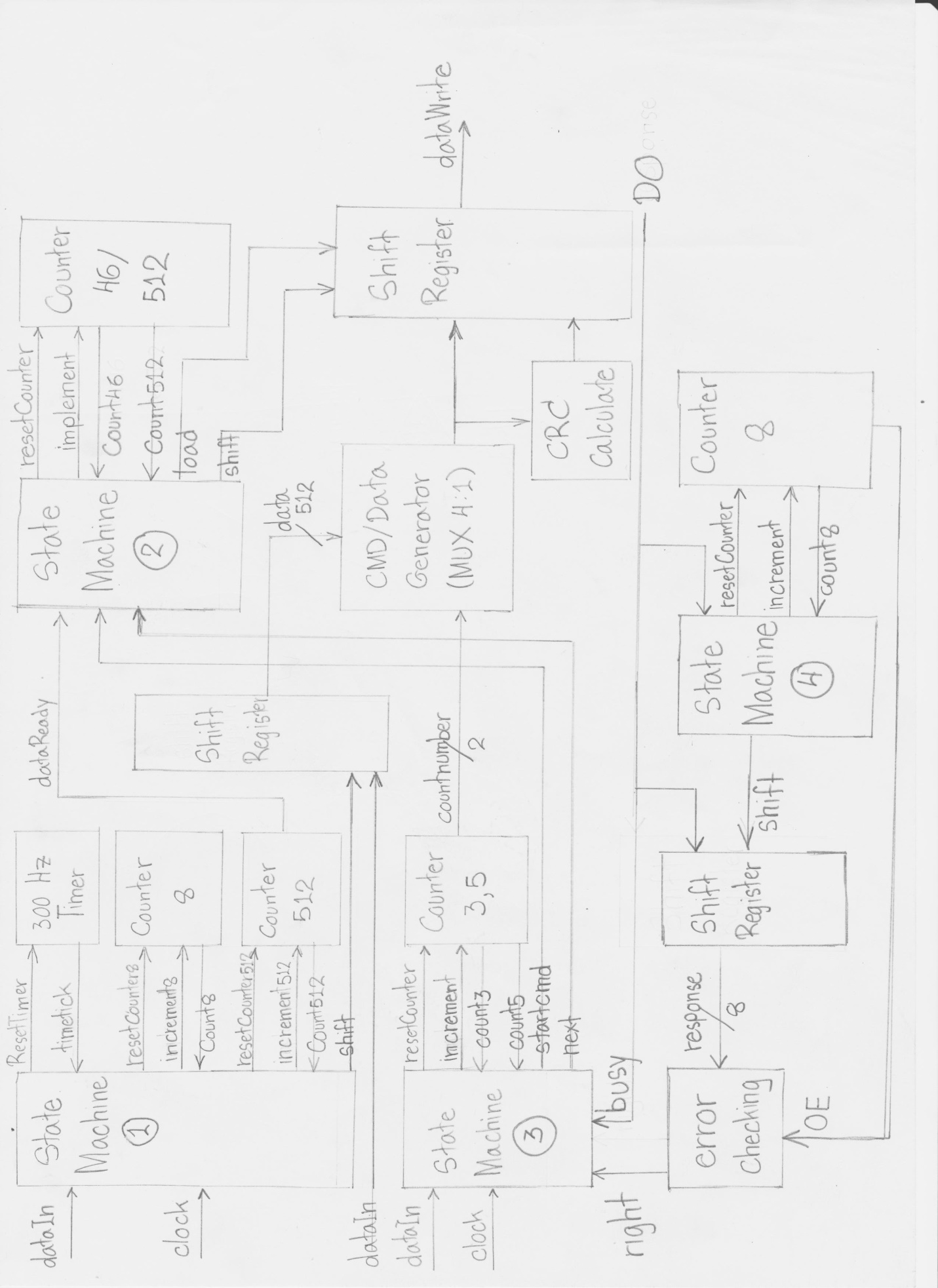
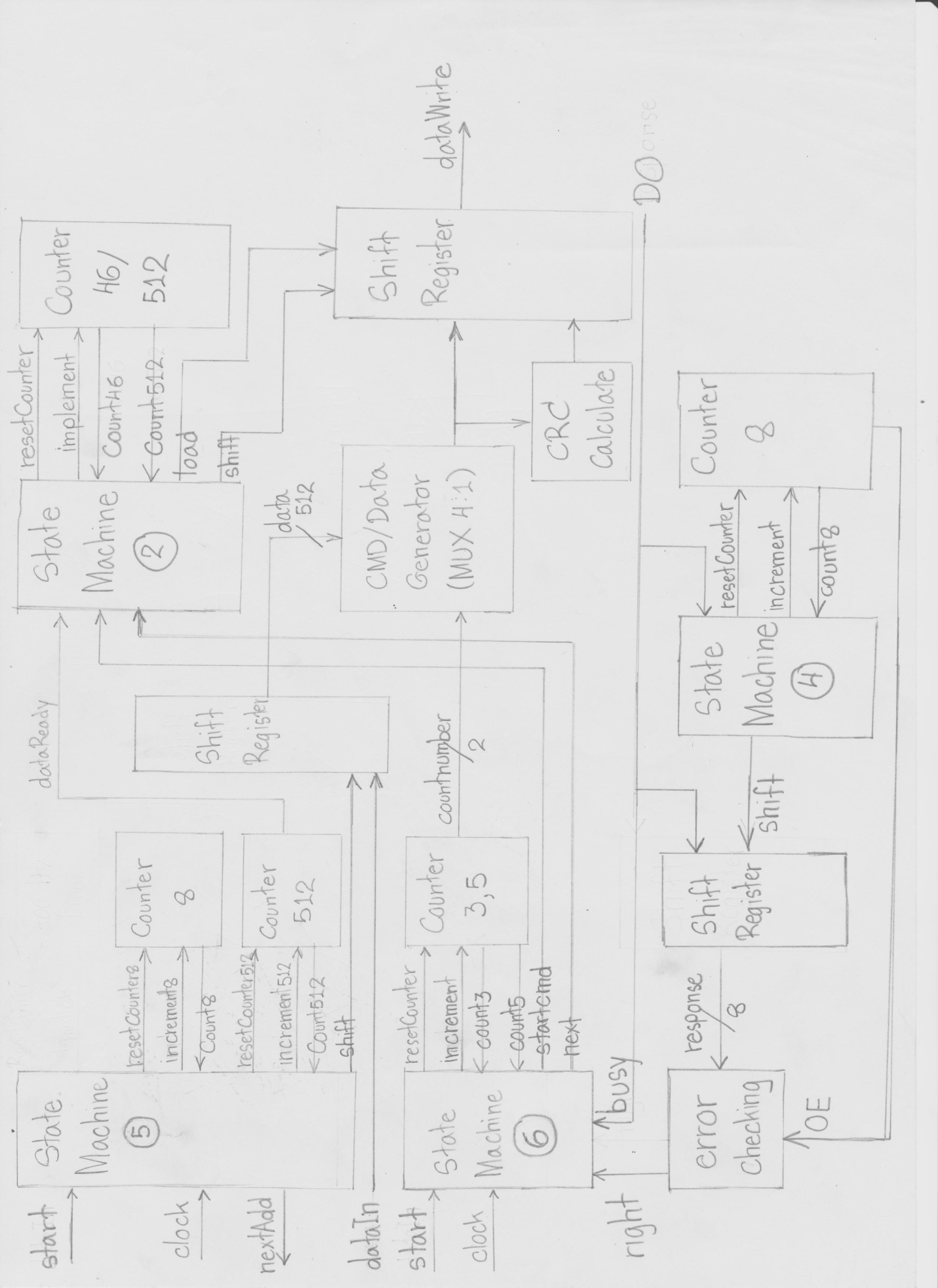
PC -> FPGA



FPGA(Transmitter) -> FPGA(Receiver)



FPGA from PC (300 baud) -> SD Card

FPGA(ROM) -> SD card

