Automatic Parallelization and Transparent Fault Tolerance



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Local evolution of scientific computing

 Serial Fortran programs Simple, imperative.

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Similar. More pleasant syntax (Free form source code!)

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- Vendor communication libraries Non-portable, but parallel

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- MPI everywhere (inter- and intra-node)
 De facto standard emerges to solve portability

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- C++
 - Object oriented goodness (and more!)

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- MPI everywhere (inter- and intra-node)
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- MPI+{Pthreads, OpenMP, OpenCL, CUDA, etc}
 We augment MPI now with other explicit parallel models

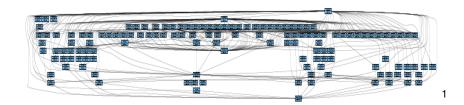
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- MPI+{Pthreads, OpenMP, OpenCL, CUDA, etc}
- Parallel runtimes, e.g., Cilk Plus, Intel Threading Building Blocks, Stanford's Legion, etc.
 - Automated scheduling for irregularly shaped problems.

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Currently MPI+threading model dominates.

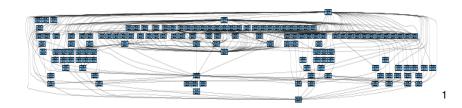
In Search of Automatic Parallelization or at least automatic scheduling



S3D task dependency, combustion chemistry calculation

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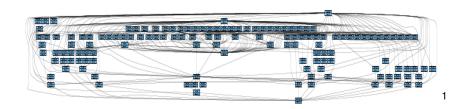


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Simplest interesting chemistry, task graph much larger with more complex reactants. Schedule by hand?

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This comes for free in the pure functional world

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Obviously, we can't just hope to get lucky on a months-long calculation

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With referential transparency, this is a much smaller problem

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But, these are not computer scientists:

- Passing functions as arguments is familiar (since early Fortran).
- · Implicit space leaks are confusing.
- Non-strict evaluation is largely irrelevant.
- Unboxed primitives and arrays of the same (unboxed vector) are the primary data.

Trends in functional programming

Appreciation of the virtues of strict-by-default semantics. GHC Strict and StrictData for HPC

Trends in functional programming

- Appreciation of the virtues of strict-by-default semantics.
- Transparent fault tolerance.
 Stewart's HdpH-RS

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Trends in scientific computing

- Implementation of pure functional computational concepts/components.
- Recognition that checkpoint/restart is increasingly intractable and unscalable.
 - Legion is "pure with respect to X"
 - Also adding task-restarting for fault-tolerance

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Strictness analysis Can be inconclusive.

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- Strictness analysis
- Bang patterns in bindings Prone to error. Hard to read.

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- Par/pseq, other specifications on expressions
- Speculative evaluation
 Difficult to get right without annotation.

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Similarly constructors.

Update: Strict let bindings.

Musings

Suppose we had a strict(-er) pure functional language implementation than Haskell?

- Could we keep a large number of hardware threads (e.g., up to 256) busy with *implicit* parallelism?
 - With what parallel efficiency?
 - With different scheduling strategies?

Musings

Suppose we had a strict(-er) pure functional language implementation than Haskell?

- Could we keep a large number of hardware threads (e.g., up to 256) busy with *implicit* parallelism?
 - With what parallel efficiency?
 - With different scheduling strategies?
- How often do new functional programmers/old scientific programmers make essential use of non-strictness? (very rarely)
 - Laziness? (never)

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- Fault tolerance TBD.

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- There exist conduits for getting student interns up to speed inexpensively.

Technical hurdles: tail calling and unwinding the stack

Following SPJ/STG our control structure is a stack of continuations.

Each continuation contains an address to go to next, not go back to, so we want something like goto or tail calling.

SPJ identifies defines the term *code label* as something that can be

- used to name an arbitrary block of code;
- can be manipulated, i.e., stored for later use;
- can be used as the destination of a jump (not just call).

setjmp/longjmp; Significant overhead and bookkeeping

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- giant switch; Ugly hack, not really linkable

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while (1) f = (*f)();
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Our initial implementation used this approach

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Fun, but out of scope

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- implement an intermediate language, e.g., C--/Cmm, where tail call is primitive;
- generate assembly code, e.g., LLVM, directly. Ambitious. Haskell LLVM bindings are unreliable

Modern compilers to the rescue: sibling call

Clang/LLVM and GCC implement a restricted form of tail call, the sibling call, i.e., jump reusing the TOS frame. Here

```
void f() {
g();
```

function g is jumped to; there is no (implicit) return following g();.

Critically, this also works for indirect calls to previously stored addresses.

```
(getInfoPtr(stgCurVal.op)->entryCode)();
```

Sibling Call

The restrictions are due to the x86(-64)/*nix/C calling conventions—caller clean-up—and the possibility of pointer aliasing.

Sufficient formula for recent gcc and Clang/LLVM on Linux or Mac OS X

- Call in leaf position of CFG;
- Caller and callee have the same type signature
- No addresses taken of formal params;
- -O3.

Obviously this is not portable.

Unwinding stacks

thread stacks for shared memory parallelism.

In one version of GHC, Harris et al. identify the need to unwind

Again, we do not want to contemplate using, e.g., setimp/longimp.

Therefore, we do not want the C main stack or Pthreads stacks to grow.

Exception: calls to the runtime.

Again, sibling call saves us.

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- GHC tie-in and parallel implementation (hopefully) this summer
- Experimentation with non-trivial code
- Fault tolerance sometime thereafter.

Acknowledgments

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Thank you

Questions?