

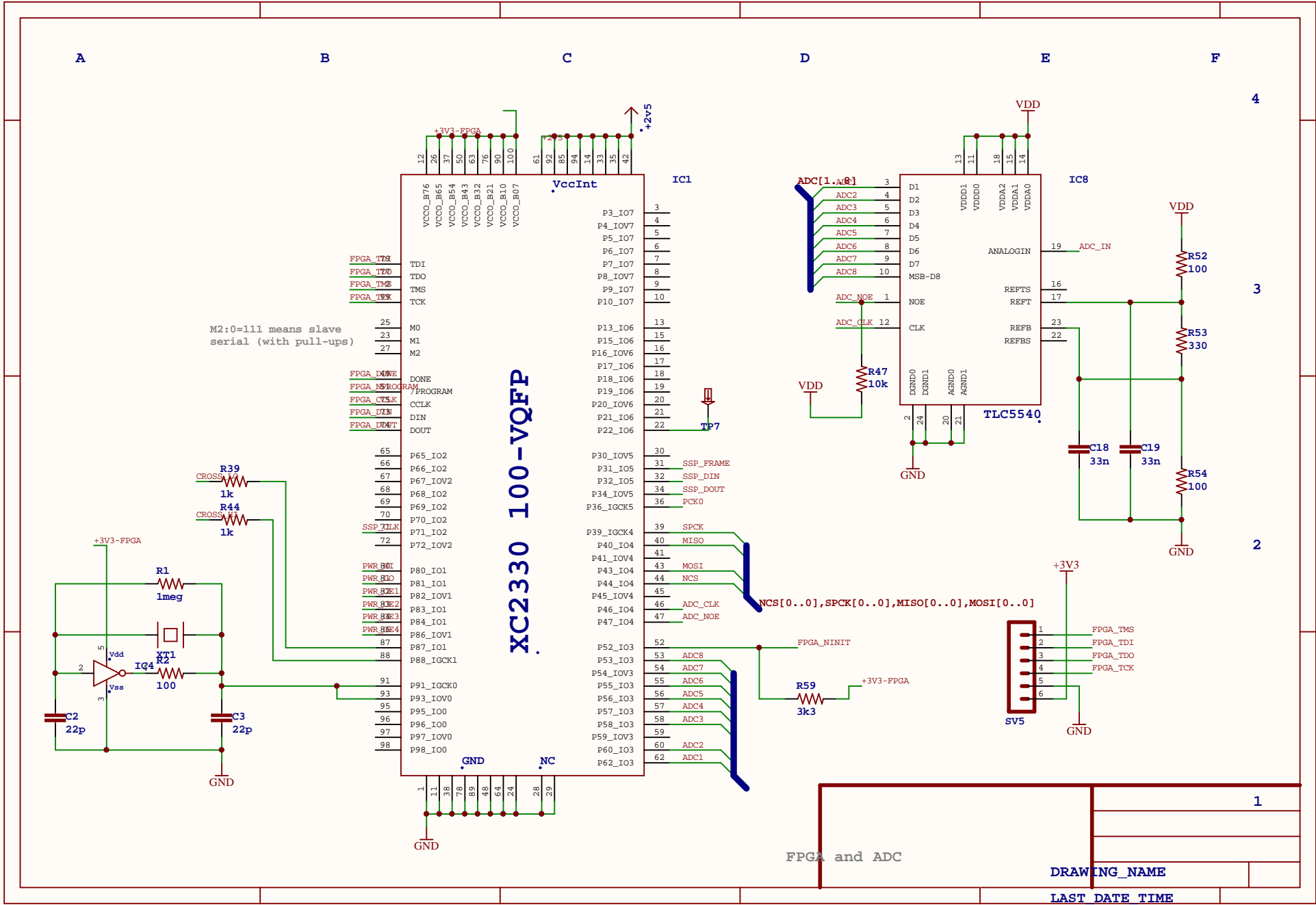
analog tx path (incl. coil drivers)		DRAWING_NAME	
		LAST_DATE_TIME	
		Sheet: SHEET	

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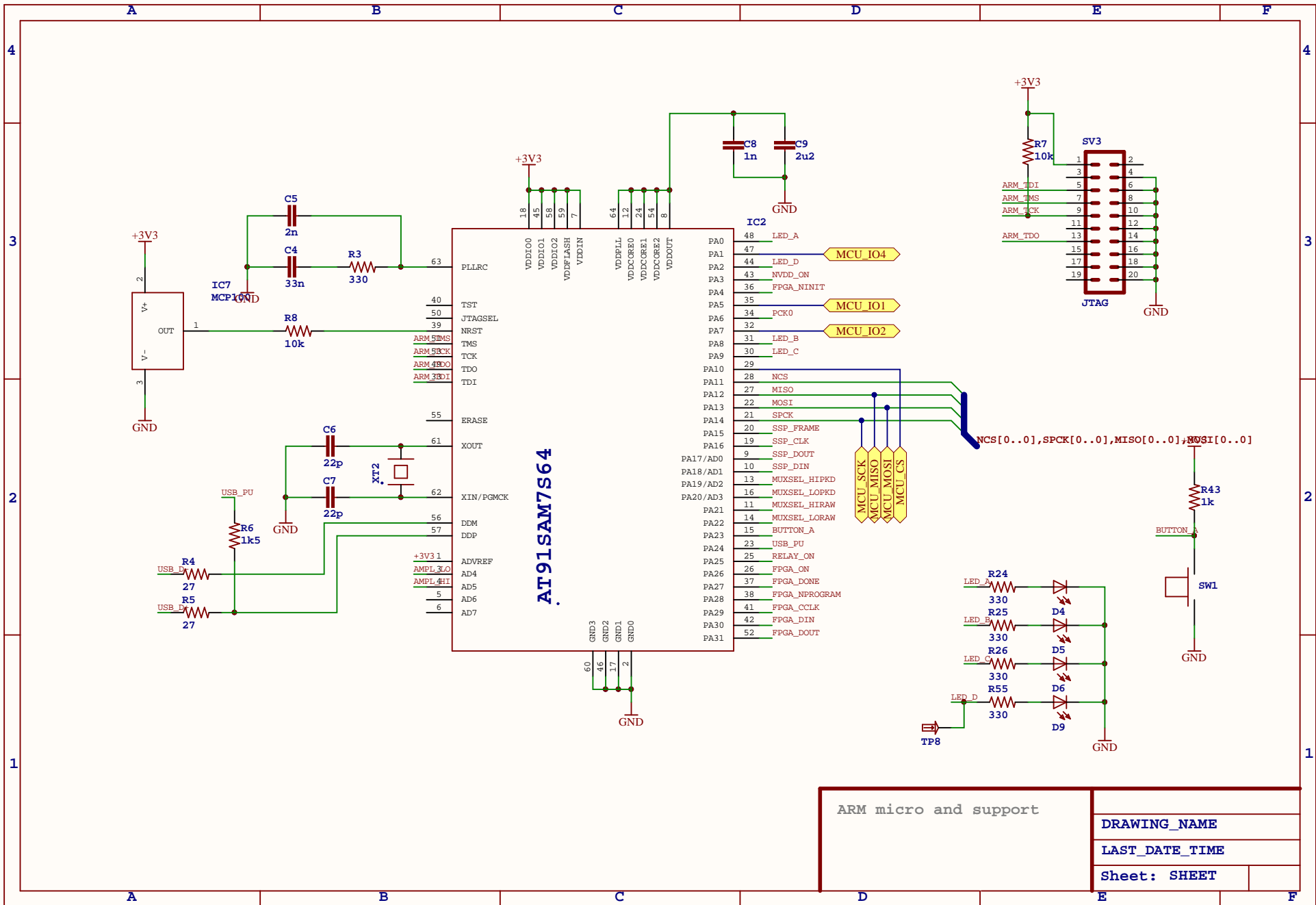


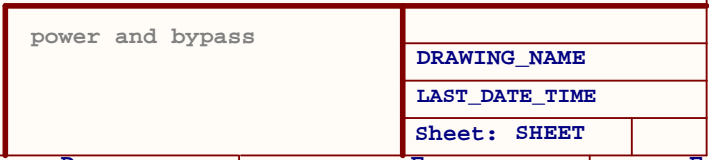
FPGA and ADC

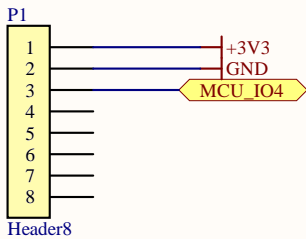
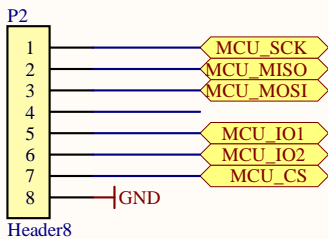
DRAWING_NAME

LAST DATE TIME

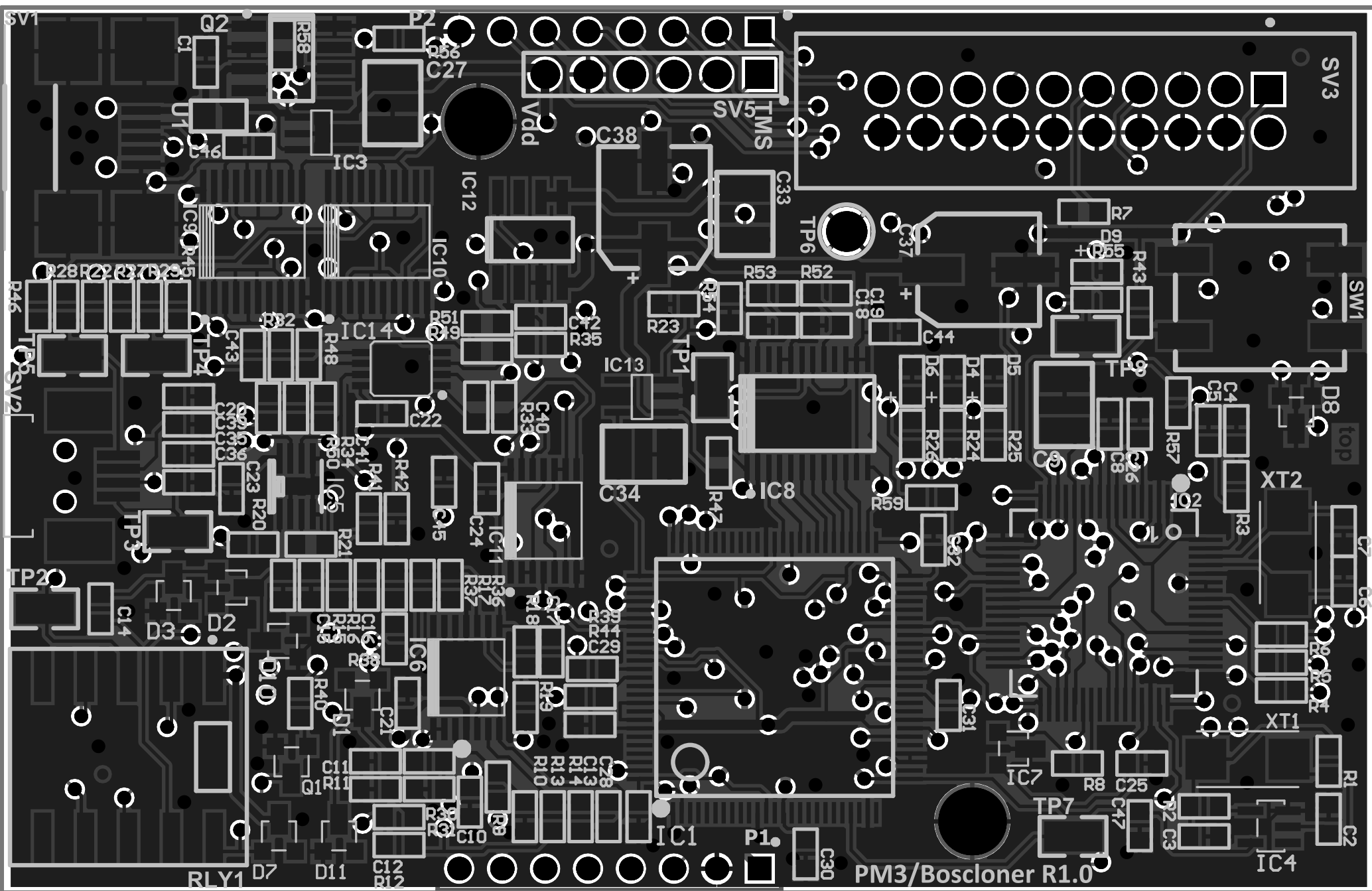
Sheet: SHEET







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