

KF70 Steve Haynal

**SofterHardware**

Sheet: /Ethernet/

File: Ethernet.sch

**Title: Ethernet**

Size: USLetter Date: 2019-02-23

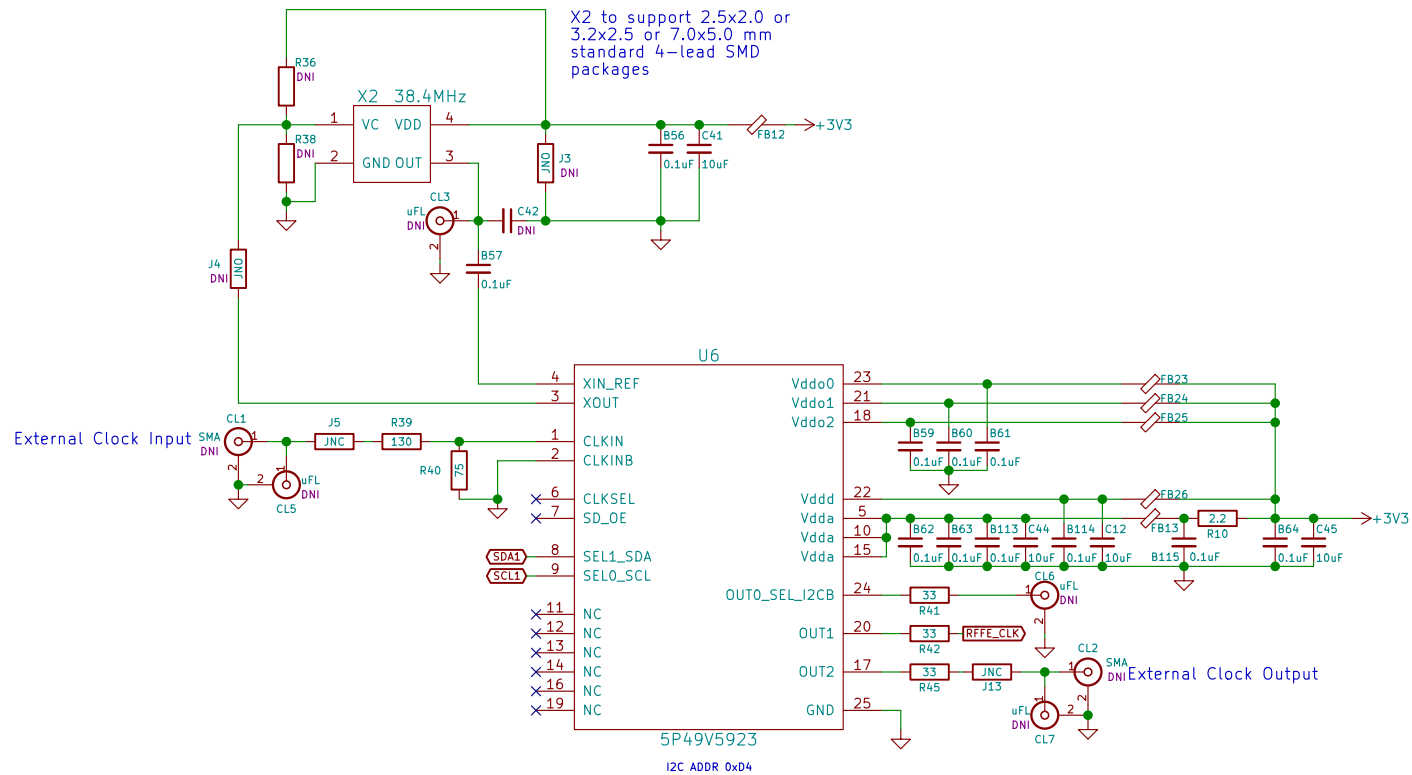
KiCad E.D.A. kicad 5.0.2-bee76a070ubuntu16.04.1

**Rev: 2.0-build8**

Id: 3/7

### Build Options:

Default Versa with oscillator: Include FB12,C41,B56,B57,X2. Include R36,R38 if required by oscillator. Exclude J3,J4,C42.  
Versa with crystal: Include X2 as crystal, B57,J4,J3 as jumper, C42,R38 as 15pF. Exclude FB12,C41,B56,R36.  
External clock: Configure U6 for CLKIN input and correct ratio, drive CL1 or CL5 with external clock.  
Other experimental options possible with uFL connectors. See RF Frontend sheet for additional AD9866 clock options.



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Sheet: /Clock/

File: Clock.sch

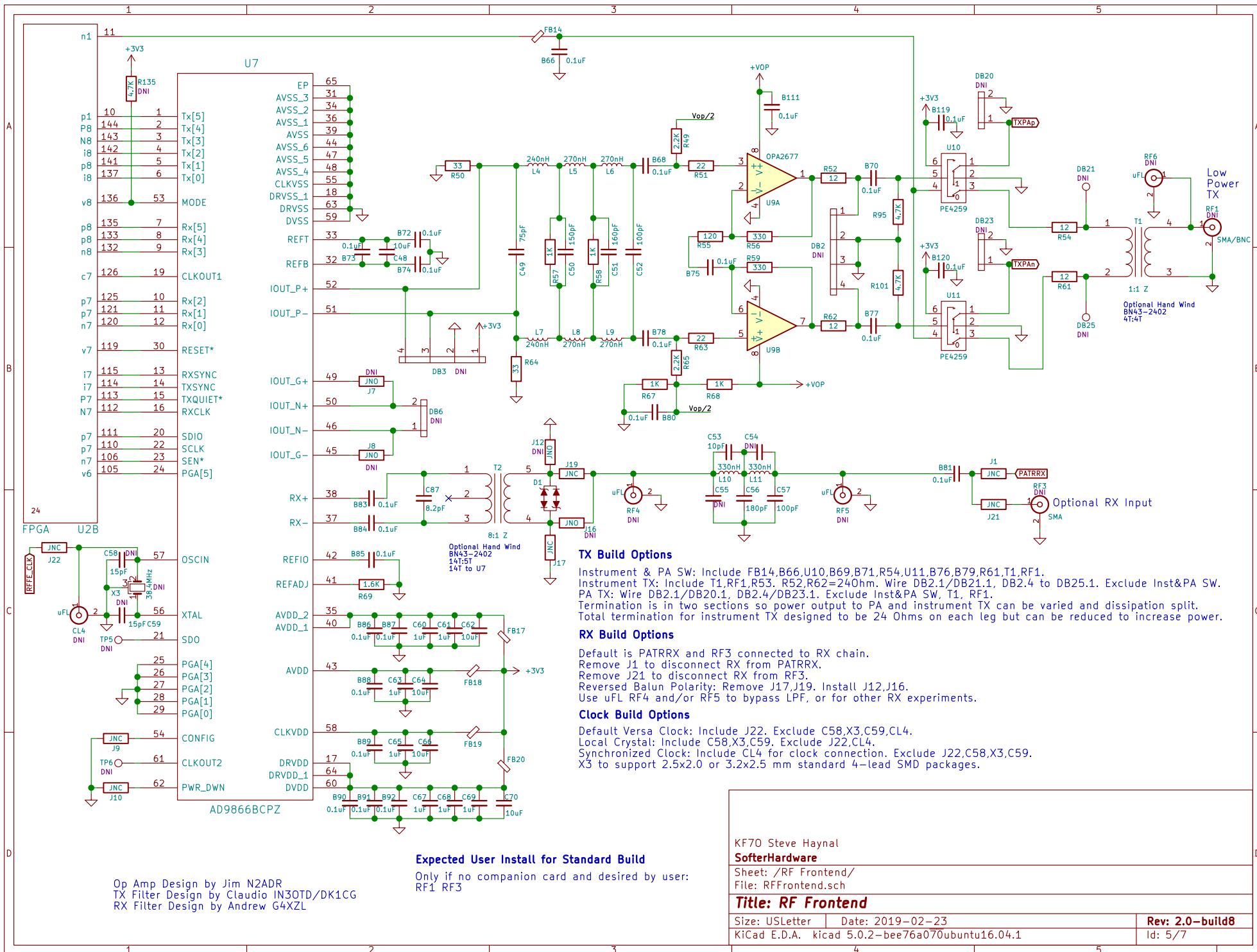
**Title: Clock**

Size: USLetter Date: 2019-02-23

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Rev: 2.0-build8

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## Build Options

Any or all components may be excluded if PA is unused.

SOT-89 or TO-220 LDMOS supported on main circuit board  
TO-220 mounts to side of enclosure  
SOT-89 dissipates heat to PCB and side of enclosure

Default build uses 2 AFT05MS003 mounted on main board, 110mA bias

PLD-1.5 and alternate SOT-89 supported by adapter board  
Adapter board dissipates heat to side of enclosure

## RD15HVF1 Build Option

L33,L34 = 4.7 Ohm  
R92,R99 = 500 Ohm  
T3 = BN61-202 4T Pri, 2+2T Sec  
200 mA bias

Add attenuation with R97,R98,R94,R100  
if PA is overdriven

