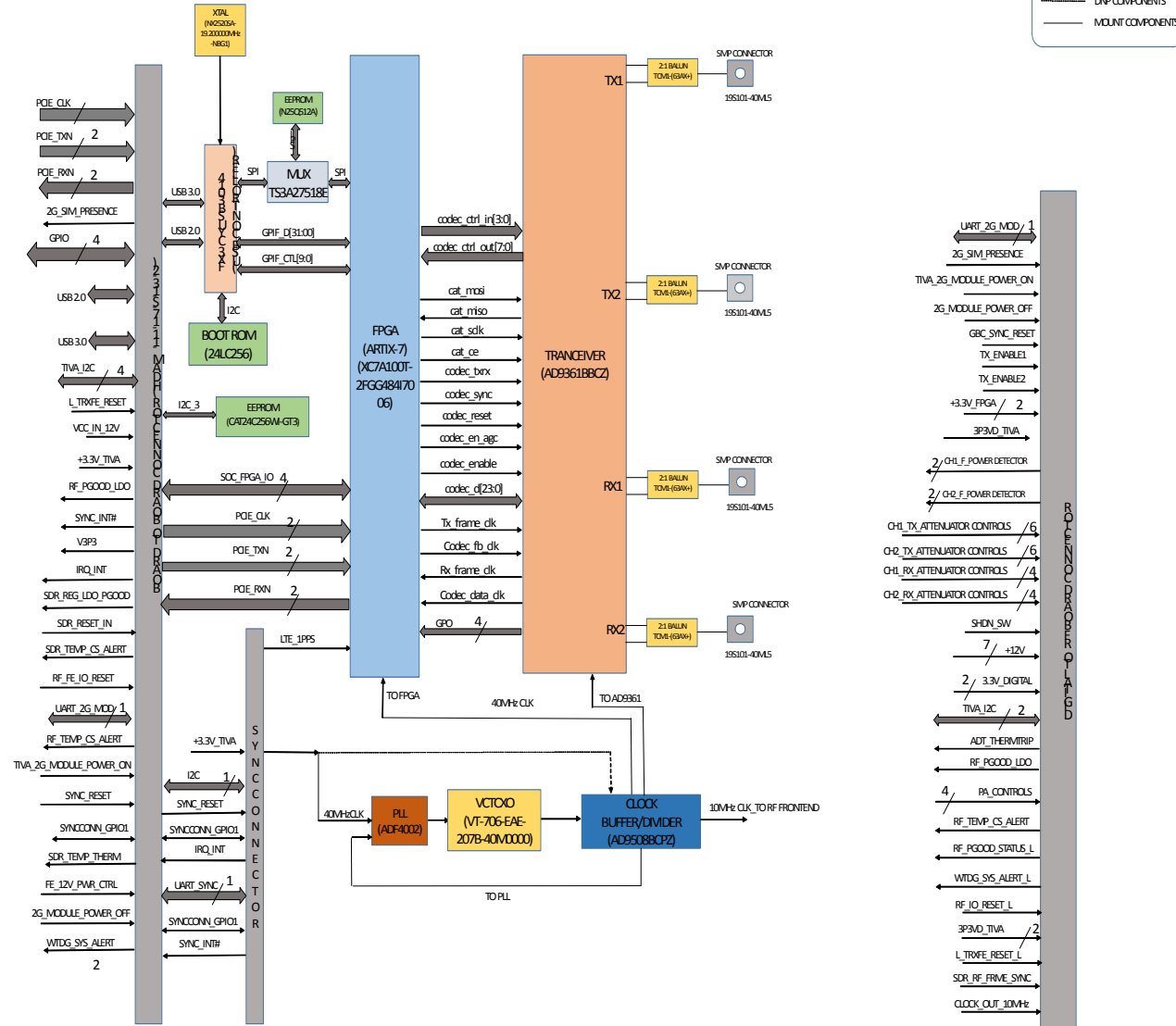


TABLE OF CONTENTS

SHEET NUMBER	SHEET NAME
1	INDEX PAGE
2	BLOCK DIAGRAM
3	CLOCK_BLOCK_DIAGRAM
4	VCTCXO
5	CLOCK_DIV_TEST
6	FPGA_1
7	FPGA_2
8	FPGA_3
9	FPGA_DEBUG
10	FPGA_4
11	FPGA_POWER
12	FPGA_POWER
13	FPGA_POWER
14	FX3_POWER
15	FX3_1
16	RESET
17	USB3 SWITCH
18	USB2 SWITCH
19	CONNECTOR
20	AD_LVDS_CTRL_CLK
21	ADI CONTROL
22	ADI_TX_PATH
23	ADI_RX_PATH
24	AD_POWER
25	REG_12V_3P7V_LDO_3P3V
26	ADI_POWER_SUPPLY
27	FPGA_EEPROM_MUX
28	STRAP_LINES
29	POWER SWITCH
30	RF BRD CONNECTOR
31	SYNC BOARD CONNECTOR
32	FPGA_LVL_TRANSL_1
33	I2C_BUFFERS
34	LEVEL_TRANS

REVISION HISTORY			
DATE	VERSION	UPDATED BY	REMARKS
02 FEB 2016	1.00		REV A RELEASE
16 JUN 2016	2.00		REV B RELEASE
30 NOV 2016	3.00		REV C RELEASE

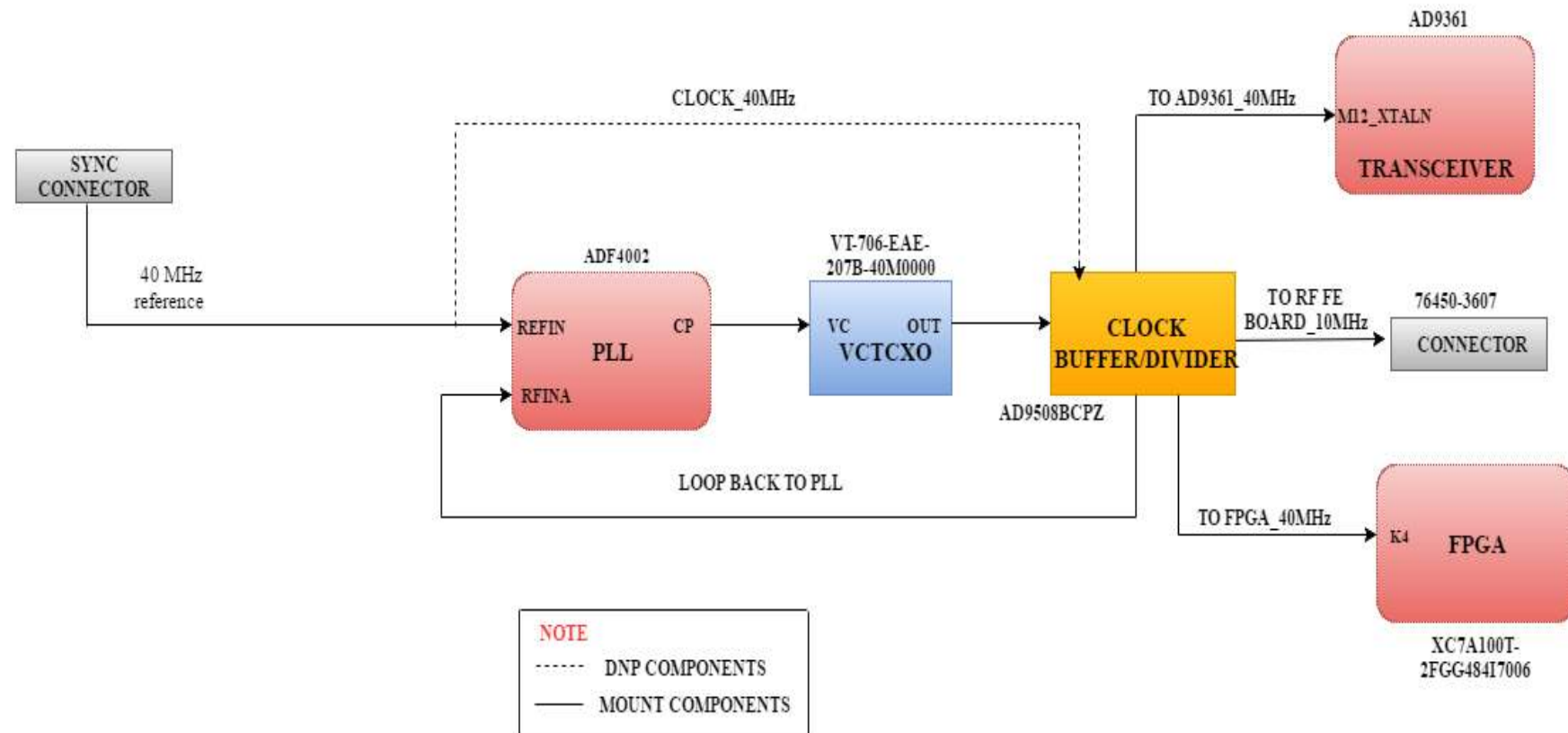
# SDR BLOCK DIAGRAM



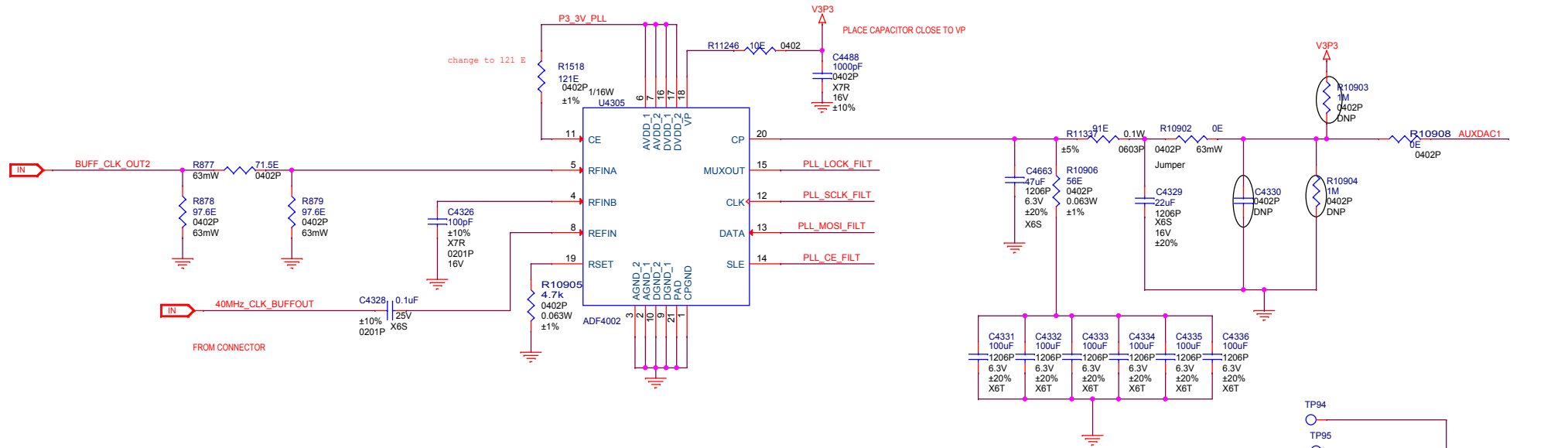
## OpenCellular connect-1

Title			RF Block Diagram
Size	A3	Document Number	00
Rev	1.1		
Date:	Friday, January 06, 2017	Sheet	2 of 34

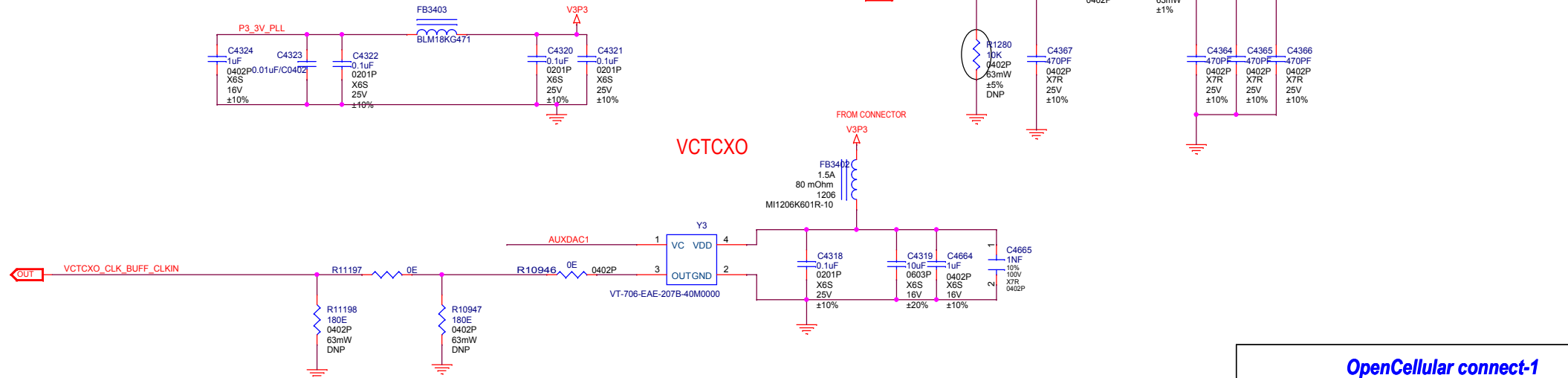
# CLOCK TREE OF SDR



## PLL CIRCUIT



## VCTCXO

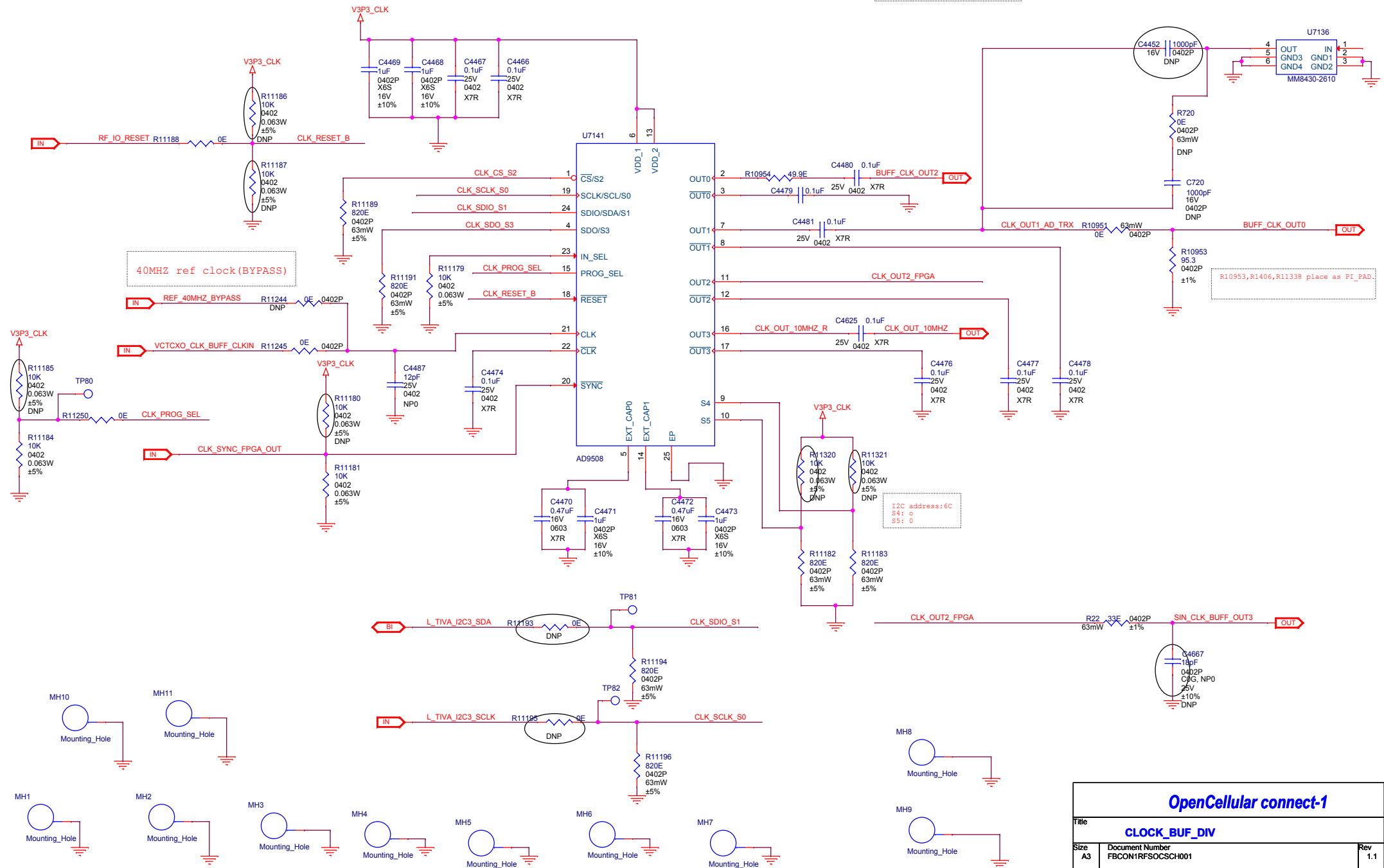


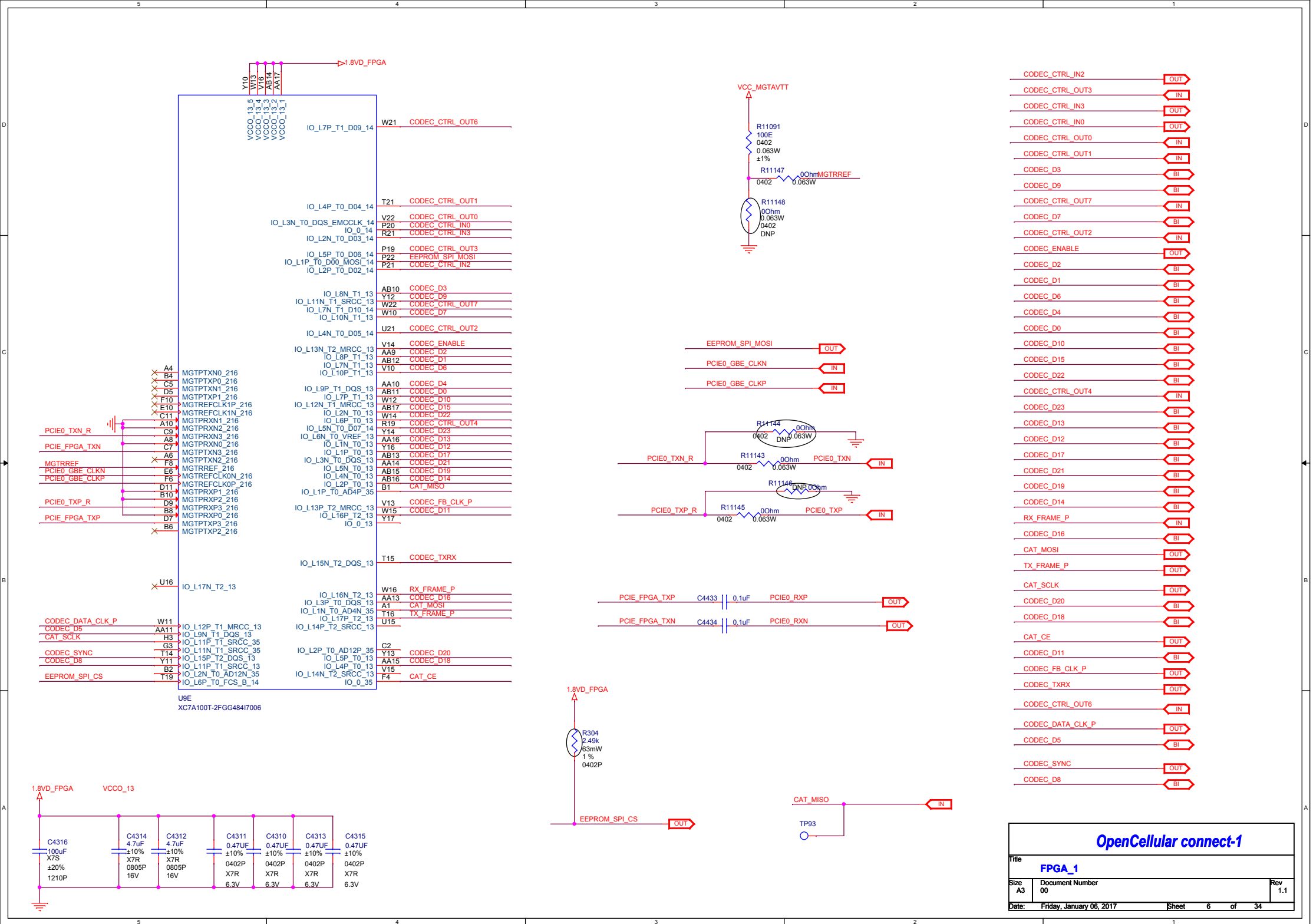
OpenCellular connect-1

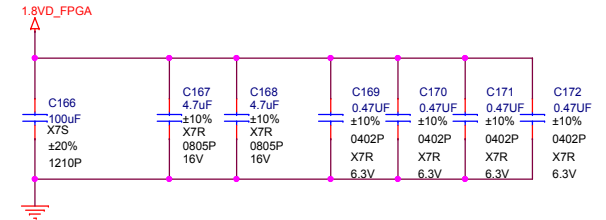
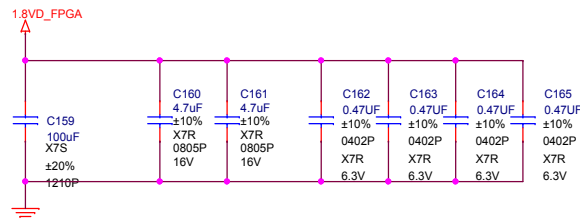
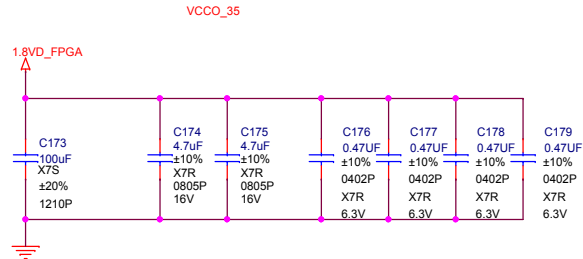
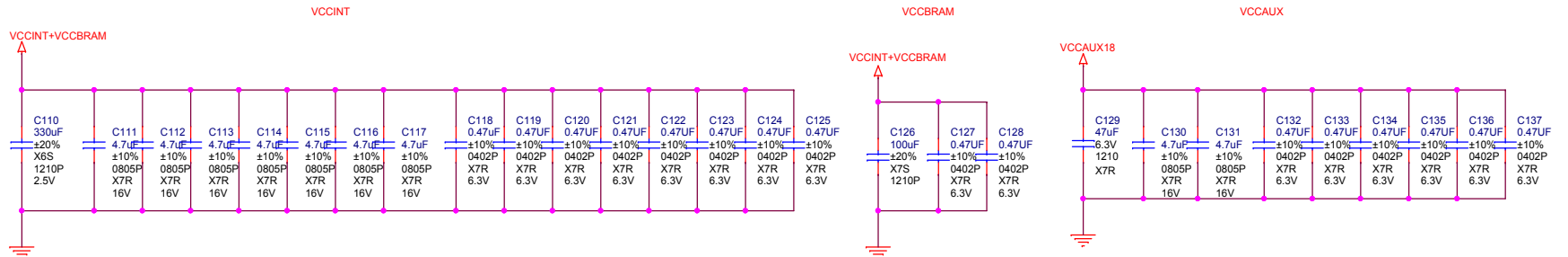
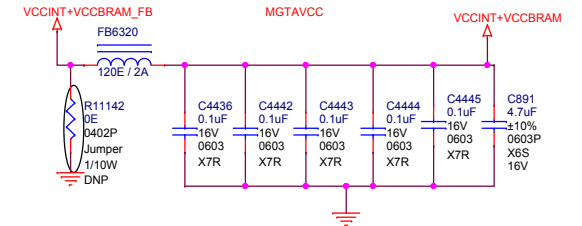
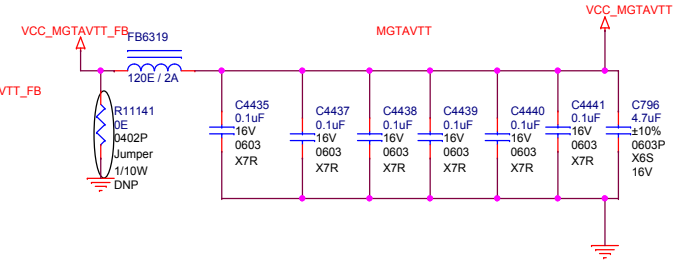
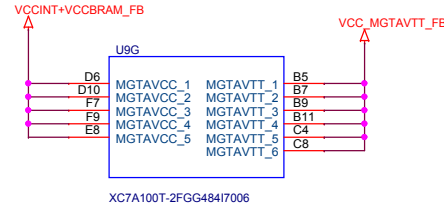
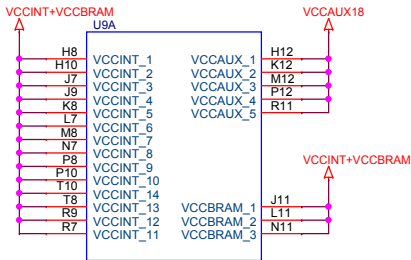
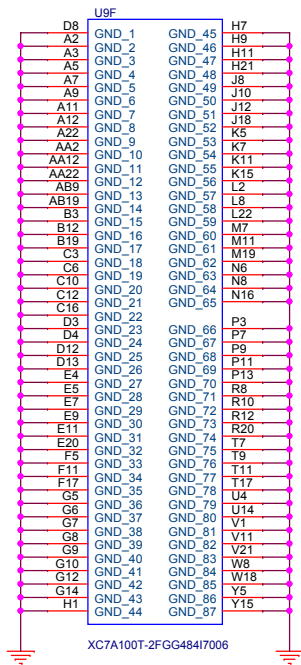
Title			VCTCXO
Size	A3	Document Number	00
Date:	Friday, January 06, 2017	Sheet	4 of 34
Rev	1.1		

Layout: place 3pad crow feet  
option for following components

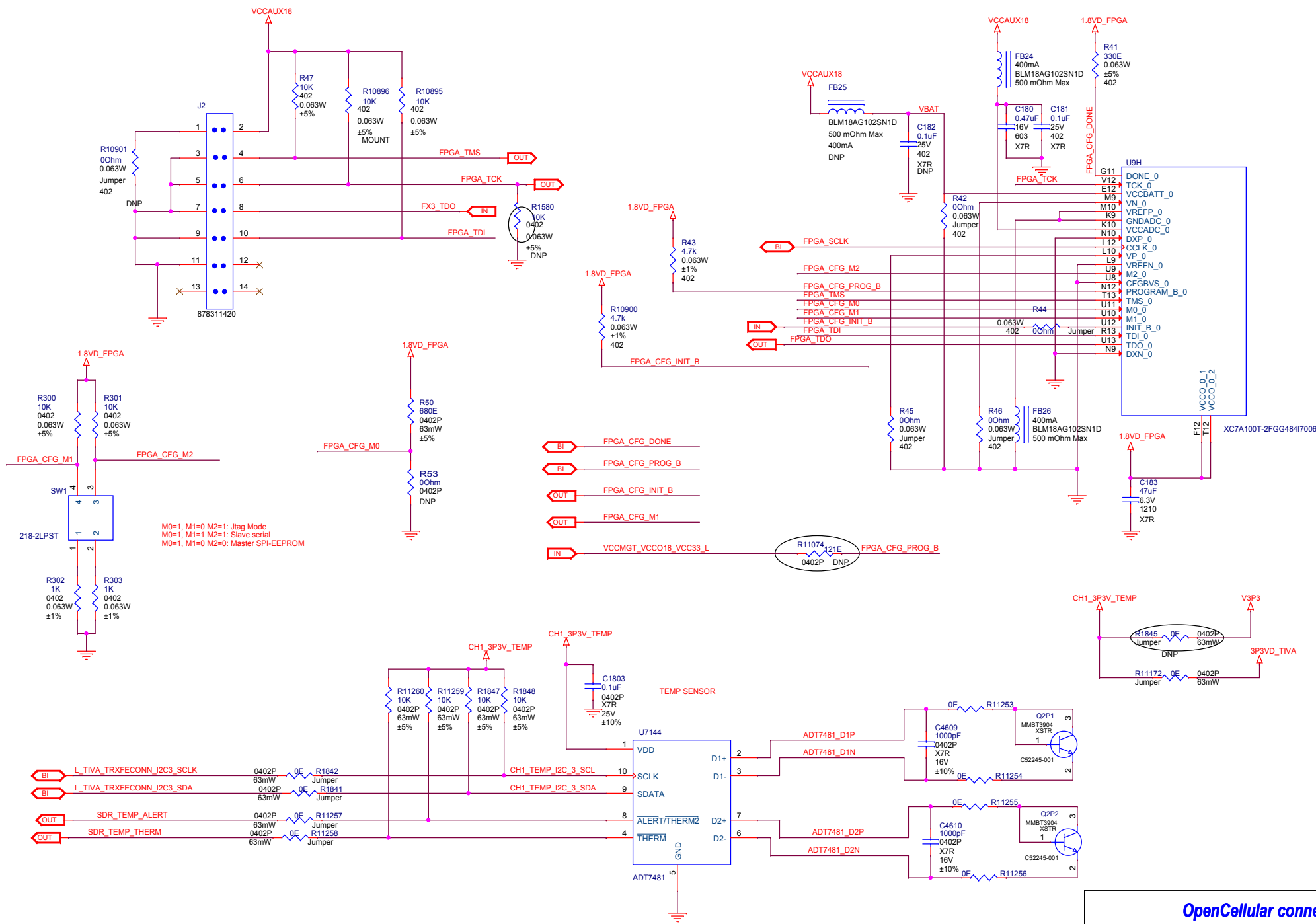
- 1)C4481,C4452.
- 2)R720,C4452.
- 3)C720,R10951.



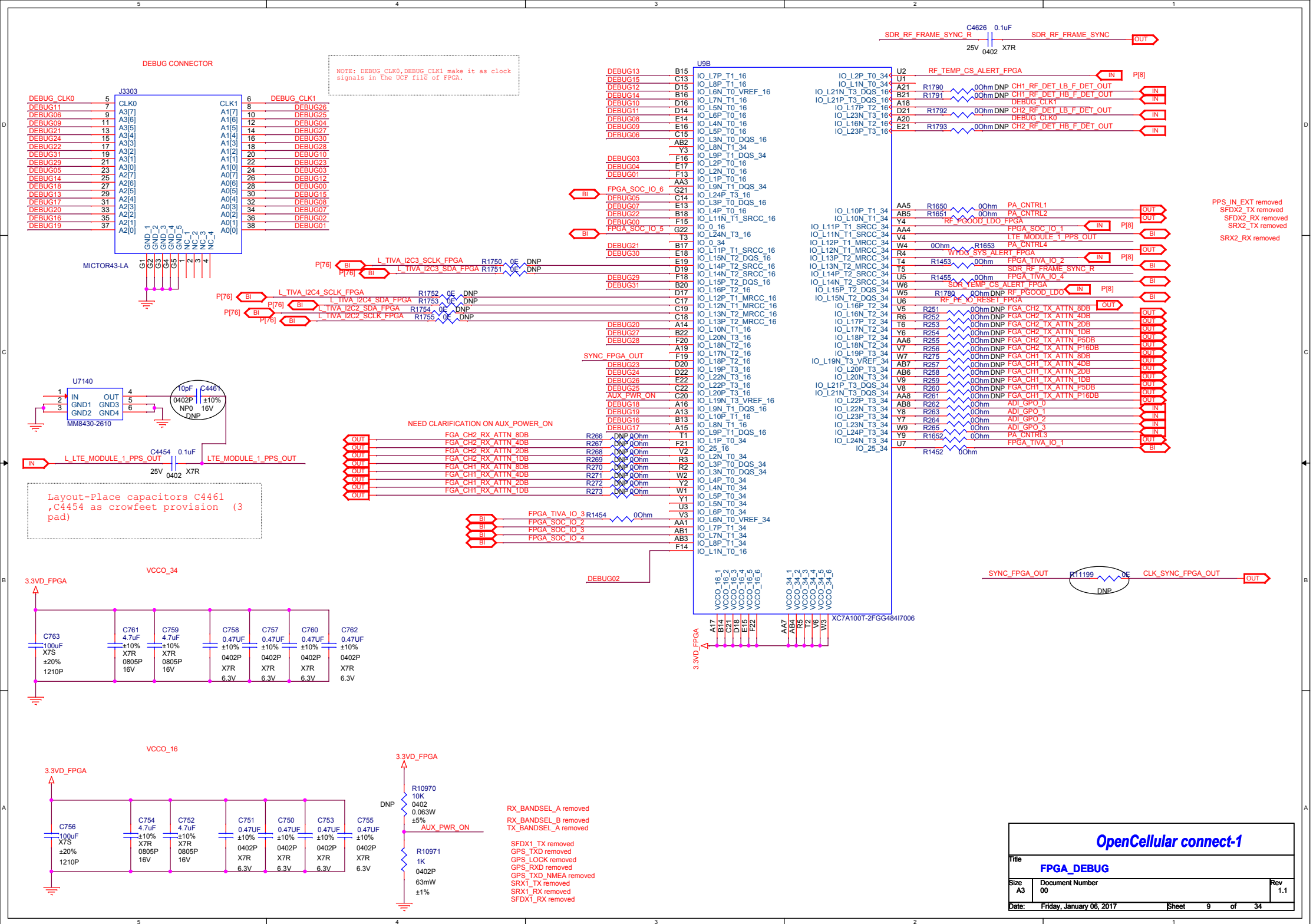


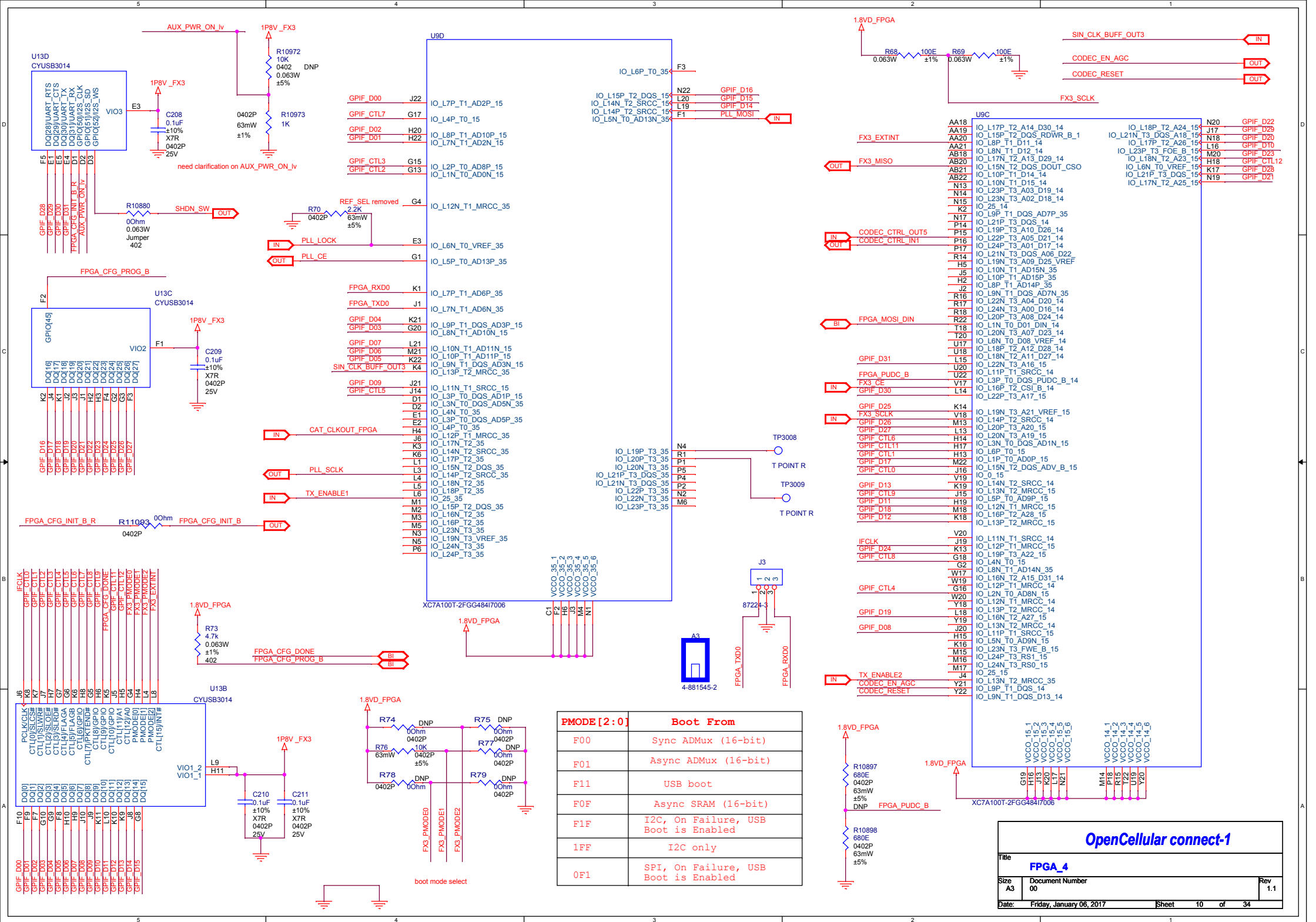


OpenCellular connect-1			
Title	FPGA_2		
Size	Document Number	Rev	
A3	00	1.1	
Date:	Friday, January 06, 2017	Sheet	7 of 34

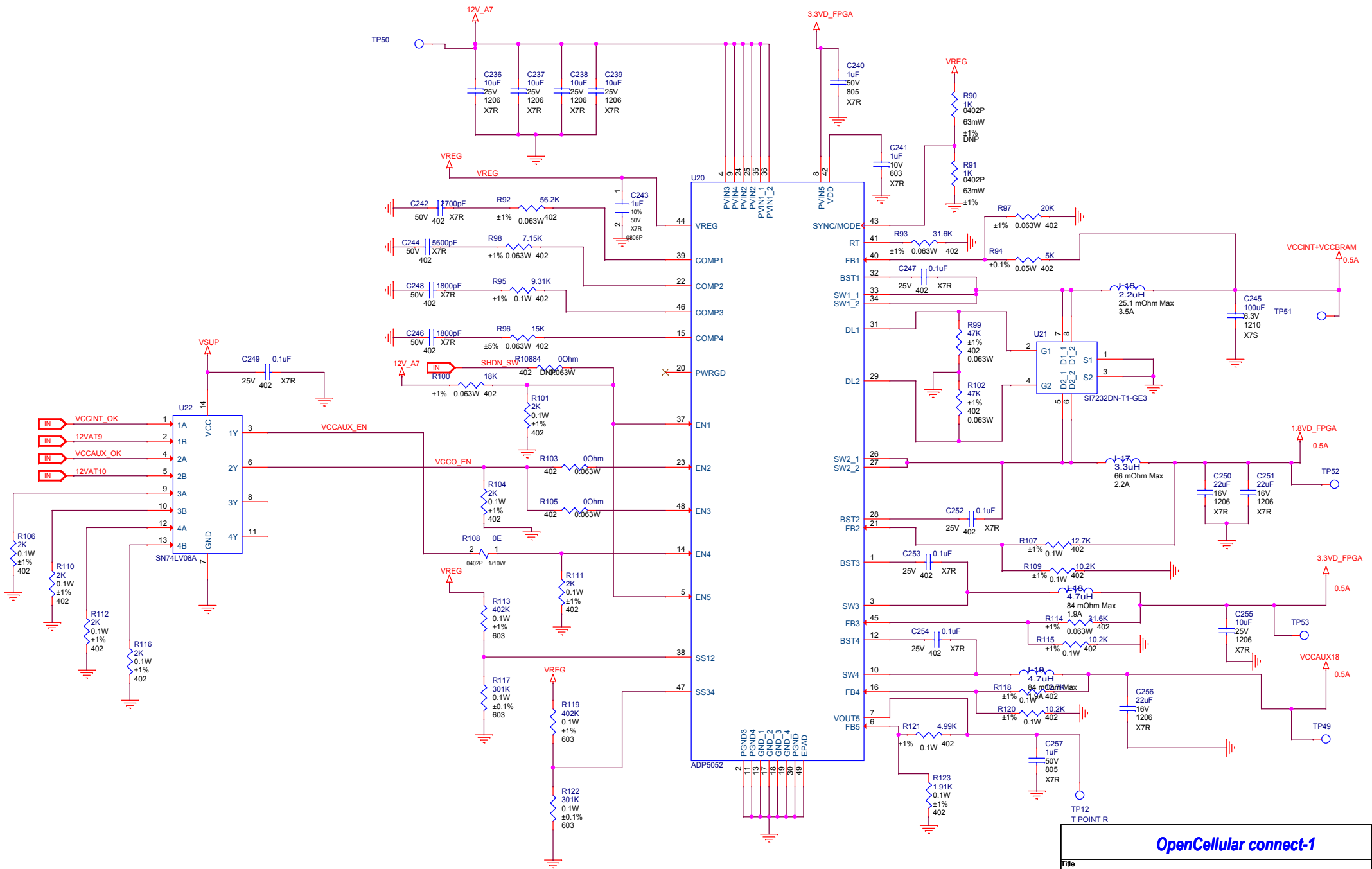




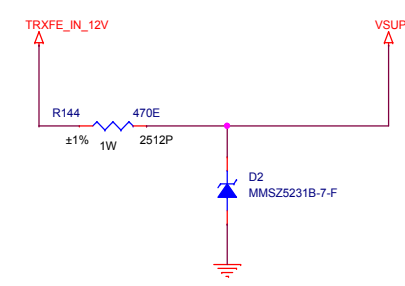
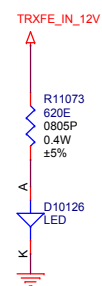
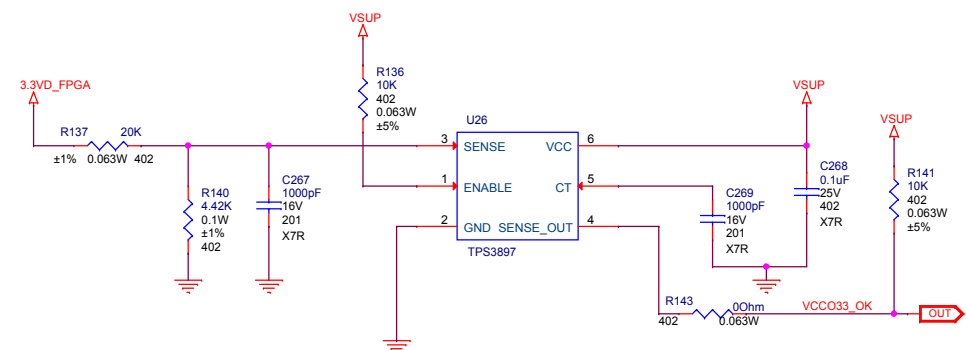
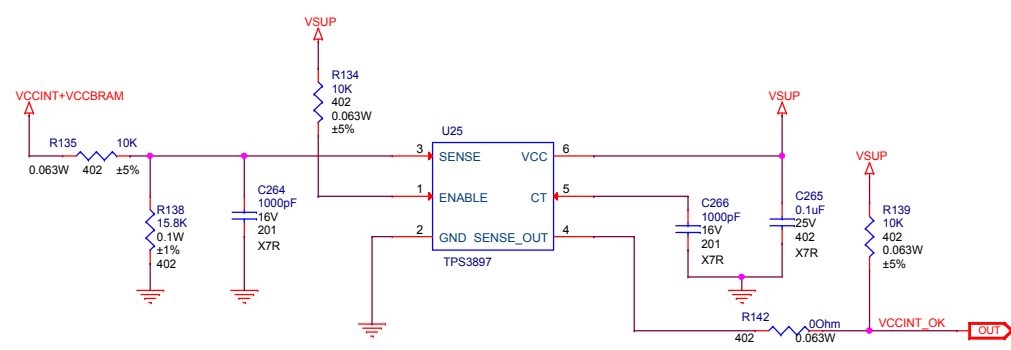
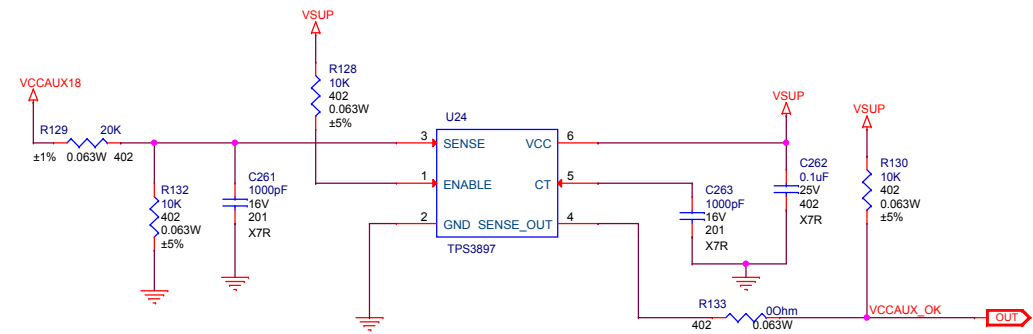
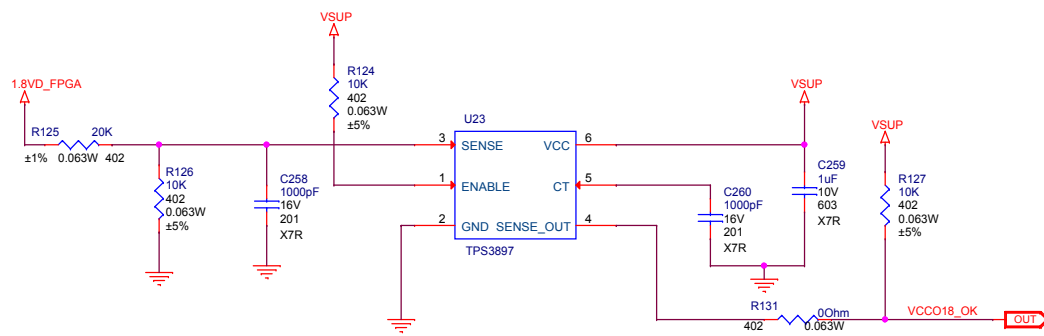


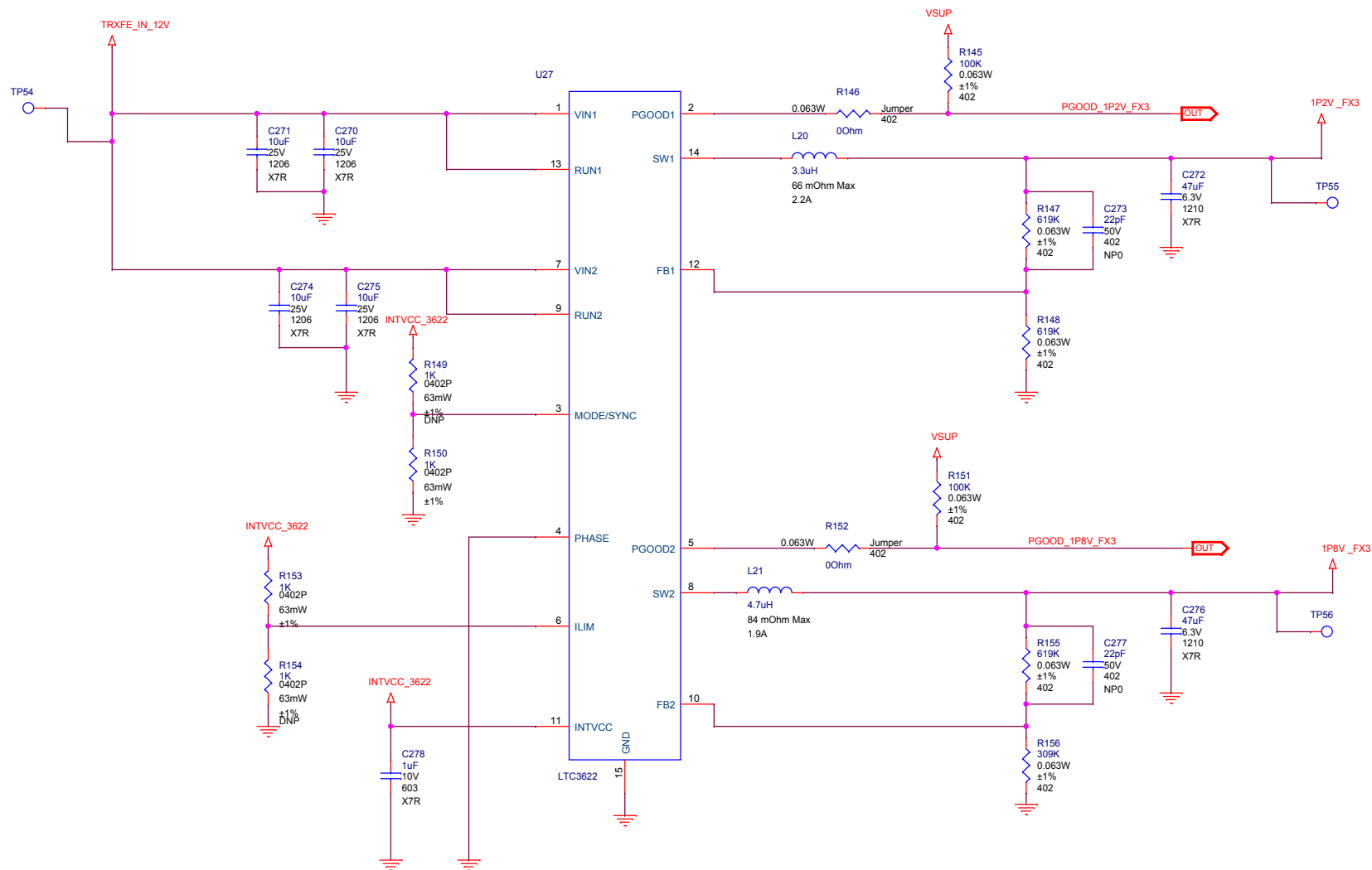






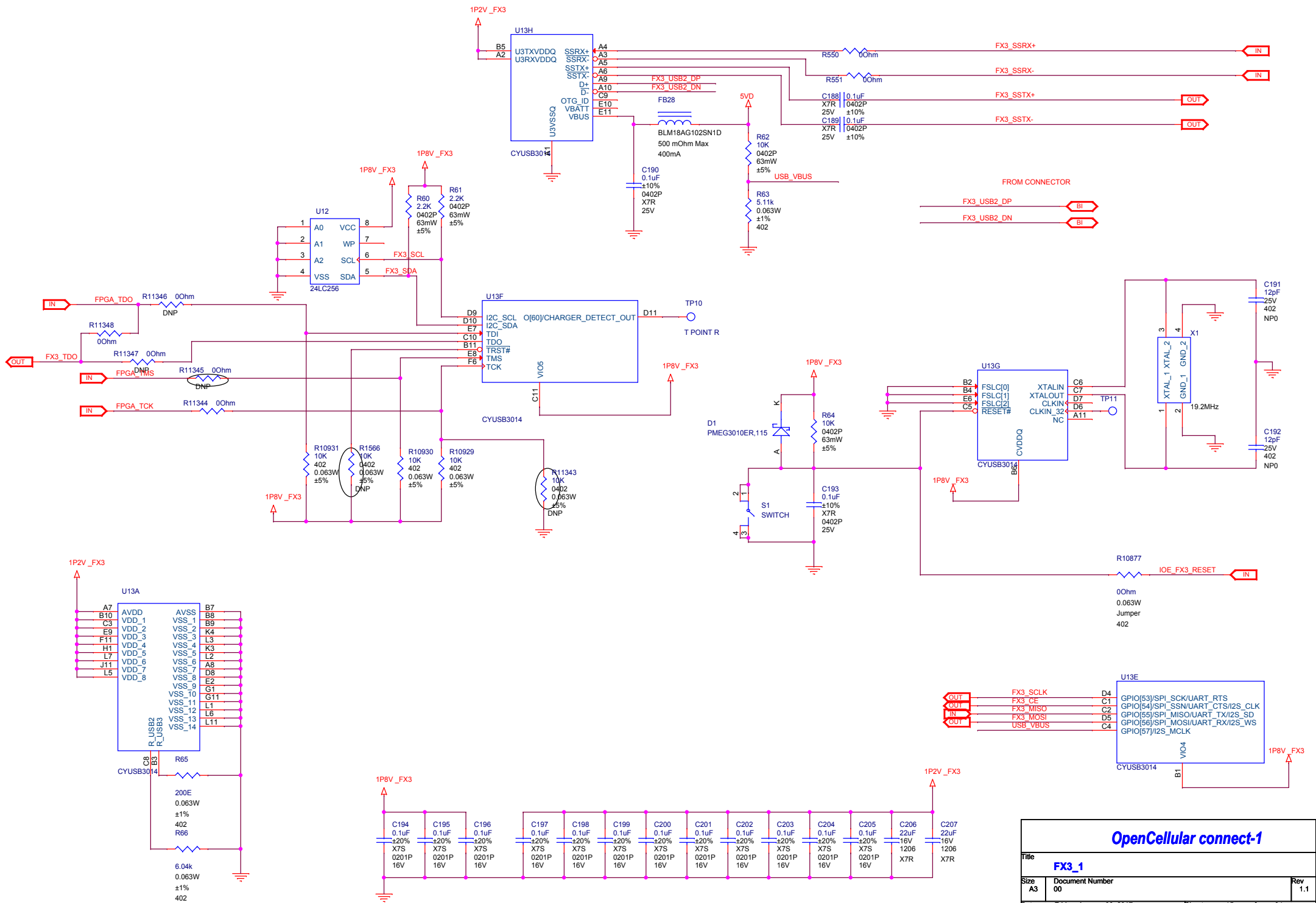
OpenCellular connect-1		
Title	FPGA_POWER	
Size	Document Number	Rev
A3	00	1.1
Date:	Friday, January 06, 2017	Sheet 12 of 34





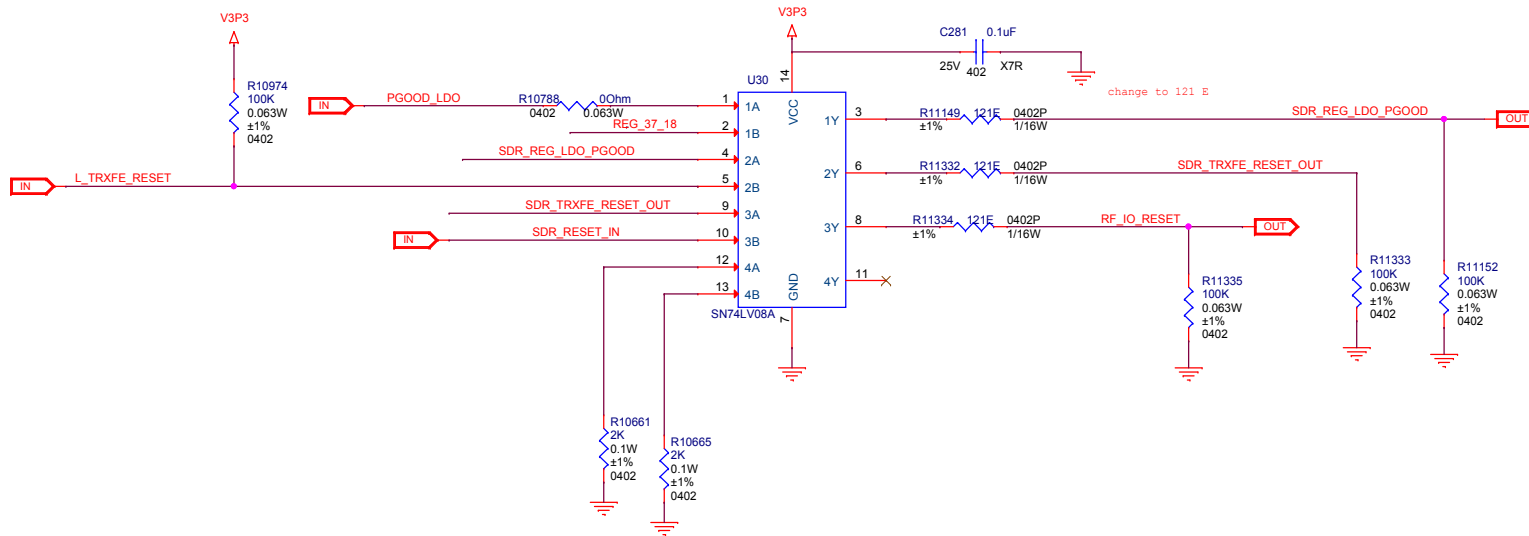
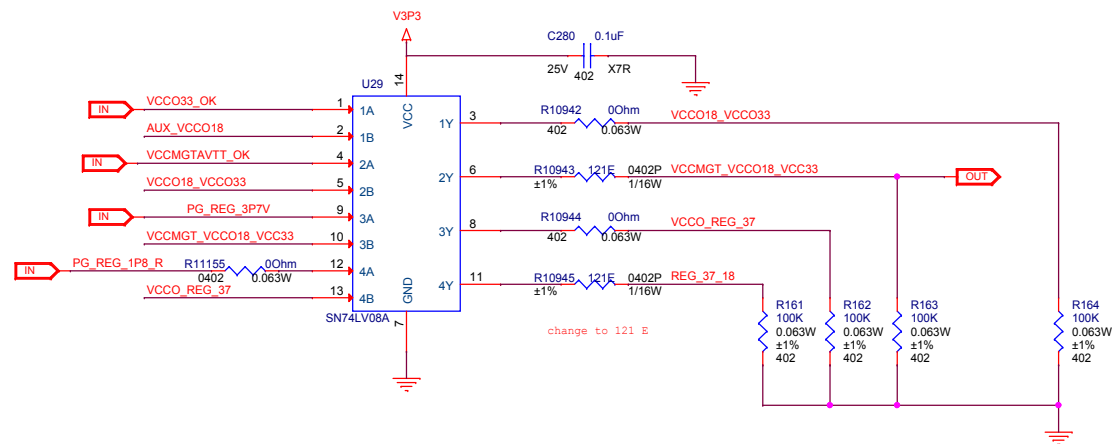
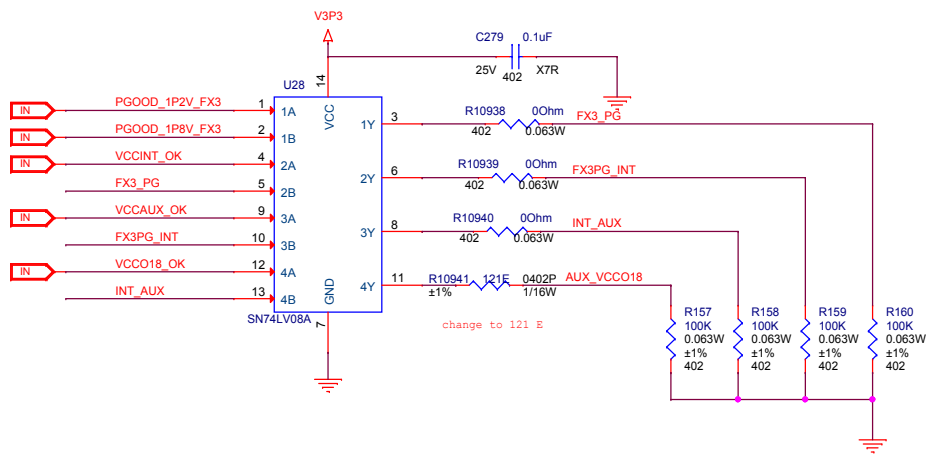
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Title		
FX3_POWER		
Size	Document Number	Rev
A3	00	1.1
Date:	Friday, January 06, 2017	Sheet 14 of 34



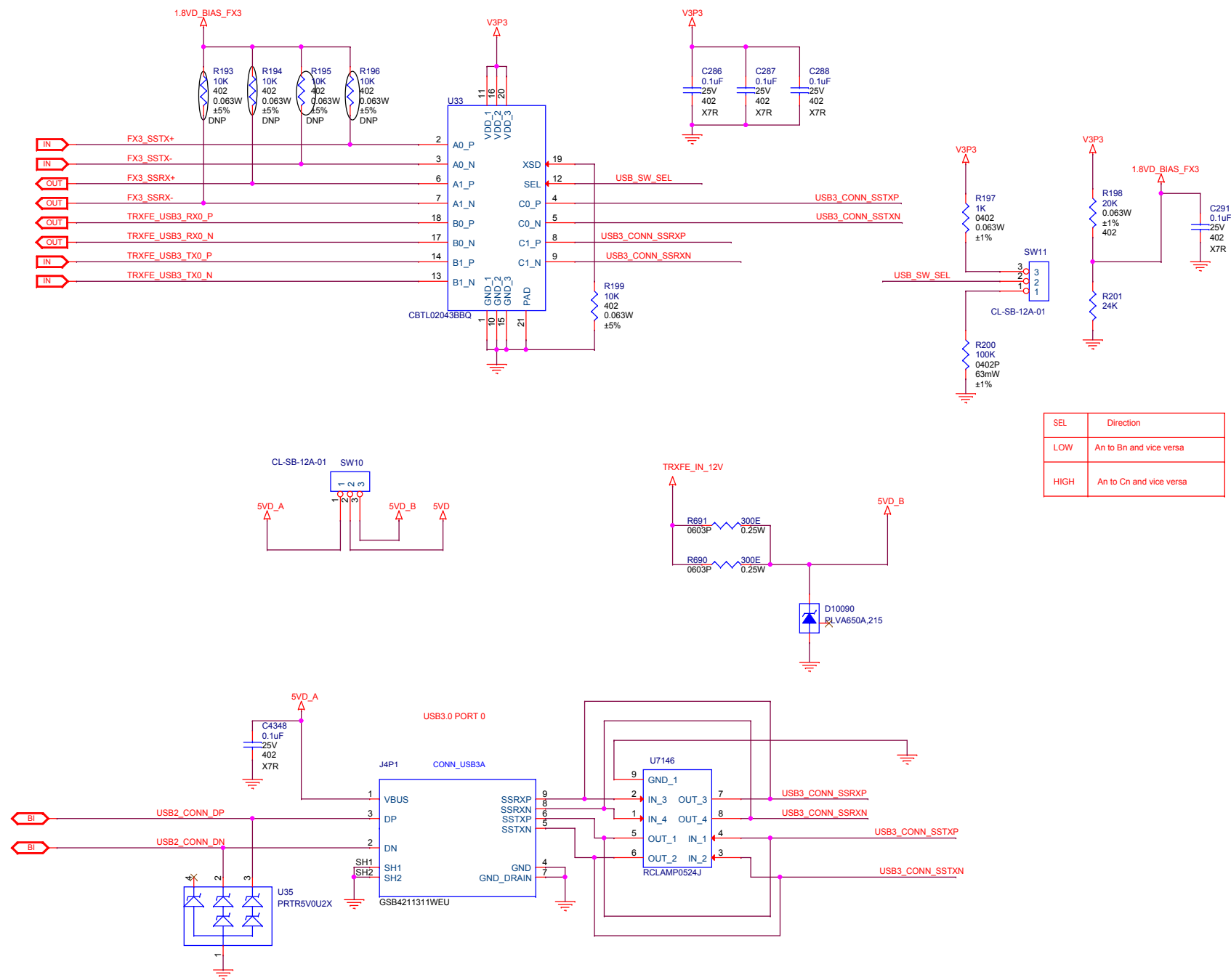
## OpenCellular connect-1

Title			FX3_1
Size	A3	Document Number	00
Date:	Friday, January 06, 2017		Sheet 15 of 34
Rev	1.1		

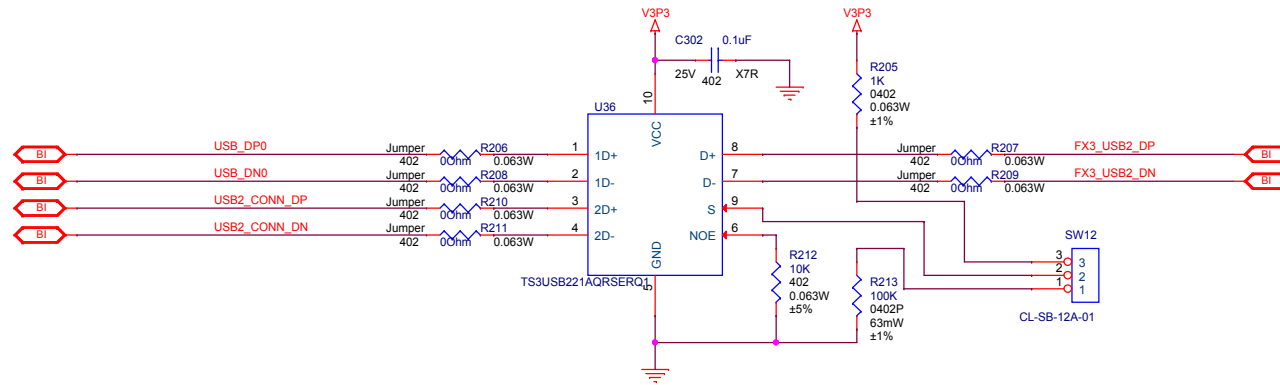


OpenCellular connect-1			
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Size	Document Number	Rev	
A3	00	1.1	
Date:	Friday, January 06, 2017	Sheet	16 of 34





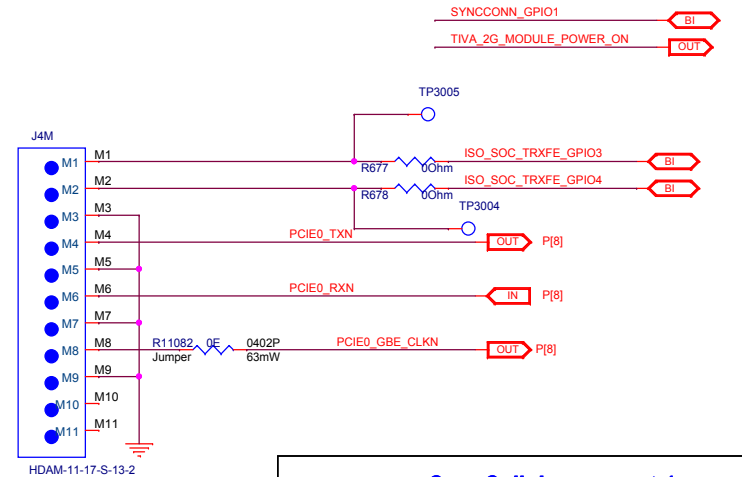
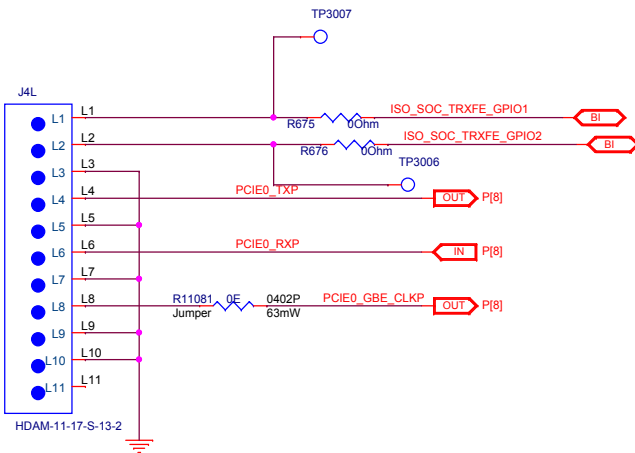
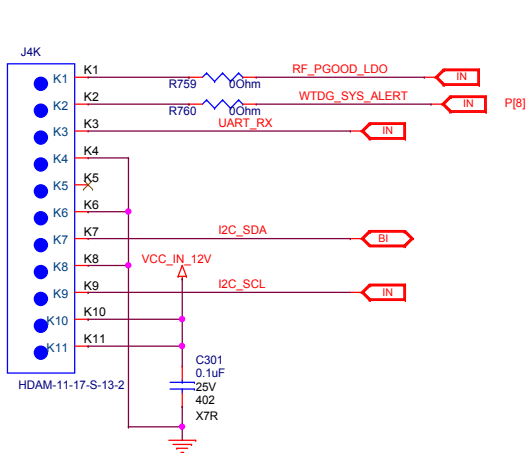
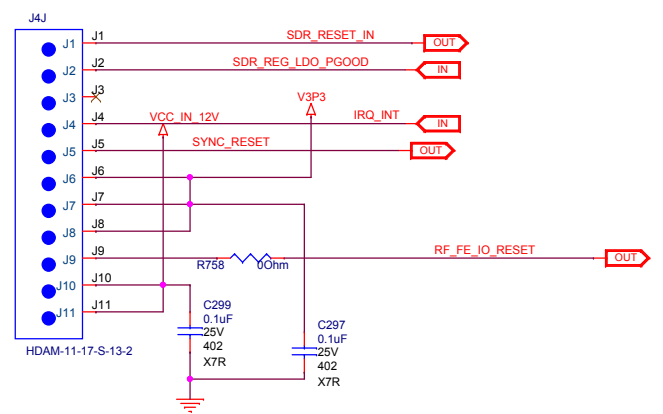
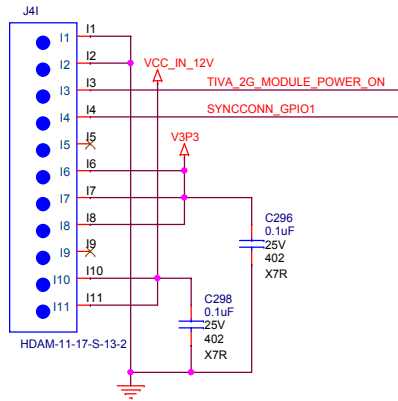
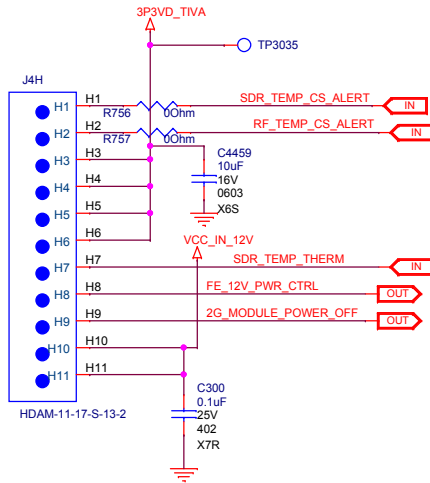
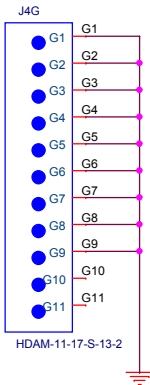
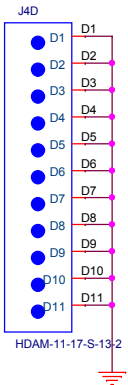
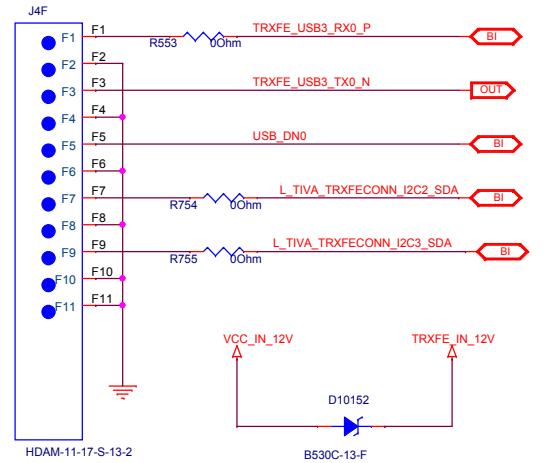
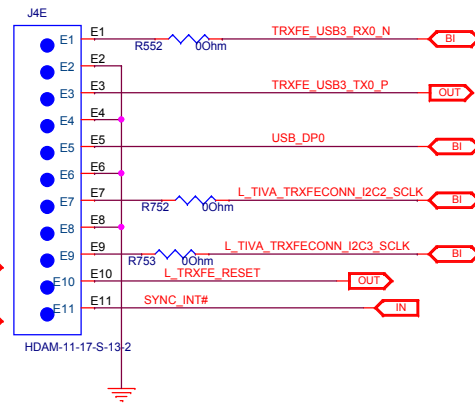
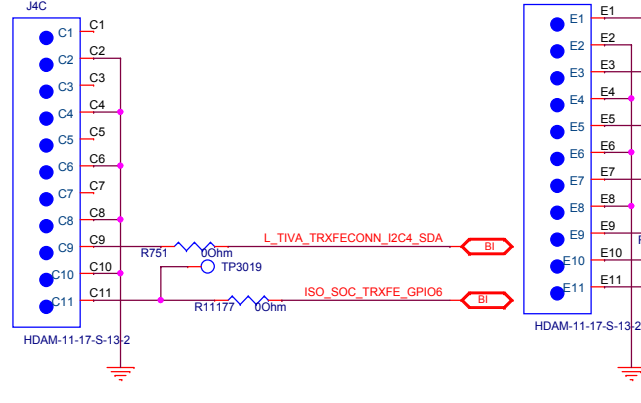
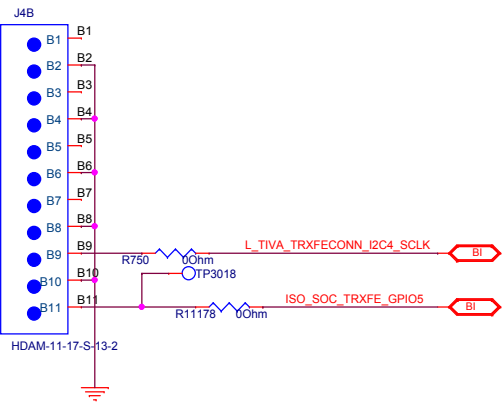
## USB2.0 SWITCH



SEL	Direction
LOW	D to 1D and vice versa
HIGH	D to 2D and vice versa

## OpenCellular connect-1

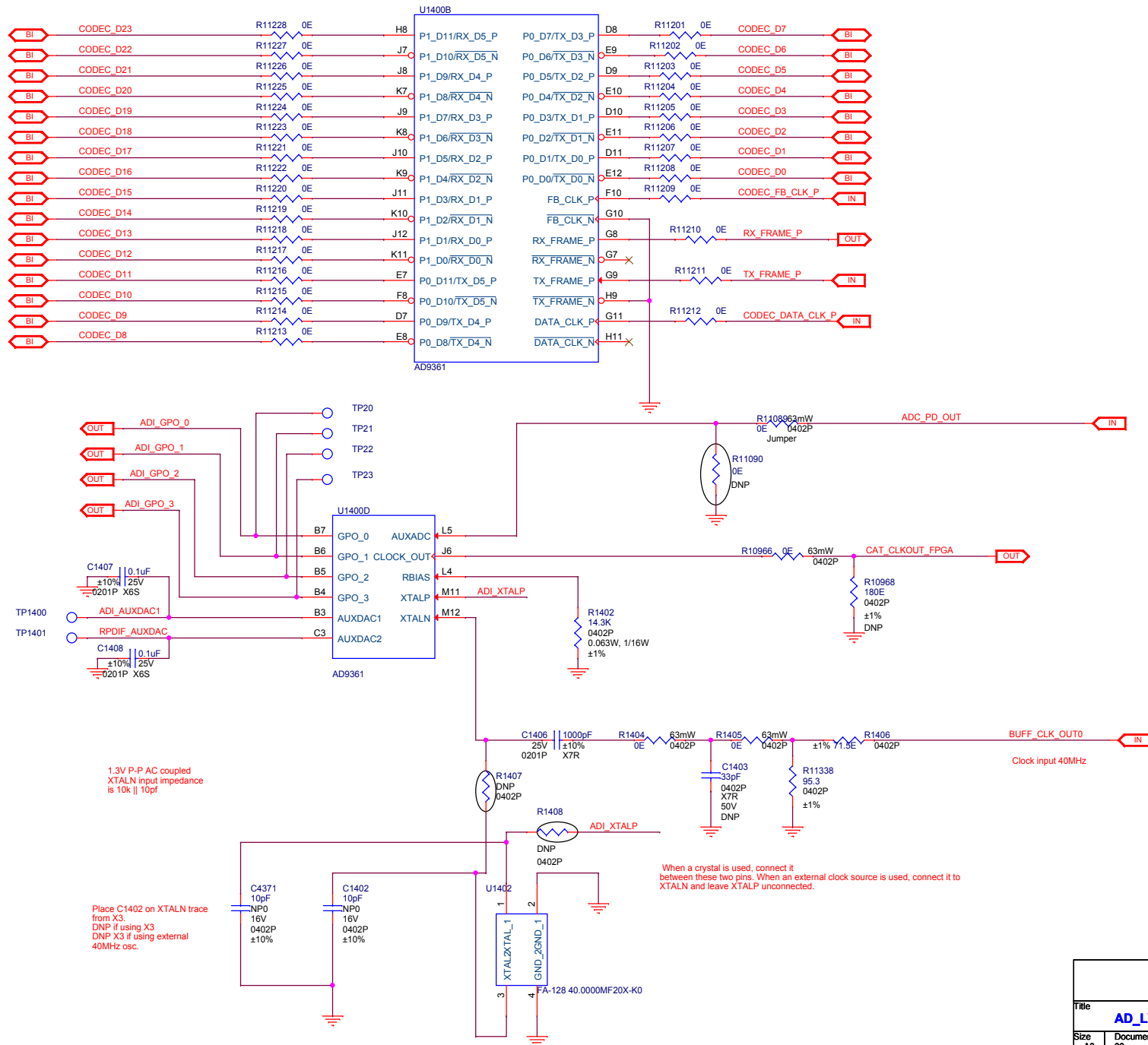
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Size	Document Number	Rev	
A3	00	1.1	
Date:	Friday, January 06, 2017	Sheet	18 of 34



## OpenCellular connect-1

Title	<b>CONNECTOR</b>		
Size	A3	Document Number	00
Date:	Friday, January 06, 2017	Sheet	19 of 34
Rev	1.1		

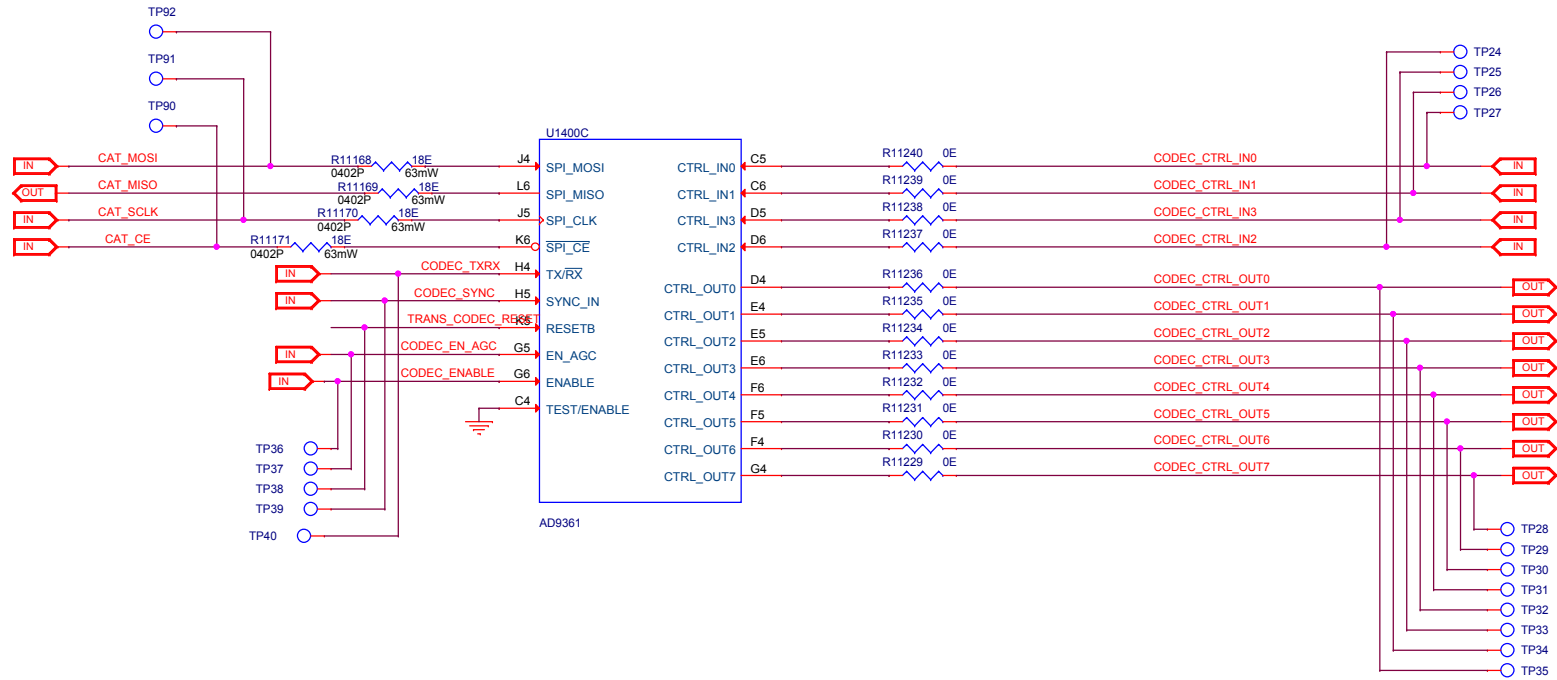
# AD\_LVDS\_CNTRL\_CLK



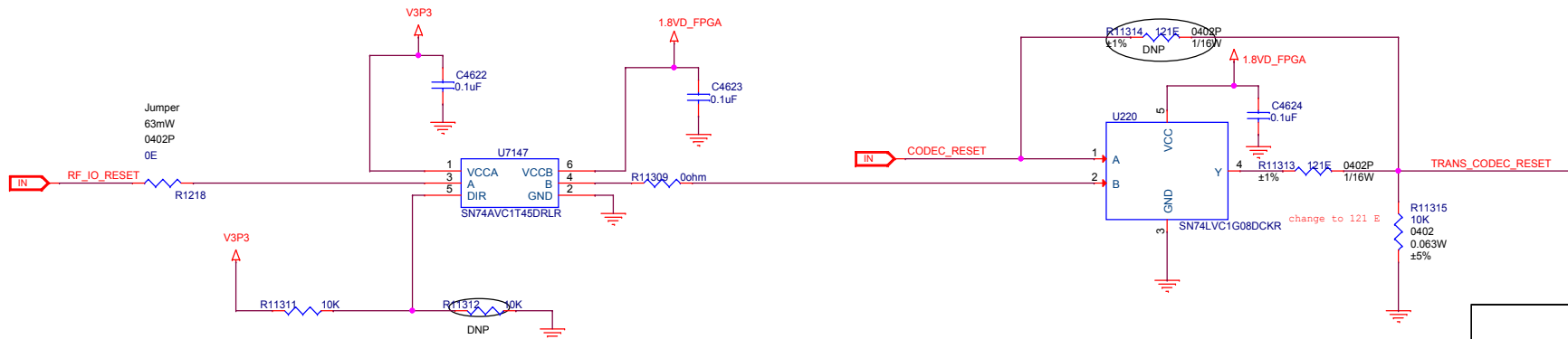
## OpenCellular connect-1

Title			AD_LVDS_CTRL
Size	Document Number	Rev	
A3	00	1.1	
Date: Friday, January 06, 2017			Sheet 20 of 34

# ADI CONTROL

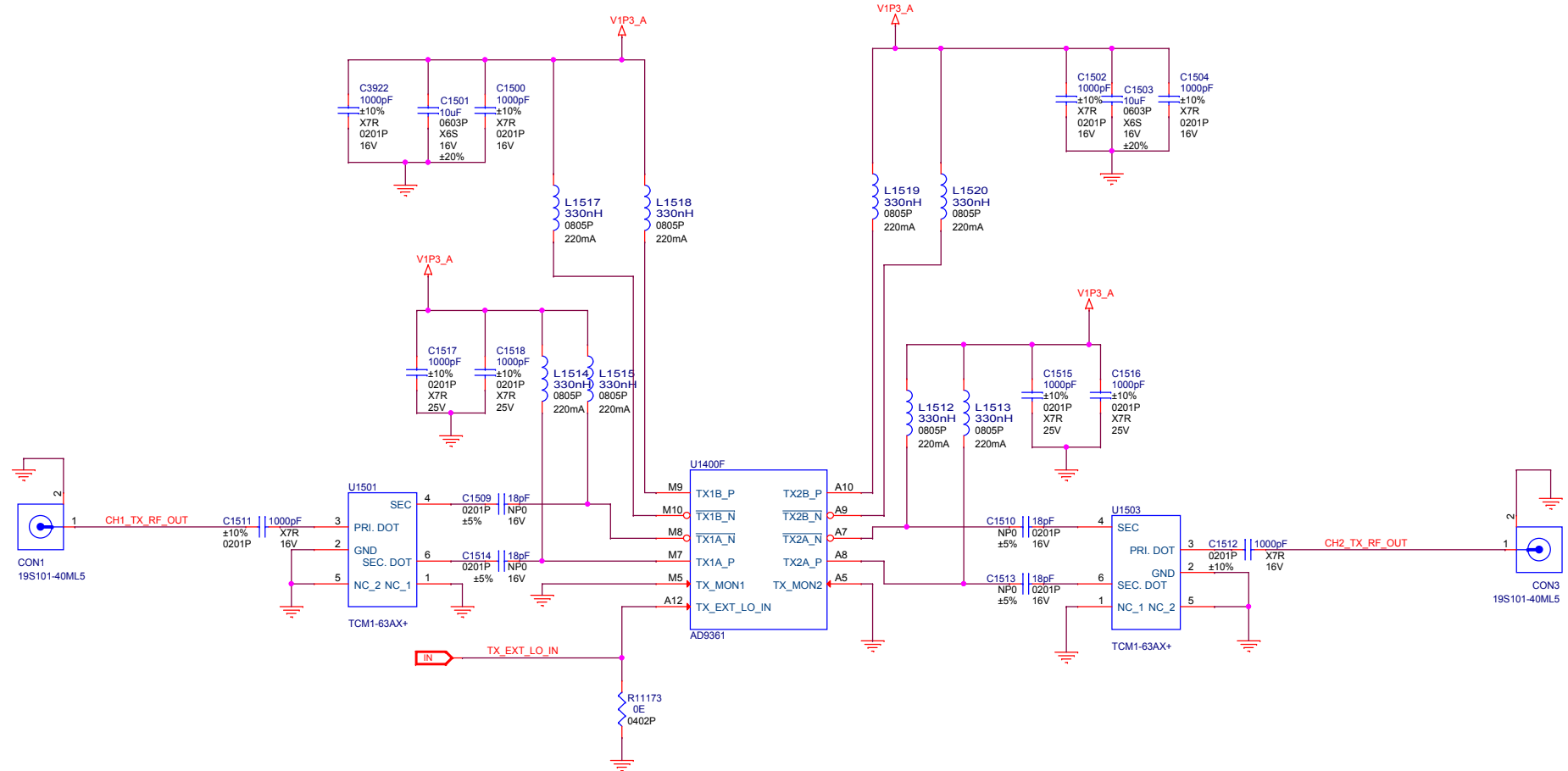


## LVL\_TRNSL

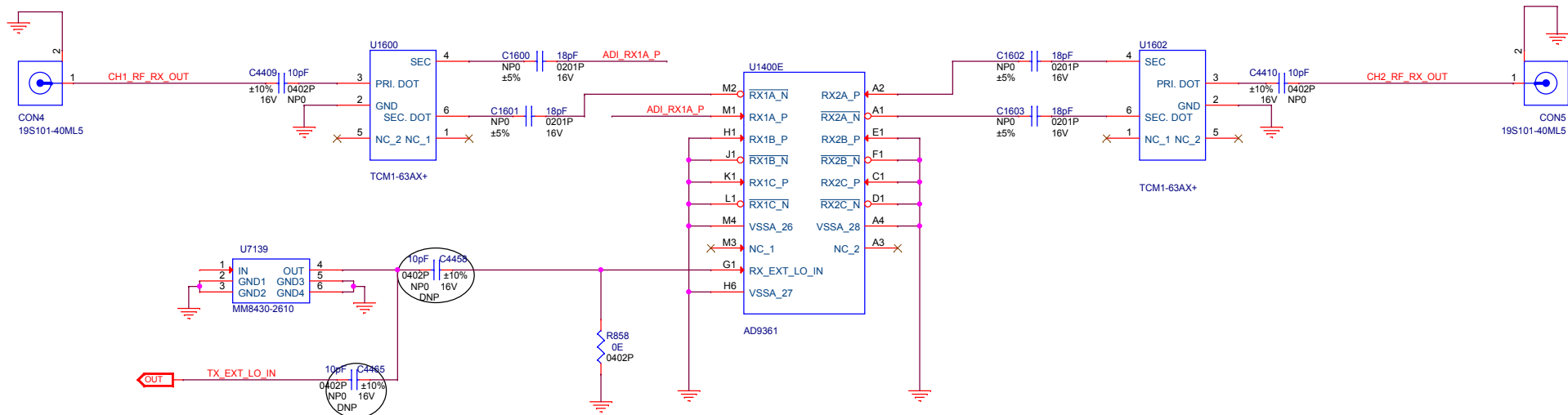


OpenCellular connect-1			
Title			
ADI CONTROL			
Size	Document Number	Rev	
A3	00	1.1	
Date:	Friday, January 06, 2017	Sheet	21 of 34

# AD\_TX



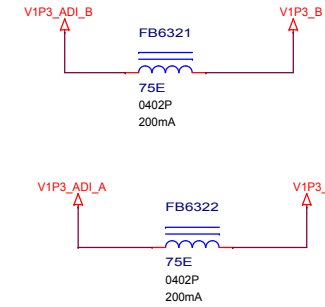
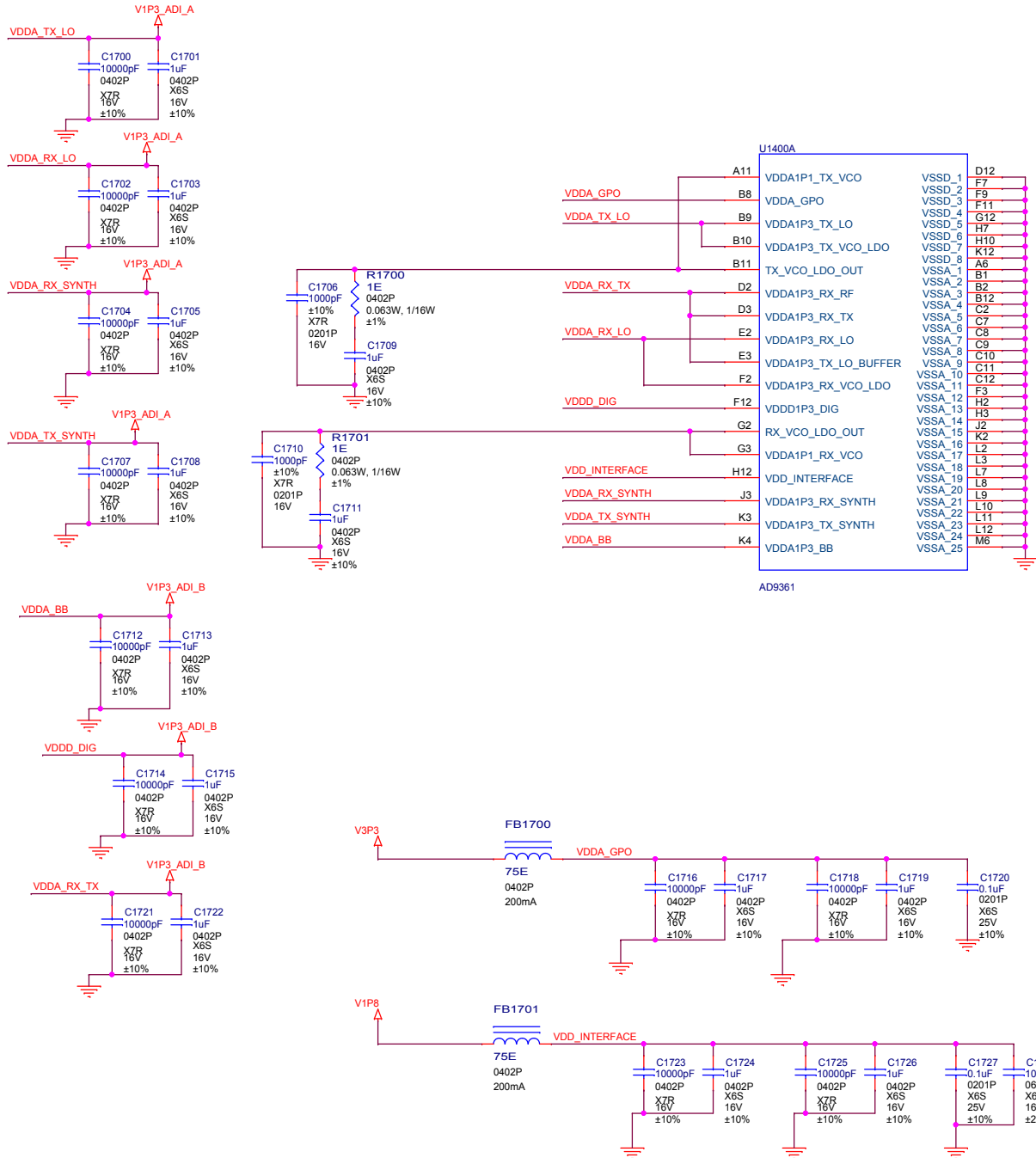
# AD\_RX\_PATH



Connector provision for external TX/RX lo input

Layout-Place capacitors C4458, C4465 as crowfeet provision (3 pad)

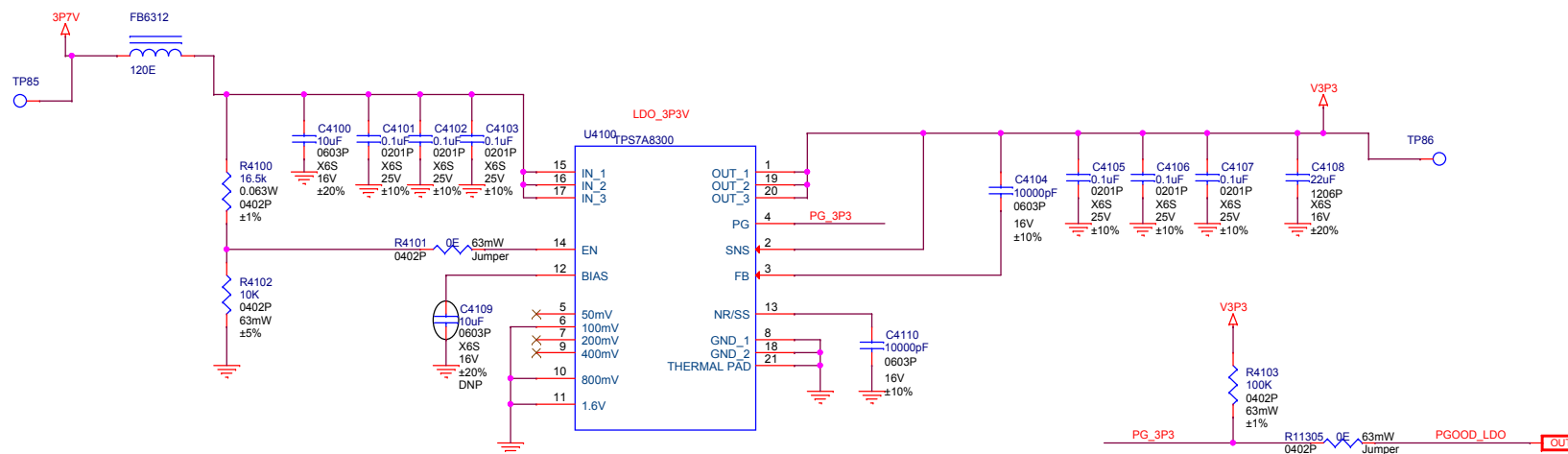
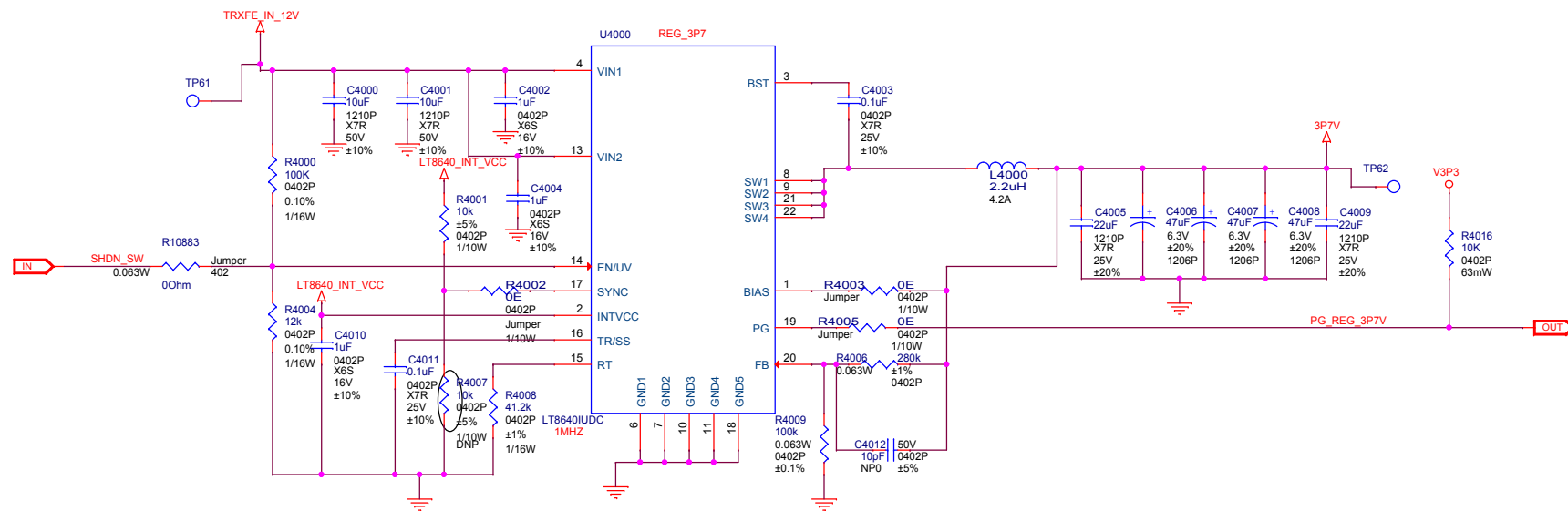
# AD\_POWER



OpenCellular connect-1			
Title			
AD_POWER			
Size	Document Number	Rev	
A3	00	1.1	
Date:	Friday, January 06, 2017	Sheet	24 of 34



REG\_3P7V\_LDO\_5V



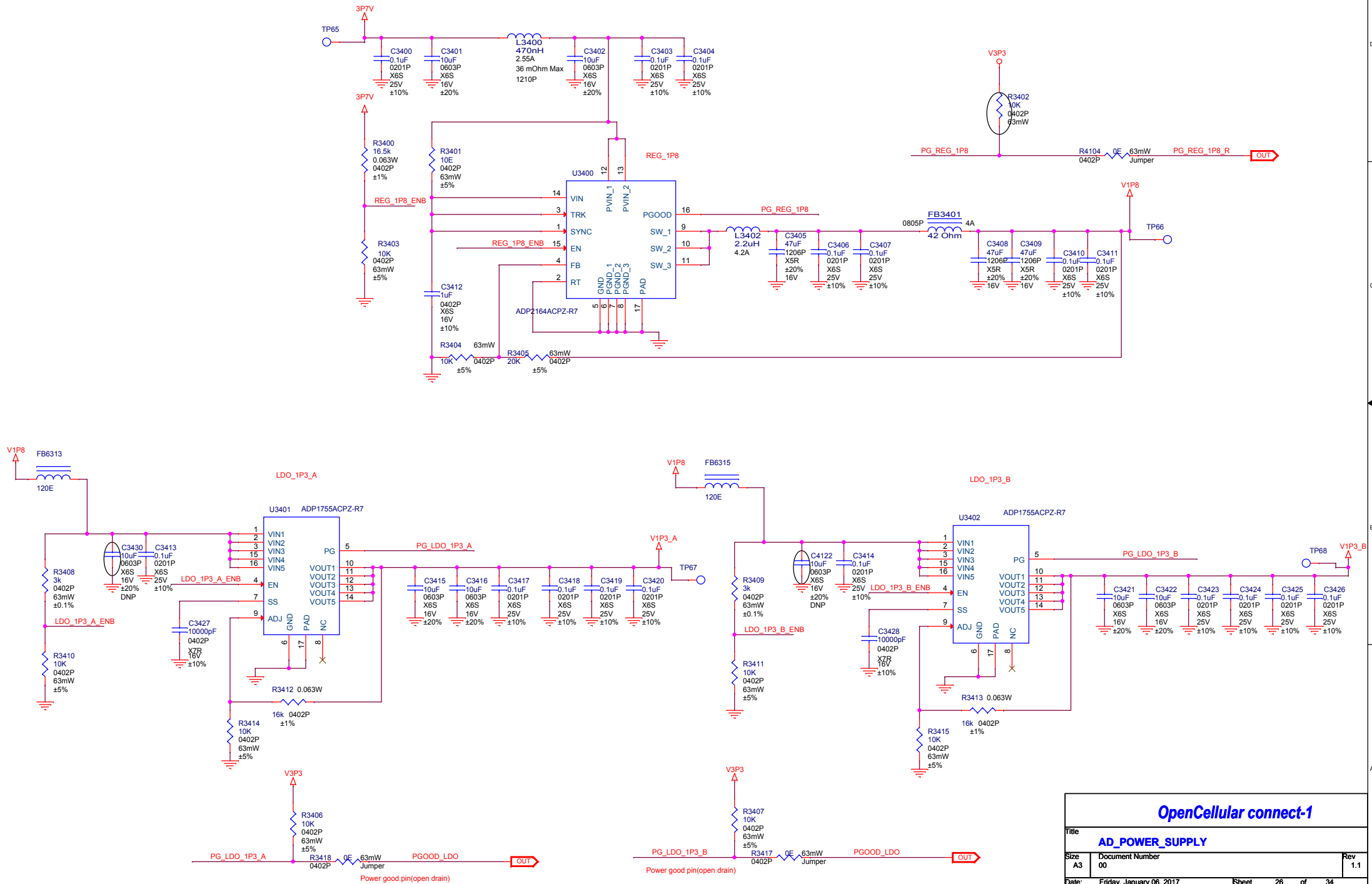
### OpenCellular connect-1

Title	REG_3P7V_LDO_3P3V
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Size A3	Document Number 00	Rev 1.1
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Date: Friday, January 06, 2017 Sheet 25 of 34

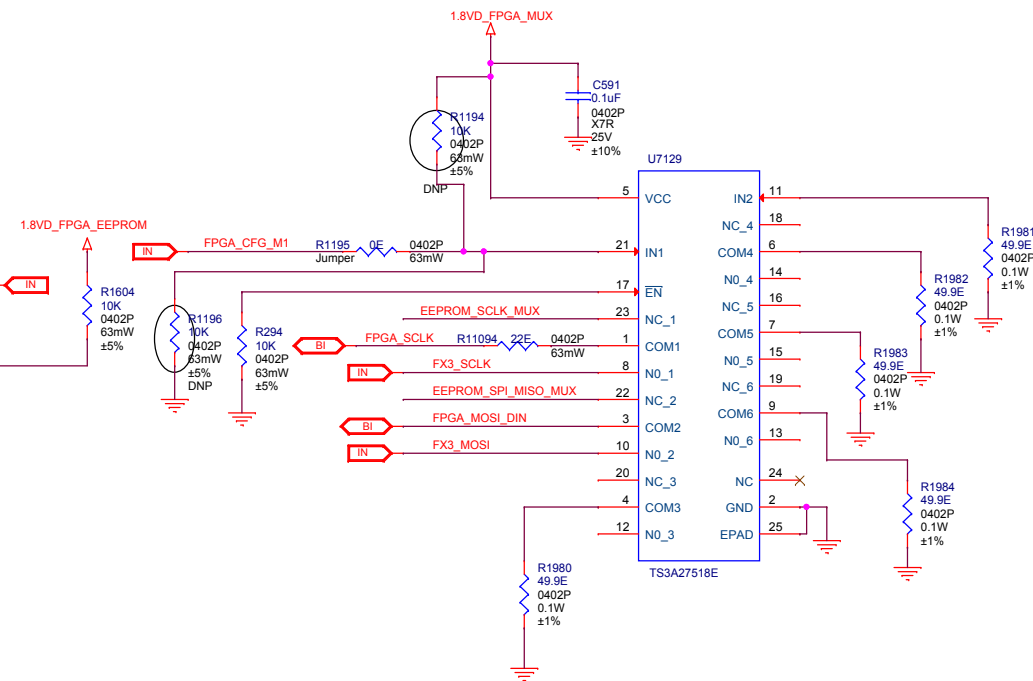
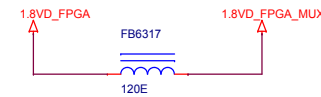
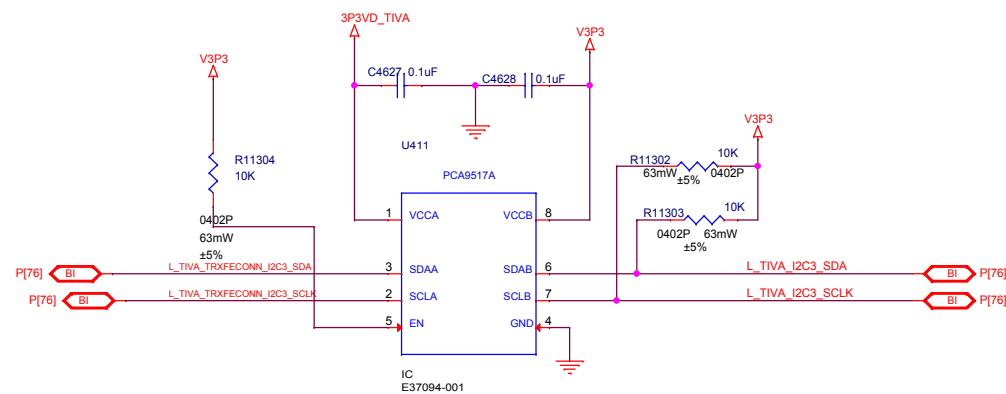
# AD\_POWER\_SUPPLY



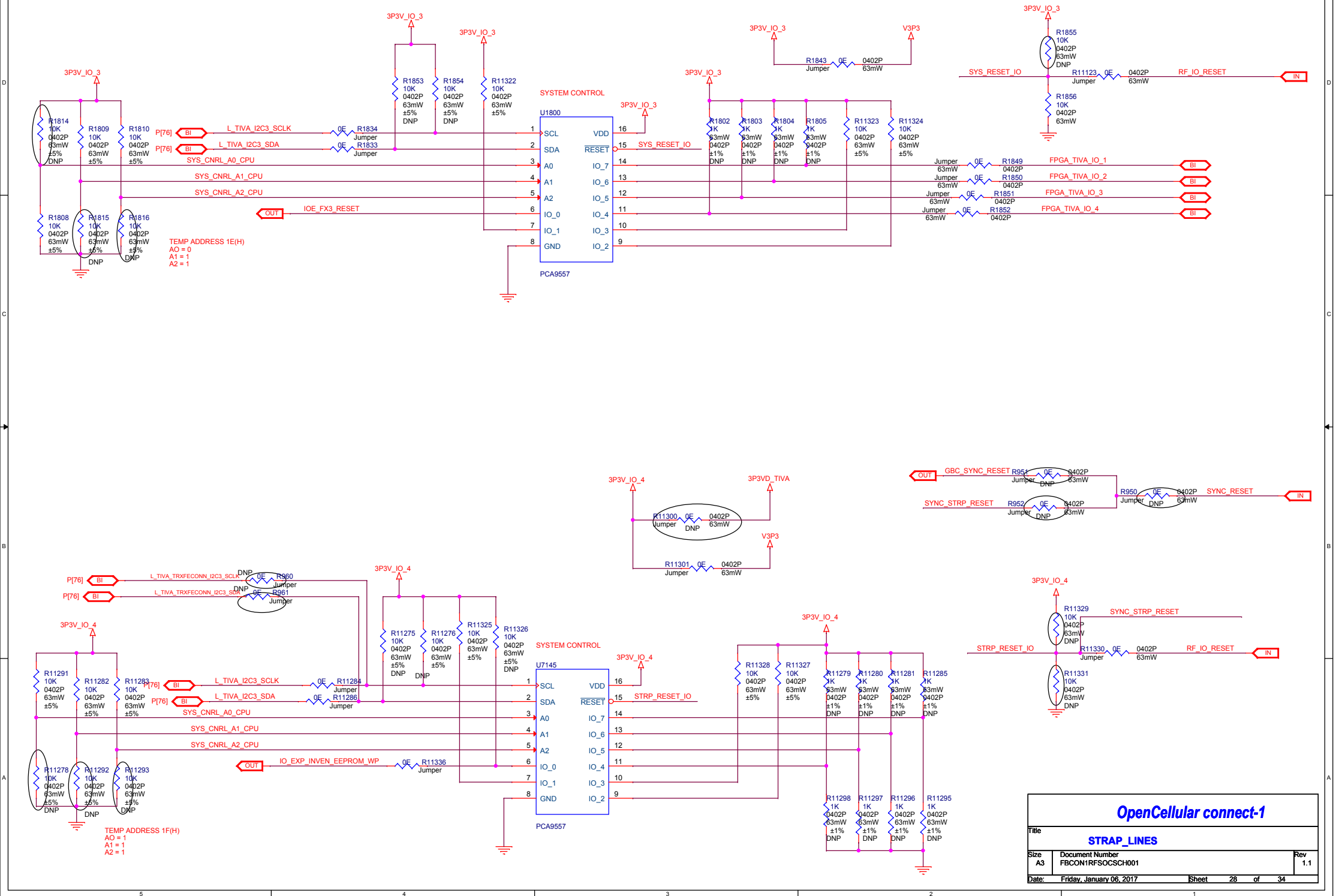
OpenCellular connect-1

Title			AD_POWER_SUPPLY
Size	A3	Document Number	00
Date:	Friday, January 06, 2017	Sheet	26 of 34
Rev	1.1		

1:6 MUX

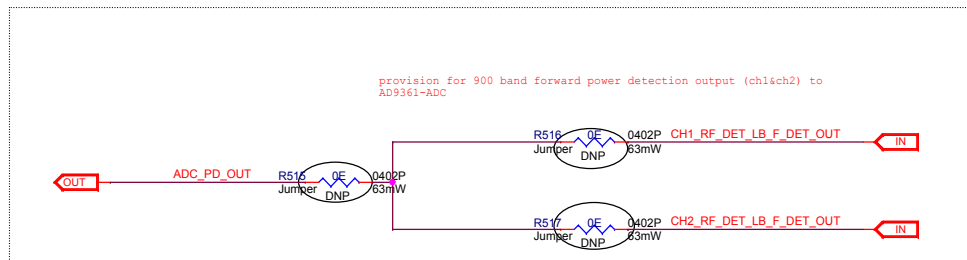
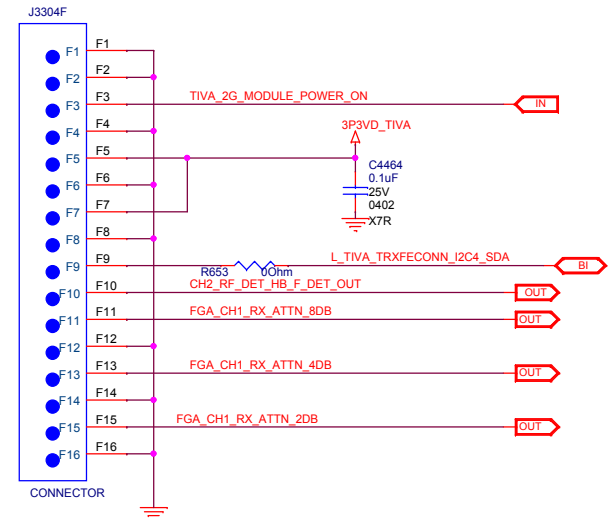
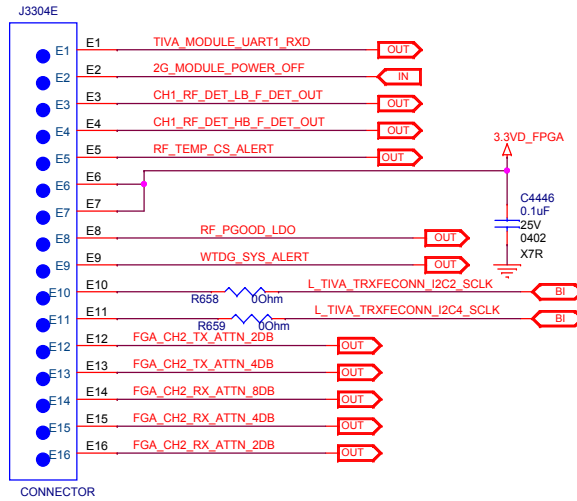
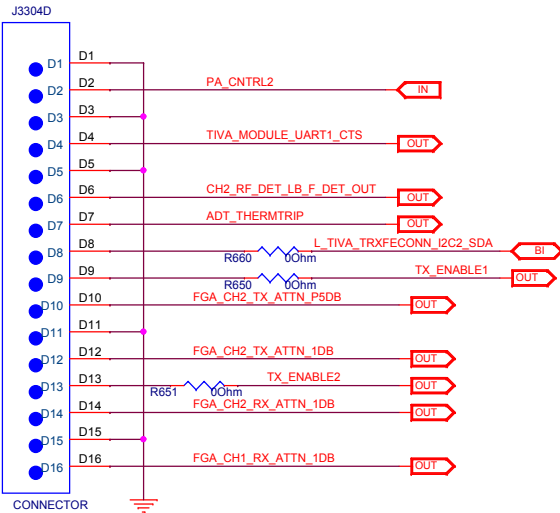
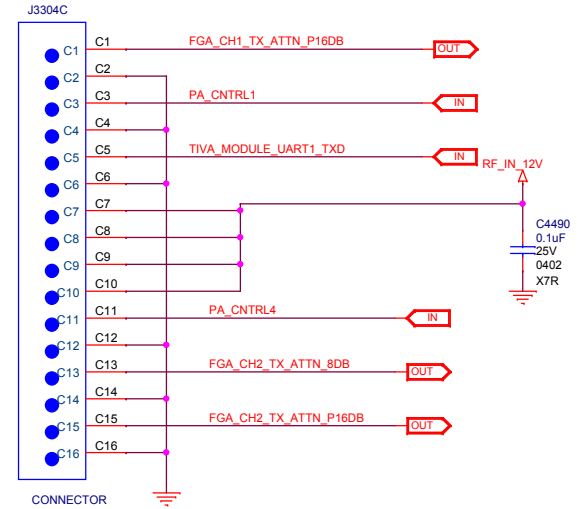
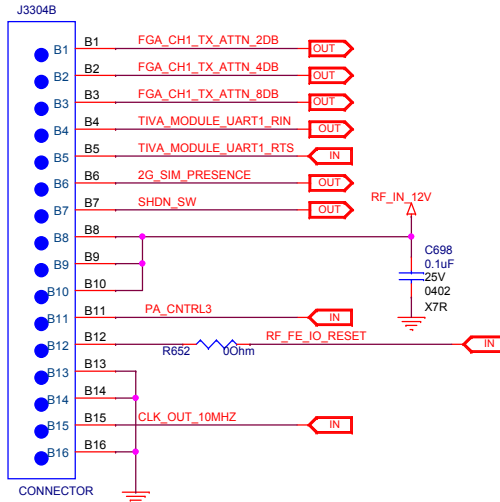
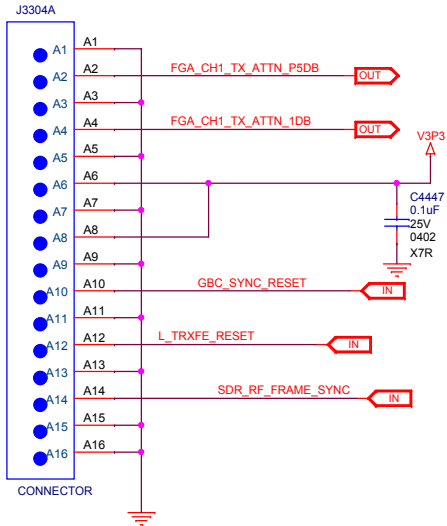
[illegible]

# IO EXPANDER TIVA CONTROLS





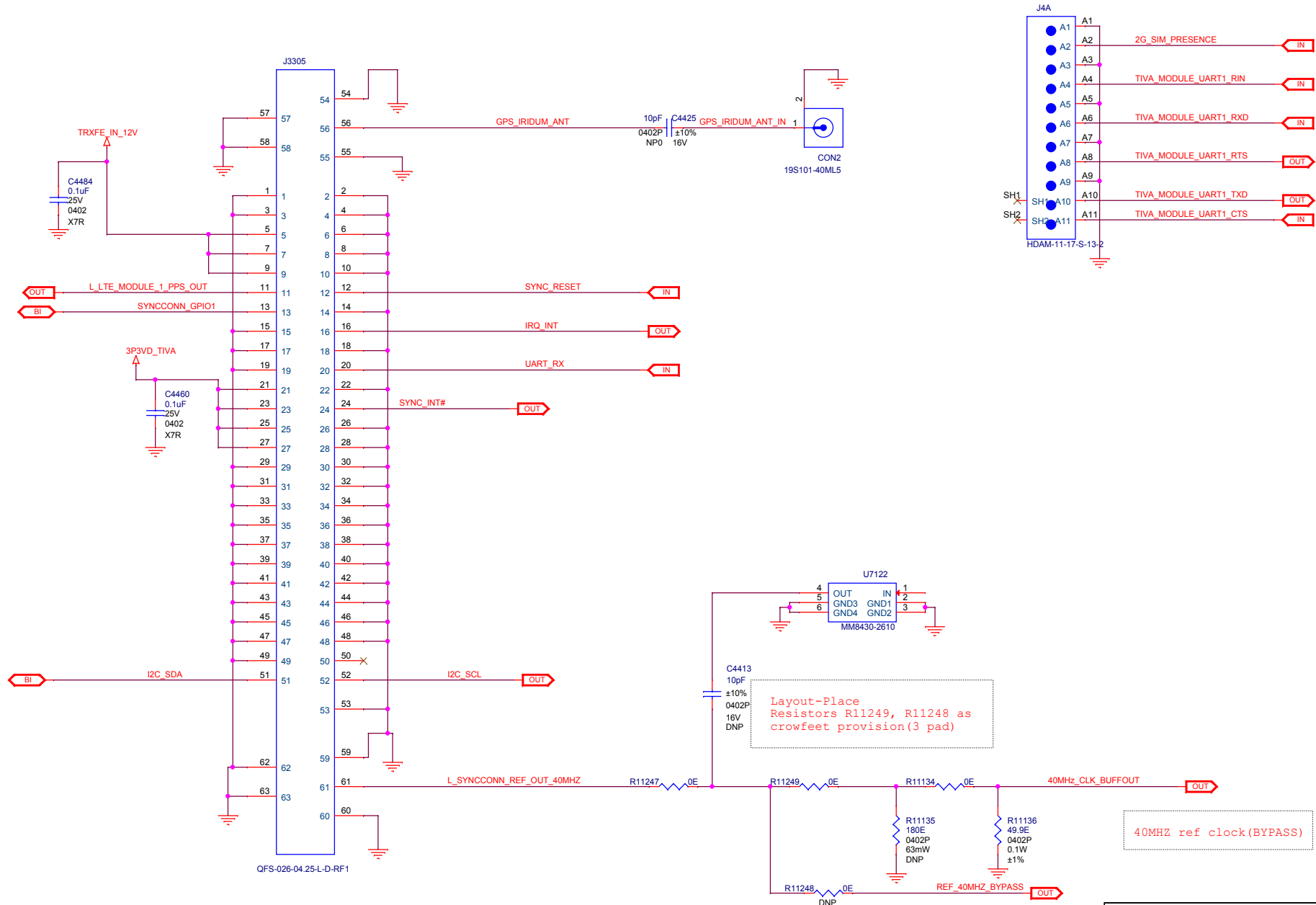
# RF BOARD CONNECTOR



OpenCellular connect-1

Title			RF BRD CONNECTOR
Size	A3	Document Number	FBCON1RFSOCSCH001
Date:	Friday, January 06, 2017	Sheet	30 of 34
Rev	1.1		

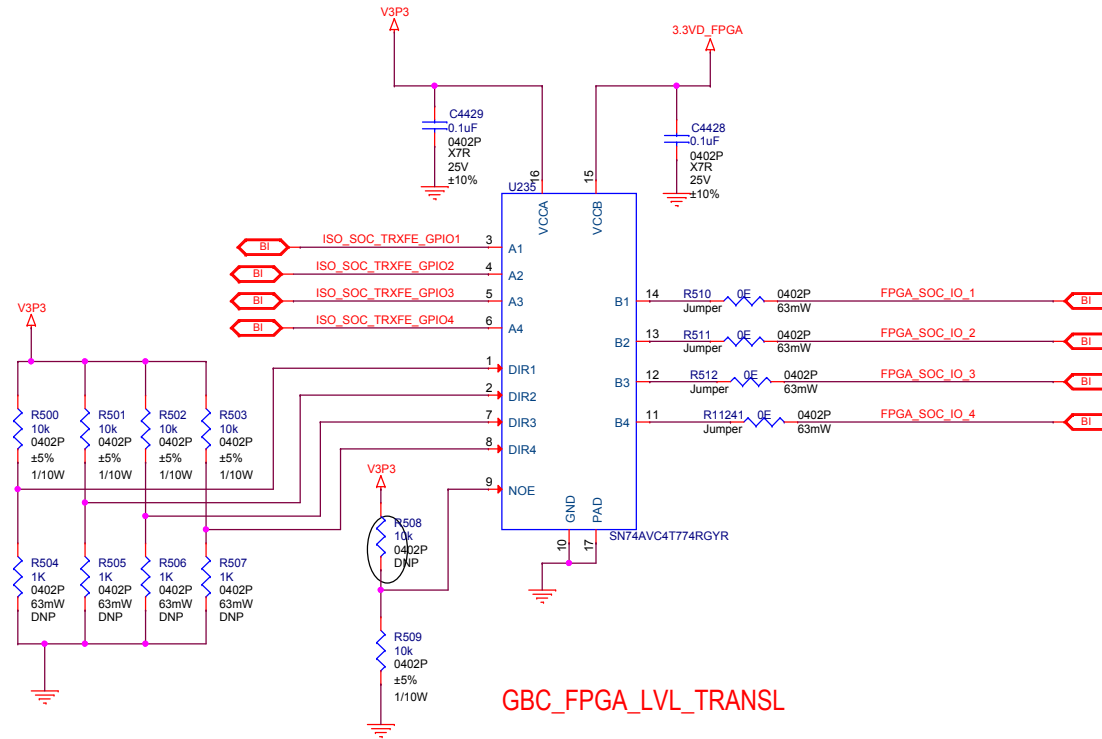
# SYNC BOARD CONNECTOR



OpenCellular connect-1		
Title		
SYNC BOARD CONNECTOR		
Size	Document Number	Rev
A3	FBCON1RFSOC SCH001	1.1
Date:	Friday, January 06, 2017	Sheet 31 of 34

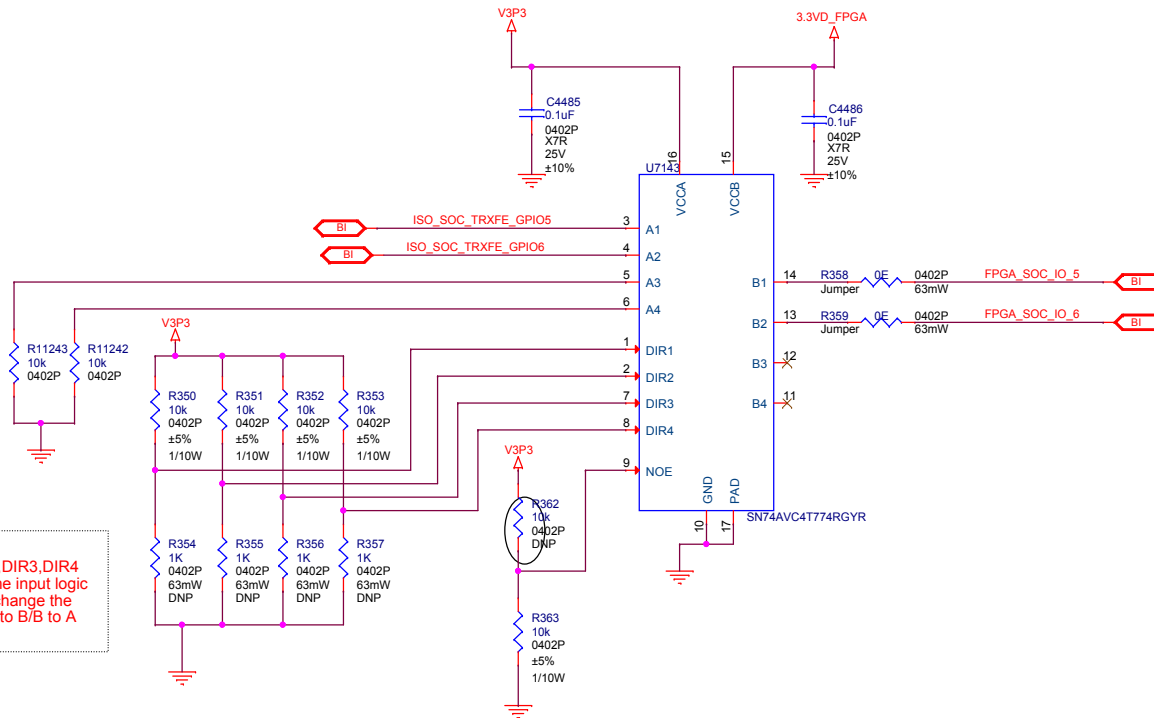
# GBC\_FPGA\_LVL\_TRANSL

DIR1,DIR2,DIR3,DIR4  
changing the input logic  
high/Low change the  
direction A to B/B to A



# GBC\_FPGA\_LVL\_TRANSL

DIR1,DIR2,DIR3,DIR4  
changing the input logic  
high/Low change the  
direction A to B/B to A

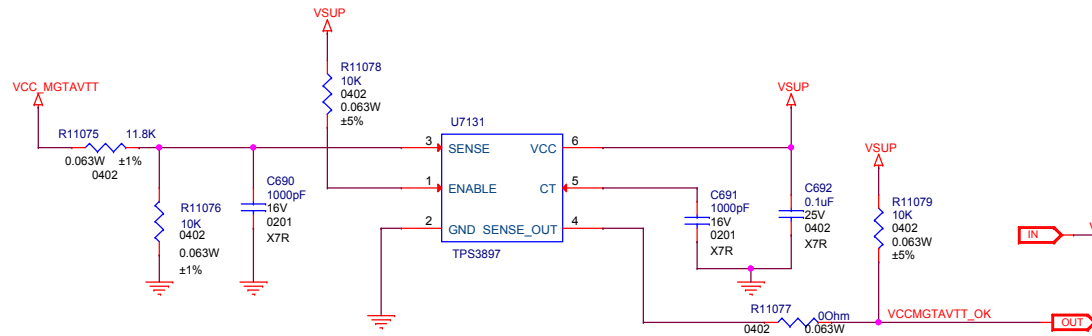


## OpenCellular connect-1

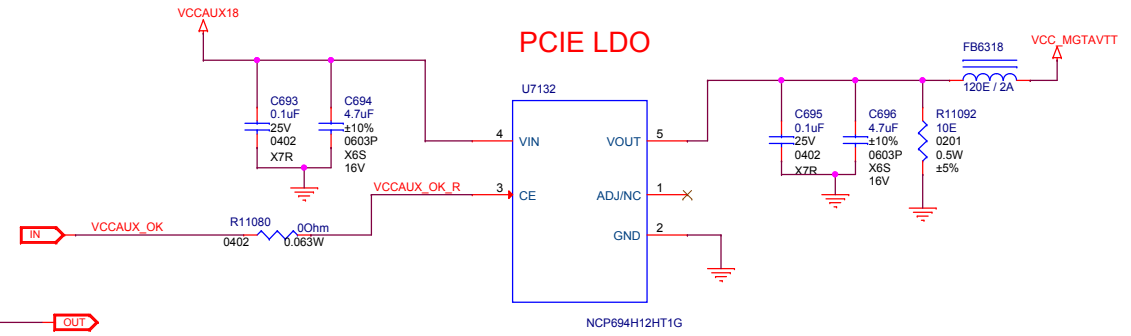
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Size	Document Number	
A3		
Date:	Friday, January 06, 2017	Sheet 32 of 34
Rev	1.1	



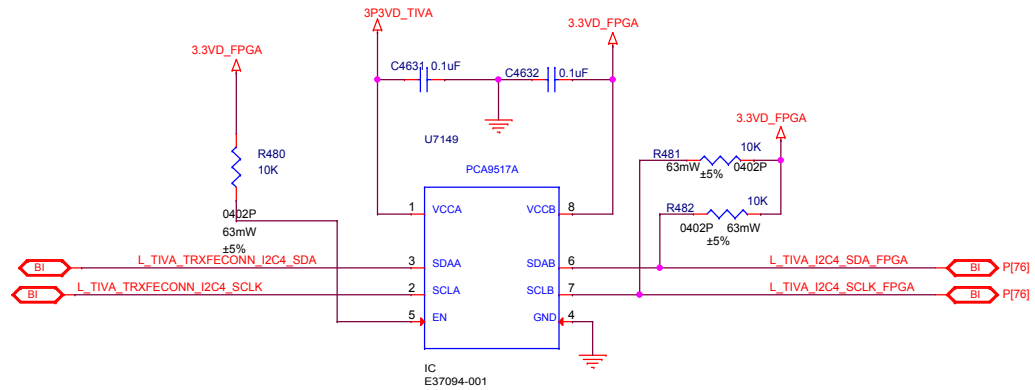
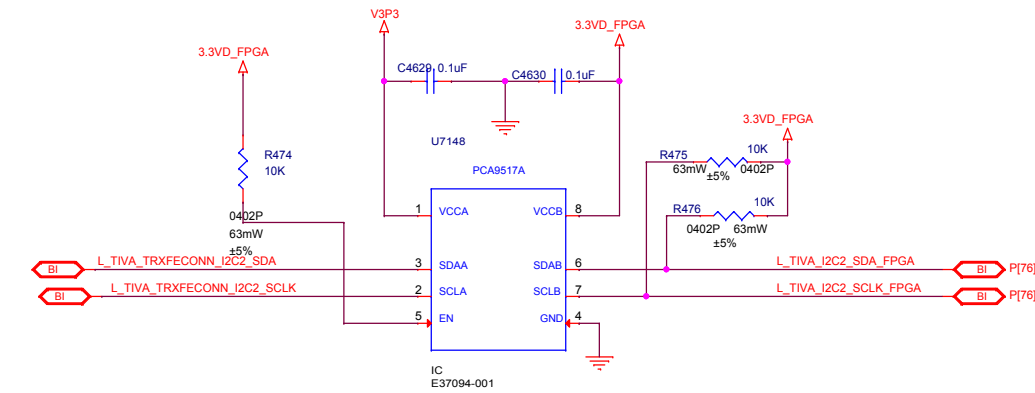
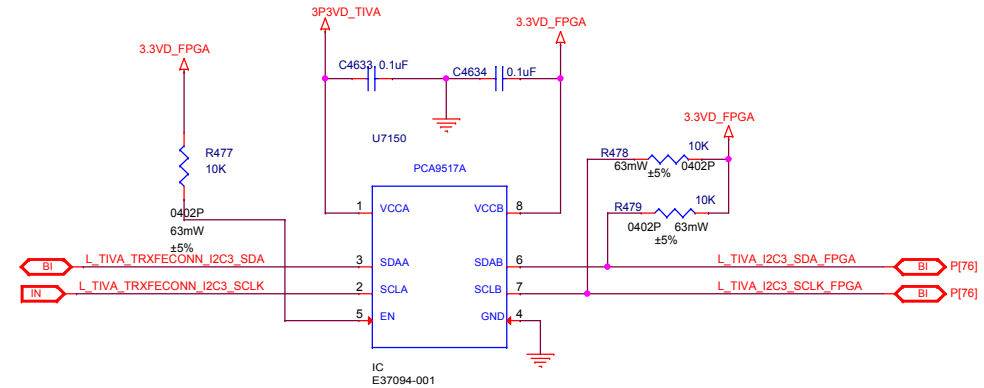
## PCIE POWER SUPPLY MONITOR



## PCIE LDO

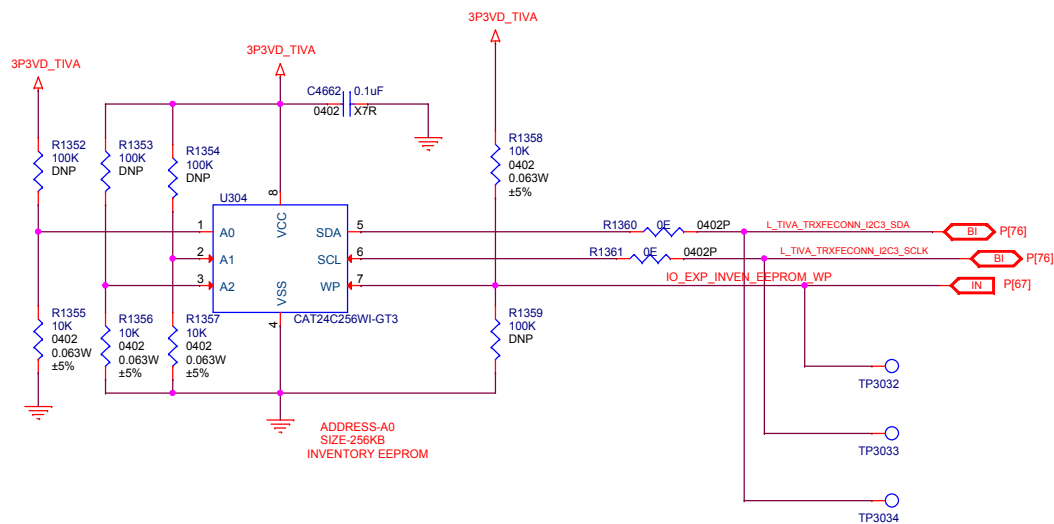
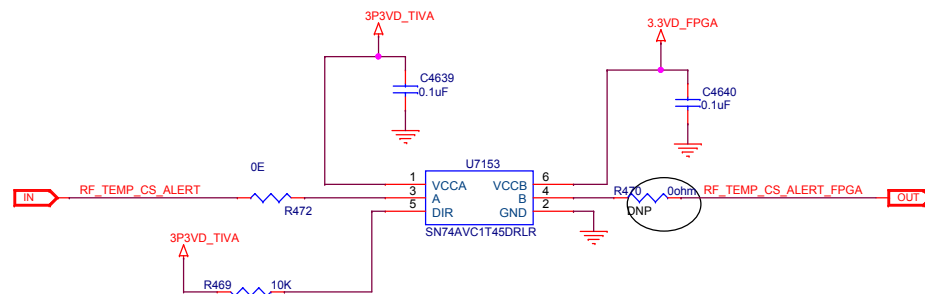
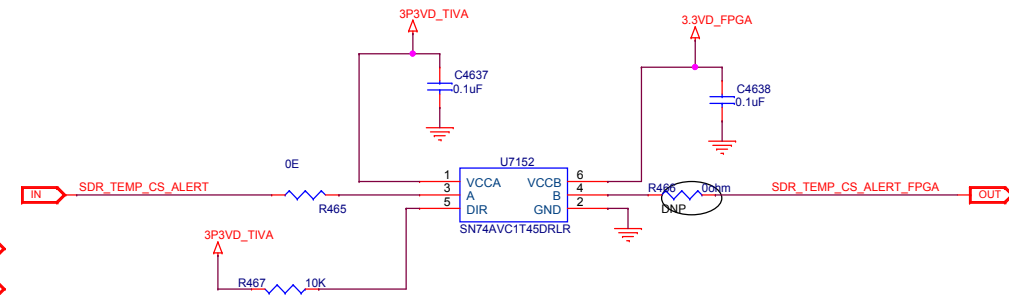
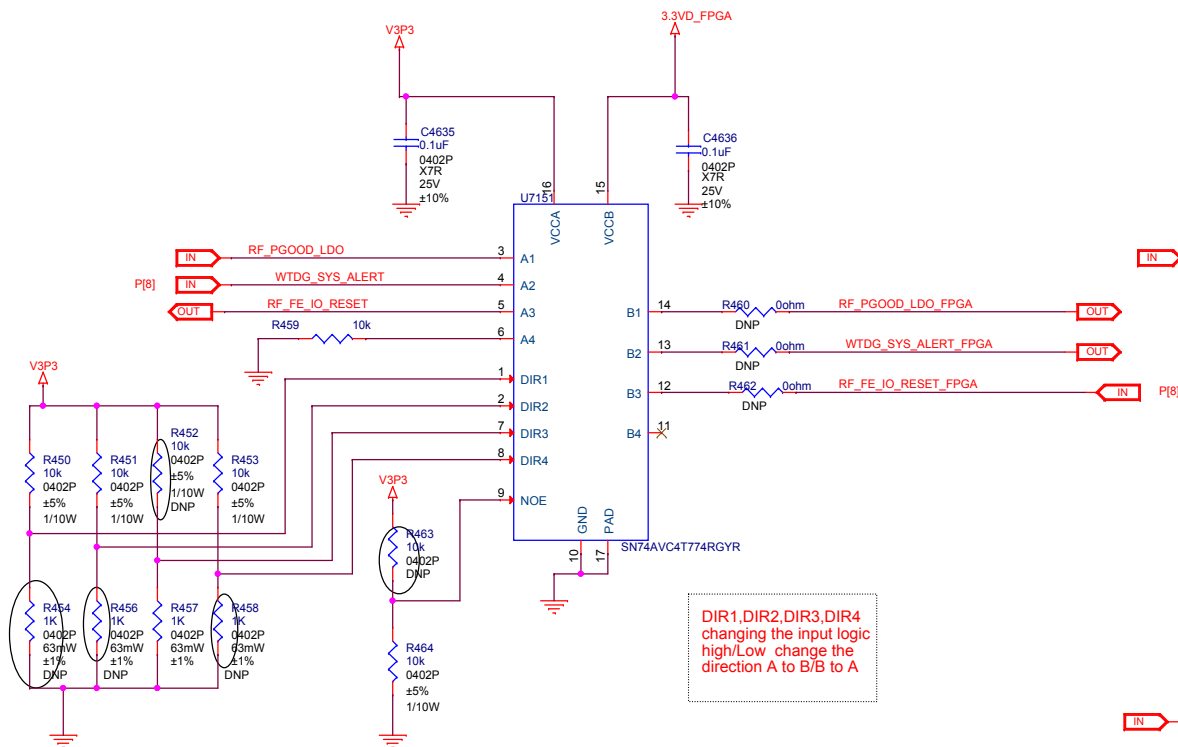


## LEVEL TRANSLATOR FOR I2C2



## OpenCellular connect-1

Title		
I2C_BUFFERS		
Size	Document Number	Rev
A3	FBCON1RFSOCSCH001	1.1
Date: Friday, January 06, 2017		
Sheet 33 of 34		



A9

M3 Sp

A10



M3 Space