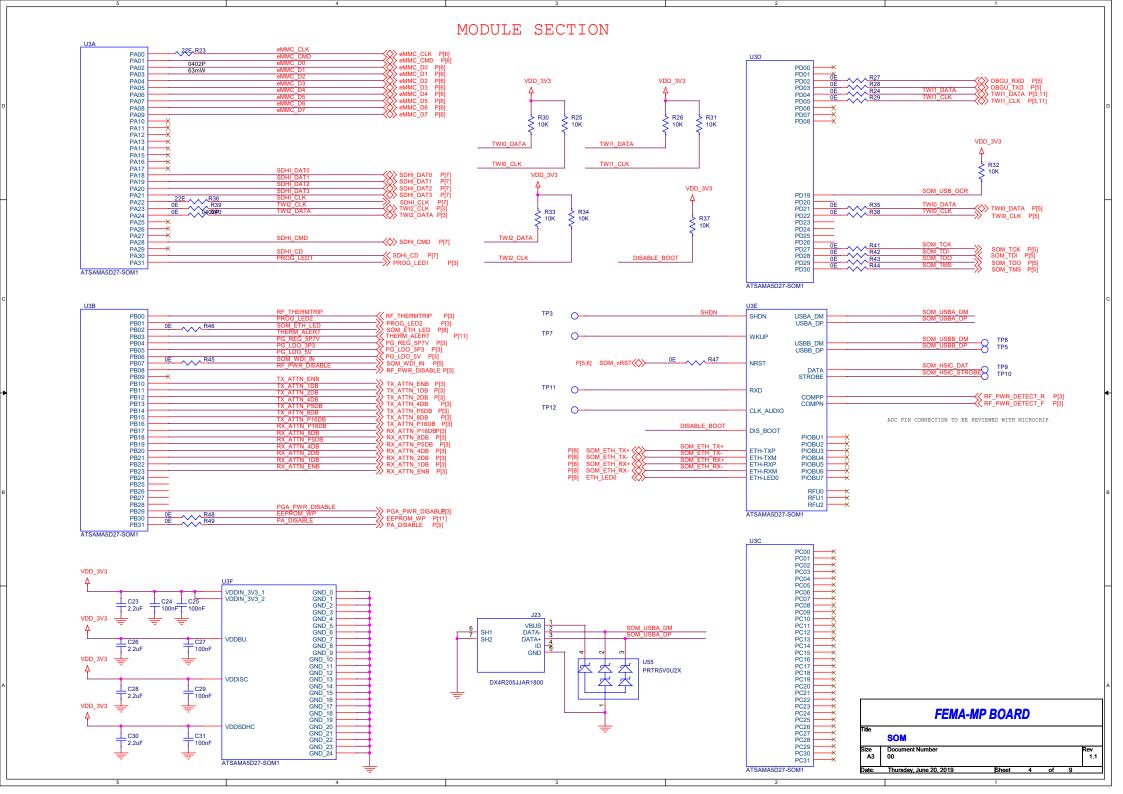
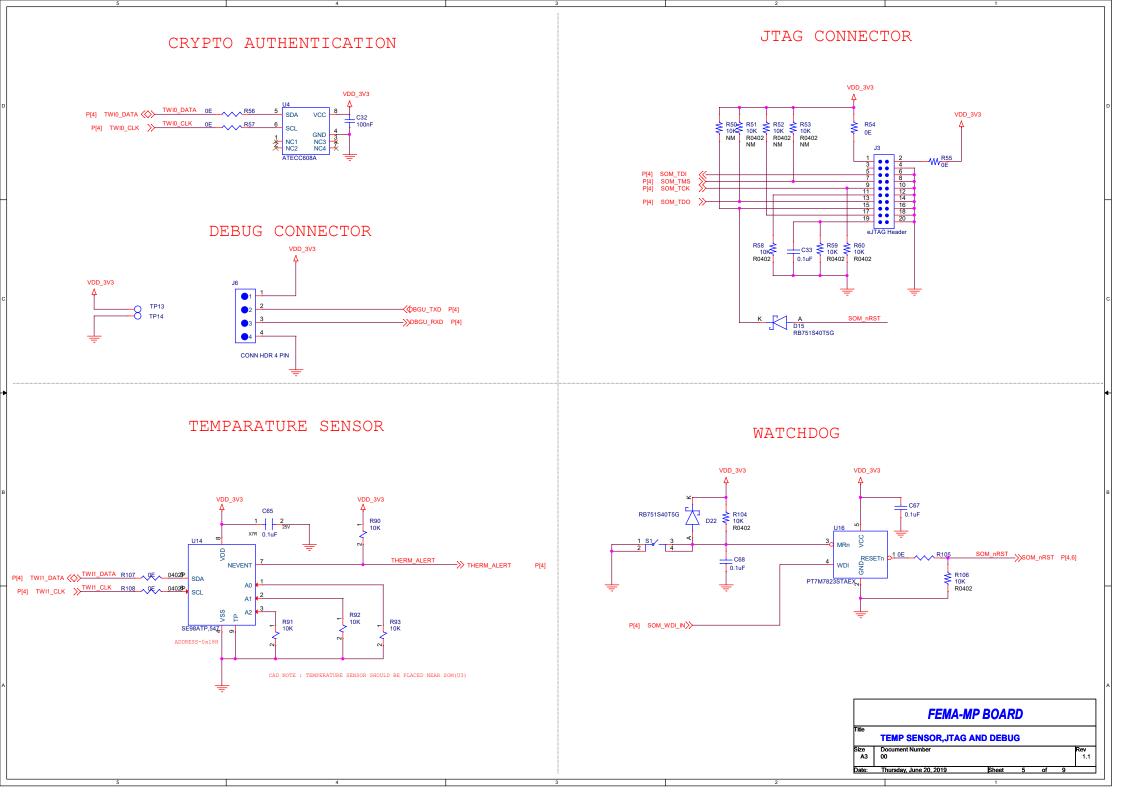
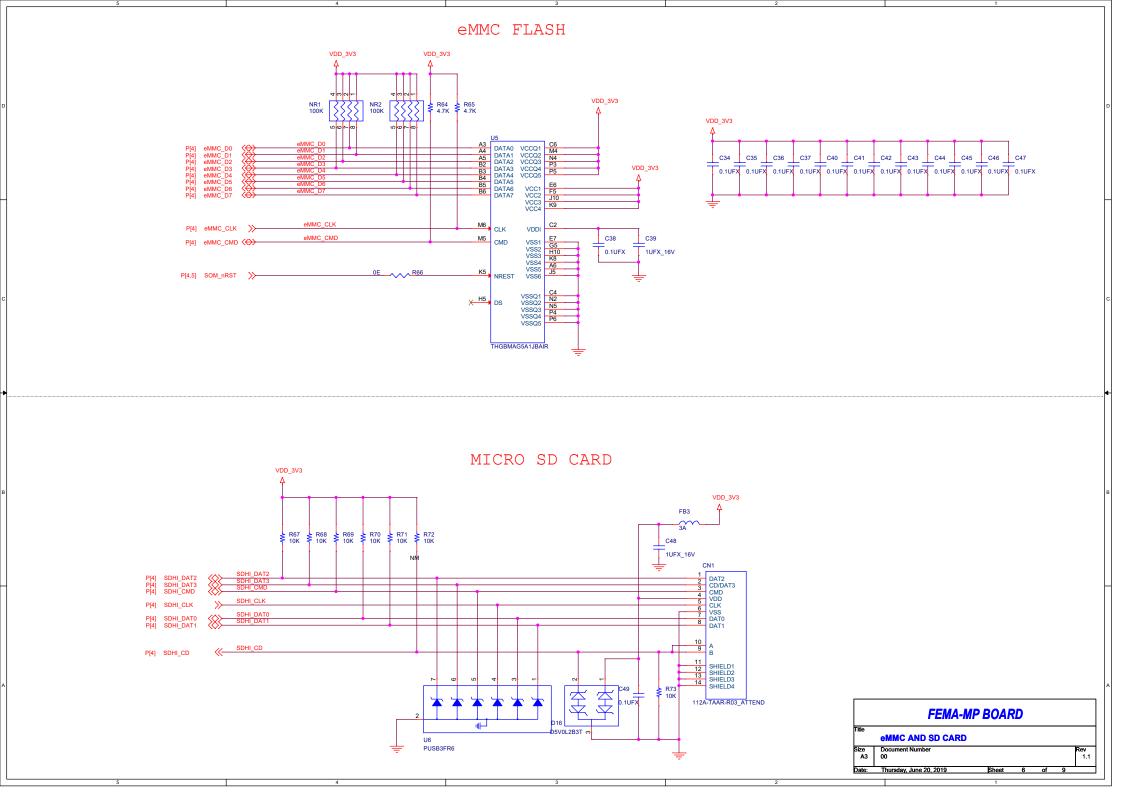


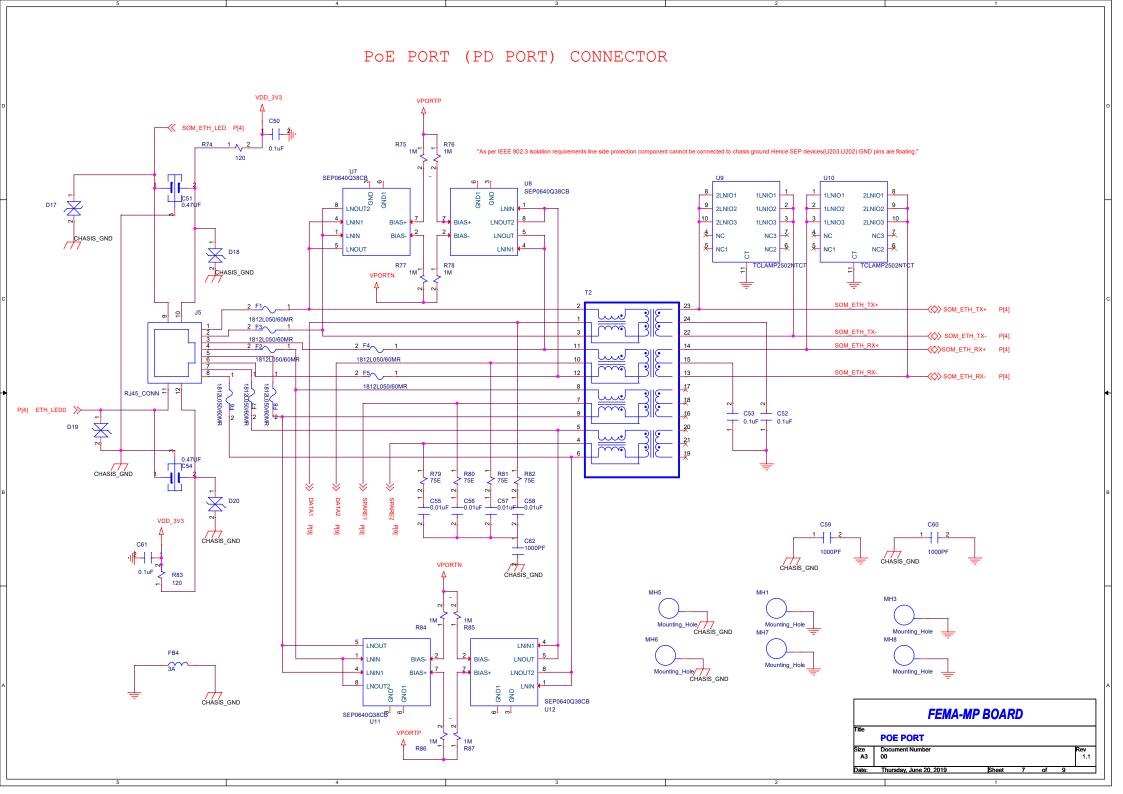
FEMA-MP BOARD						
Title	POWER DIAGRAM					
Size A3	Document Number 00					Rev 1.1
Date:	Thursday, June 20, 2019	Sheet	2	of	9	1

## BOARD TO BOARD CONNECTOR PROGRAMMABLE LED VDD\_3V3 B2B\_3V3 B2B\_12V TRXFE\_IN\_12V VDD\_3V3 C4741 C4742 NFM18PC225B1A3D NFM18PC225B1A3D R10784 B2B\_3V3 B2B\_12V D10116 P[4]PROG\_LED1 >> PROG\_LED1 R10783 210k 0402P 1710W RX ATTENUATION CONTROLS CONTROL SIGNALS RX ATTN ENB ≪ RX ATTN ENB P[13] R10785 100K 0402P 1/10W 5% C2035 --0.1uF --10% 25V X7R 0402P RX ATTN 1DB KRX\_ATTN\_1DB P[13] PGA\_PWR\_DISABLE RX\_ATTN\_2DB P[4] PGA\_PWR\_DISABLE >> RX\_ATTN\_2DB P[13] RX\_ATTN\_4DB RF\_PWR\_DETECT\_F R61 \_ 0E P[4] RF\_PWR\_DETECT\_F>> RX\_ATTN\_4DB P[13] RX\_ATTN\_P5DB P[4] RF\_PWR\_DISABLE>> RX\_ATTN\_8DB RX\_ATTN\_8DB P[13] RX\_ATTN\_P16DE P[4] RF\_PWR\_DETECT\_R>>-RX\_ATTN\_P16DB P[13] RF\_THERMTRIP 30 P[4] RF\_THERMTRIP >> PA\_EN\_CNTRL TX\_ATTN\_ENB TX\_ATTN\_ENB P[4] PG\_REG\_5P7V TX\_ATTN\_1DB VDD\_3V3 32 P[4] PG\_REG\_5P7V >> TX\_ATTN\_1DB P[4] PG\_LDO\_3P3 33 TX\_ATTN\_2DB P[4] PG\_LDO\_3P3 >>> TX\_ATTN\_2DB P[4] PG\_LDO\_5V 34 TX\_ATTN\_4DB R10786 120 P[4] PG\_LDO\_5V >> TX\_ATTN\_4DB P[4] TX\_ATTN\_P5DB 0805P 1/8W 5% TWO WIRE INTERFACE(12C) TX\_ATTN\_8DB TX\_ATTN\_8DB P[4] TX\_ATTN\_P16DB TWI2\_DATA P[4] TWI2\_DATA TX\_ATTN\_P16DB P[4] D10117 P[4] TWI2\_CLK >> TX ATTENUATION CONTROLS TWI1\_DATA P[4,11] TWI1\_DATA < P[4,11] TWI1\_CLK >>-P[4] PROG\_LED2 >> PROG\_LED2 PC4 PC2 R10788 100K 0402P C2036 -0.1uF -10% 25V X7R 0402P 1/10W 5% FX23L-50-P-0.5SV10 TWO INPUT NAND GATE WITH ONE INVERTED INPUT VDD\_3V3 U9001 5 RF\_THERMTRIP 4 R10789 0EPA\_EN\_CNTRL **FEMA-MP BOARD** P[4,11] PA\_DISABLE >> PA\_DISABLE IN0 R10790 **B2B CONNECTOR AND LED** SN74LVC1G97DBVR Size A3

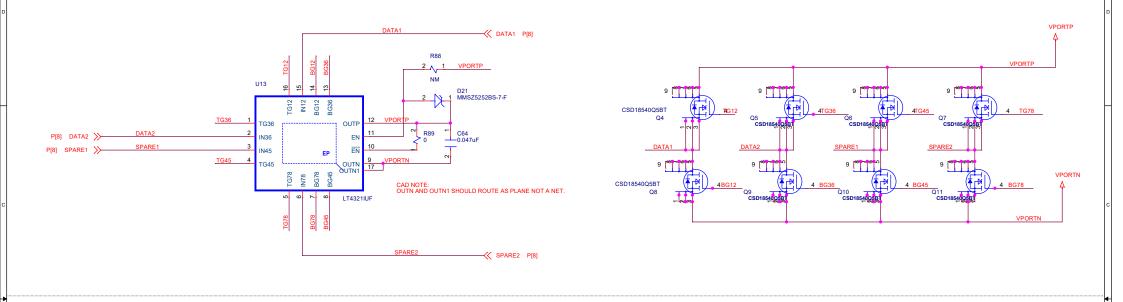




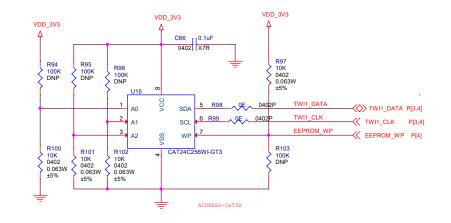








## INVENTORY EEPROM-256KB



**FEMA-MP BOARD** 

**POE BRIDGE** 

