BOARD TO BOARD CONNECTOR J113B E1 C1989 1 20.1uFTRXFE_USB3_RXG N 25V 10% ● B1 B1 ● B2 B2 ● E2 ● F2 TRXFE_USB3_TX0_N ● B3 F4 E5 F5 ● B5 E6 F6 Ę7 F7 L_TIVA_TRXFECONN_I2C2_SCLK L_TIVA_TRXFECONN_I2C2_SDA E8 F8 L_TIVA_TRXFECONN_I2C4_SCLK E9 L_TIVA_TRXFECONN_I2C3_SCLK F9 L_TIVA_TRXFECONN_I2C3_SDA Ç9 _TIVA_TRXFECONN_I2C4_SDA E10 L_TRXFE_RESET F10 C10 E11 C11 C11 SH1 HDAF-11-18-S-13-2 HDAF-11-18-S-13-2 HDAF-11-18-S-13-2 SH2 J113A HDAF-11-18-S-13-2 A2 A2 2G_SIM_PRESENCE J113H J113I H1 SDR_TEMP_CS_ALERT TIVA_MODULE_UART1_RIN SDR REG LDO PGOOD J2 | A5 J3 3 _2G_MODULE_POWER_ON TRXFE_IN_12V ● H3 | • TIVA_MODULE_UART1_RXD C4459 10uF IRQ_INT • Α7 TRXFE_IN_12V 16V 0603 TIVA_MODULE_UART1_RTS • 3.3VD_TRXFE ₹X6S 17 TIVA_MODULE_UART1_TXD • 18 TIVA_MODULE_UART1_CTS RF_FE_IO_RESET • TRXFE_IN_12V HDAF-11-18-S-13-2 110 J10 111 HDAF-11-18-S-13-2 HDAF-11-18-S-13-2 HDAF-11-18-S-13-2 J113D J113K J113M J113G K1 RF_PGOOD_LDO ISO SOC TRXFE GPIO3 G1 D2 J113L D2 1 G2 OUT **G2** ISO SOC TRXFE GPIO D3 G3 ● G3 F D4 K4 Ģ4 D5 G5 PCIE0 TXP D6 K6 PCIE0 RXN Ģ6 IN P[8] ● G6 } D7 G7 ● M7 PCIE0_RXP D8 K8 TRXFE_IN_12V M8 PCIE0_GBE_CLKN G8 **G**8) D9 ● G9 F PCIE0 GBE CLKP D10 G10 D11 K11 G11 L10 C1880 -0.1uF HDAF-11-18-S-13-2 HDAF-11-18-S-13-2 HDAF-11-18-S-13-2 HDAF-11-18-S-13-2 **Facebook** HDAF-11-18-S-13-2 **BOARD TO BOARD CONNECTOR** Thursday, January 05, 2017



