


Title <i>Power</i>			
Size: <i>A4</i>	Number:	Revision <i>V1.0</i>	
Date: <i>28.12.2016</i>	Time: <i>01:35:33</i>	Sheet <i>1</i> of <i>5</i>	
File: <i>D:\Ultrasonic\Jammer\power.SchDoc</i>			

A

B

C

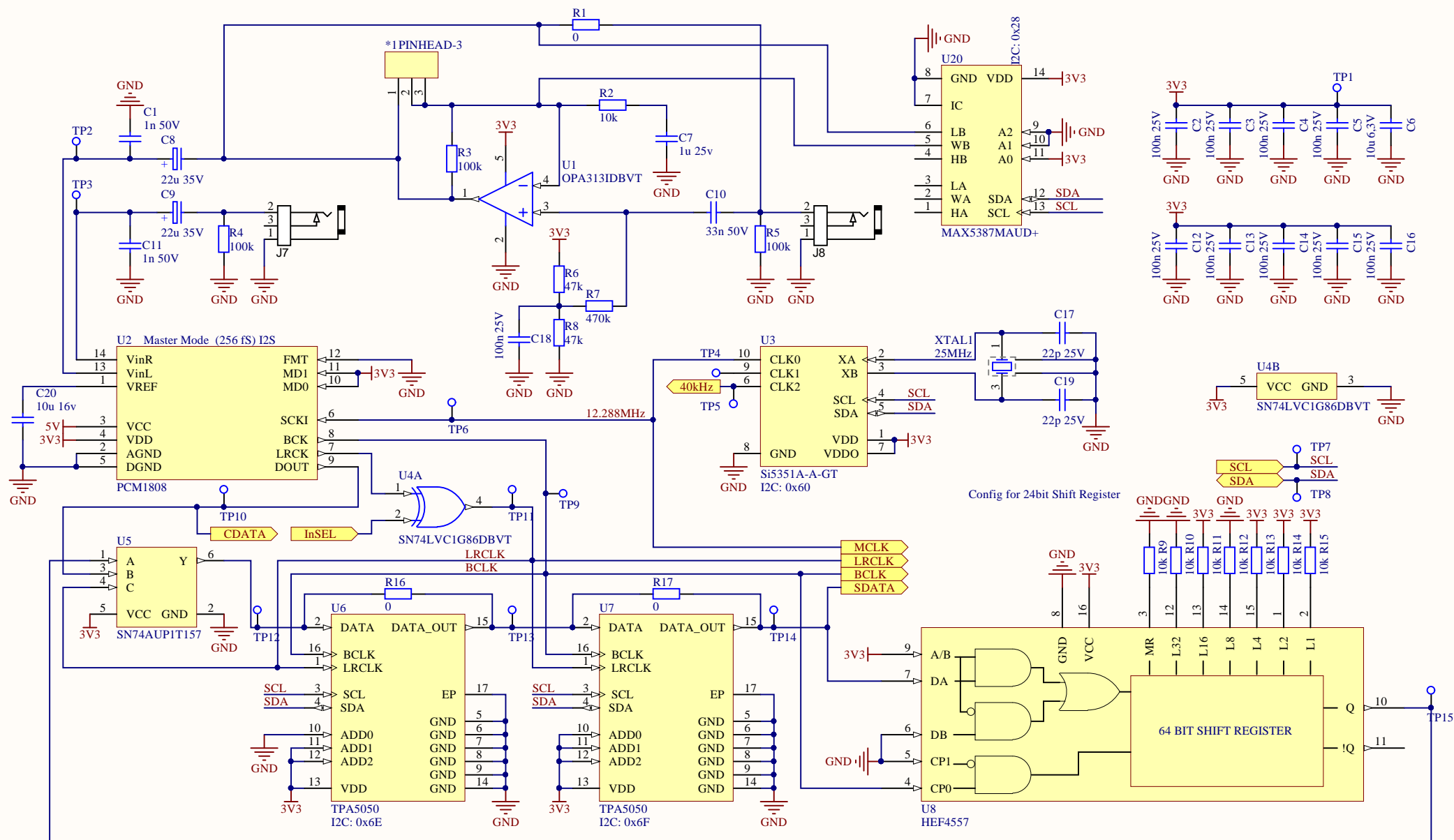
D

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C

D

Title **ADC & Delay**Size: **A4**

Number:

Revision: **V1.0**

Date: 28.12.2016

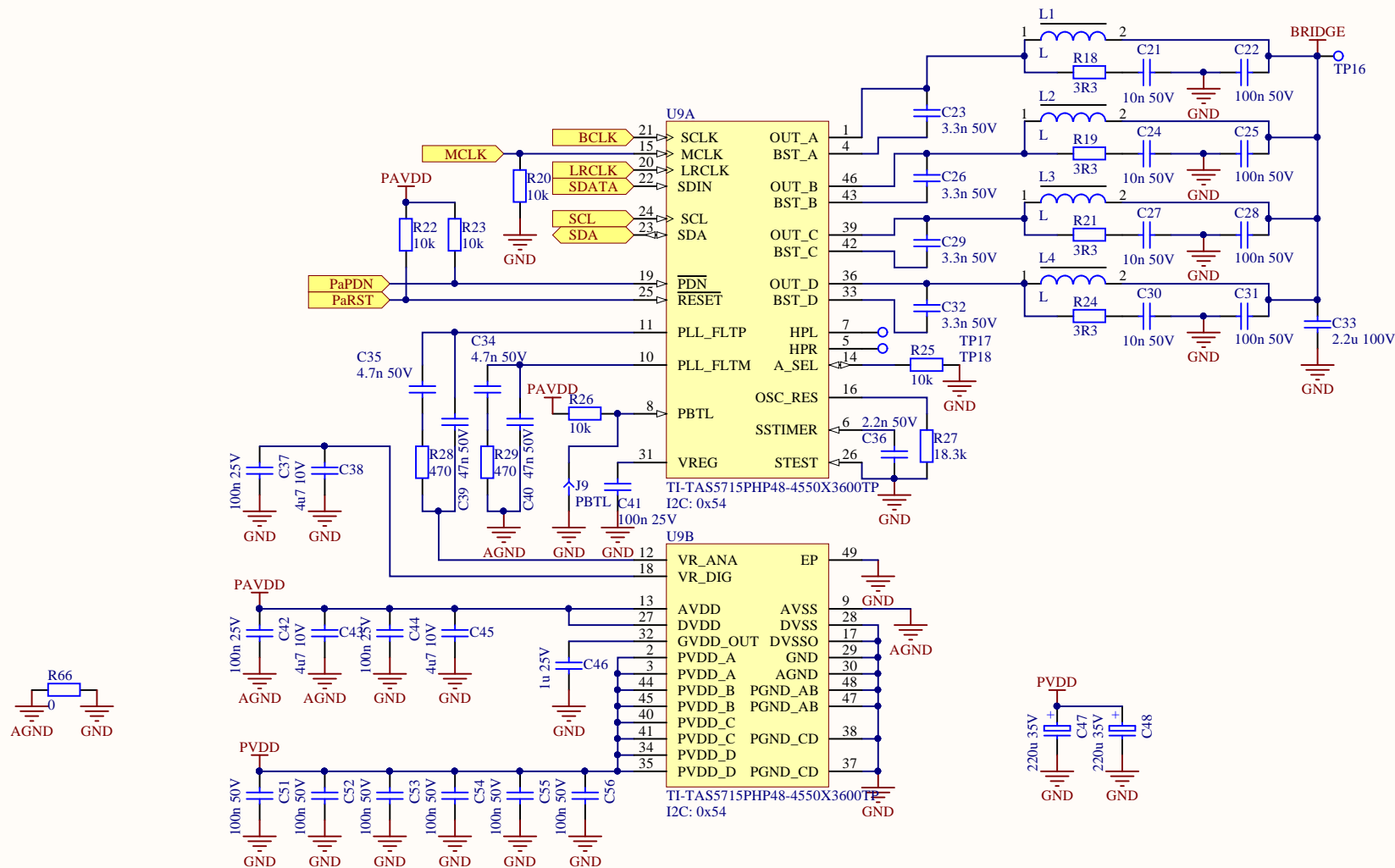
Time: 01:35:33

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Niklas Fauth

File: D:\Ultrasonic\Jammer\logic.SchDoc

Altium

Title **Class D DC/DC**Size: **A4**

Number:

Revision: **V1.0**

Date: 28.12.2016

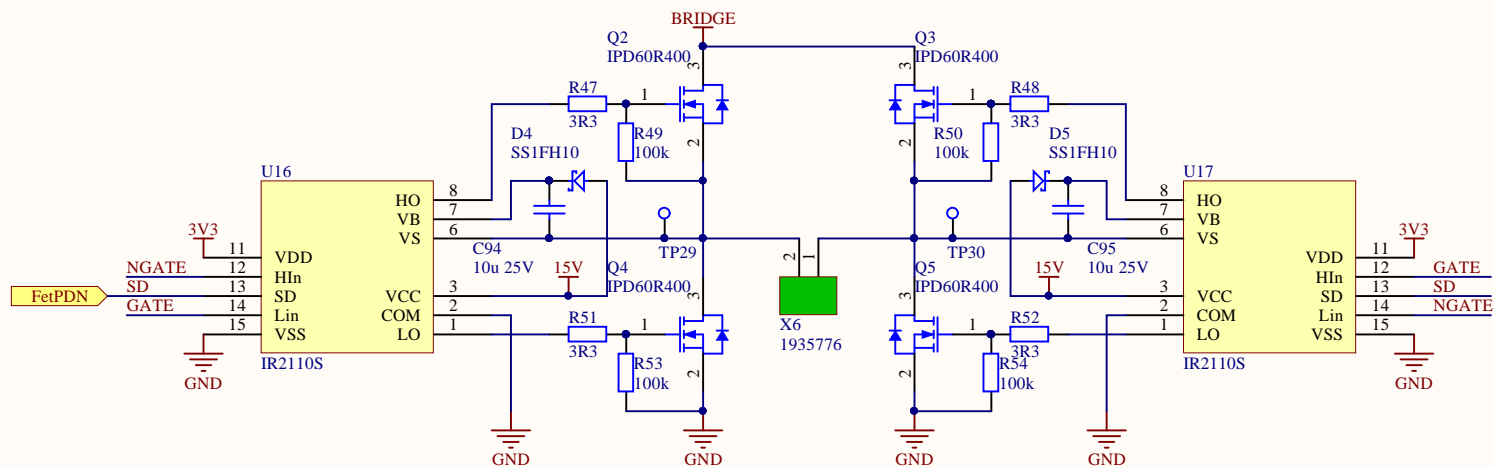
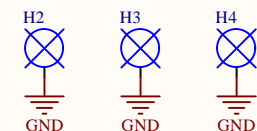
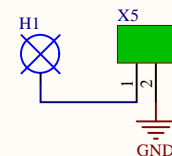
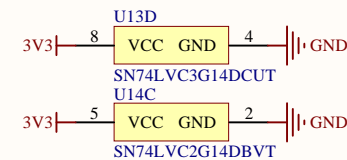
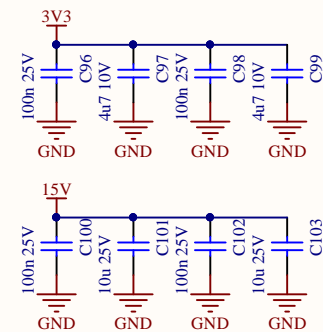
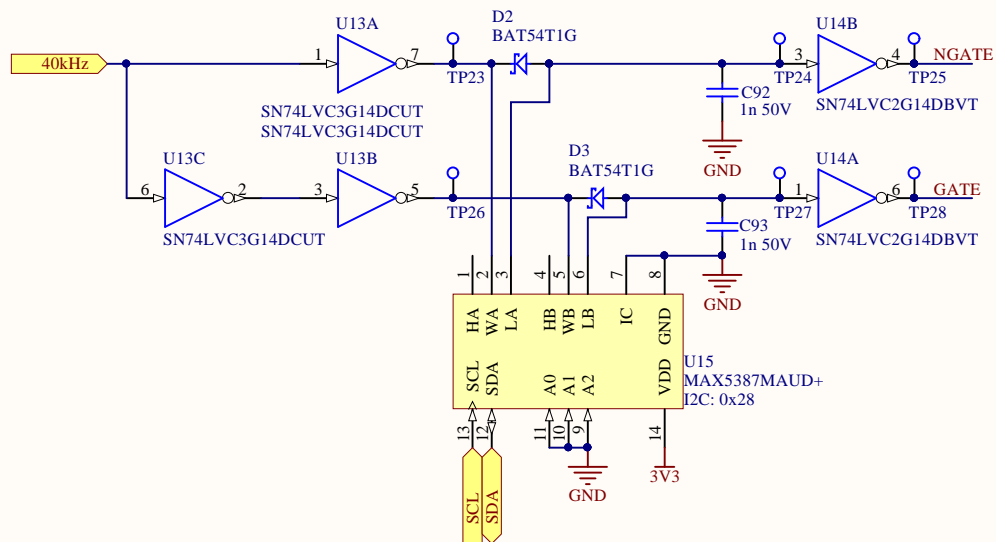
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Niklas Fauth

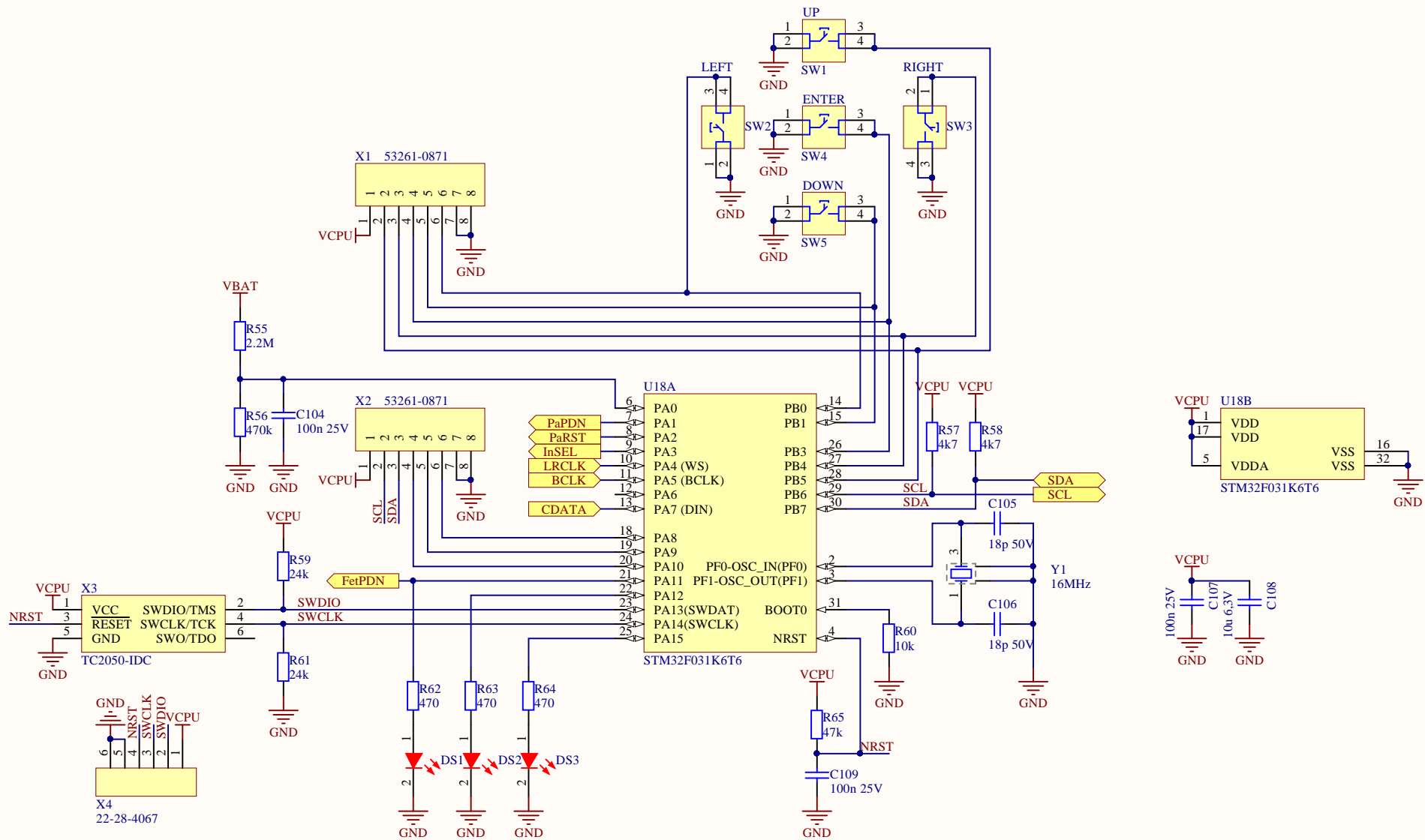
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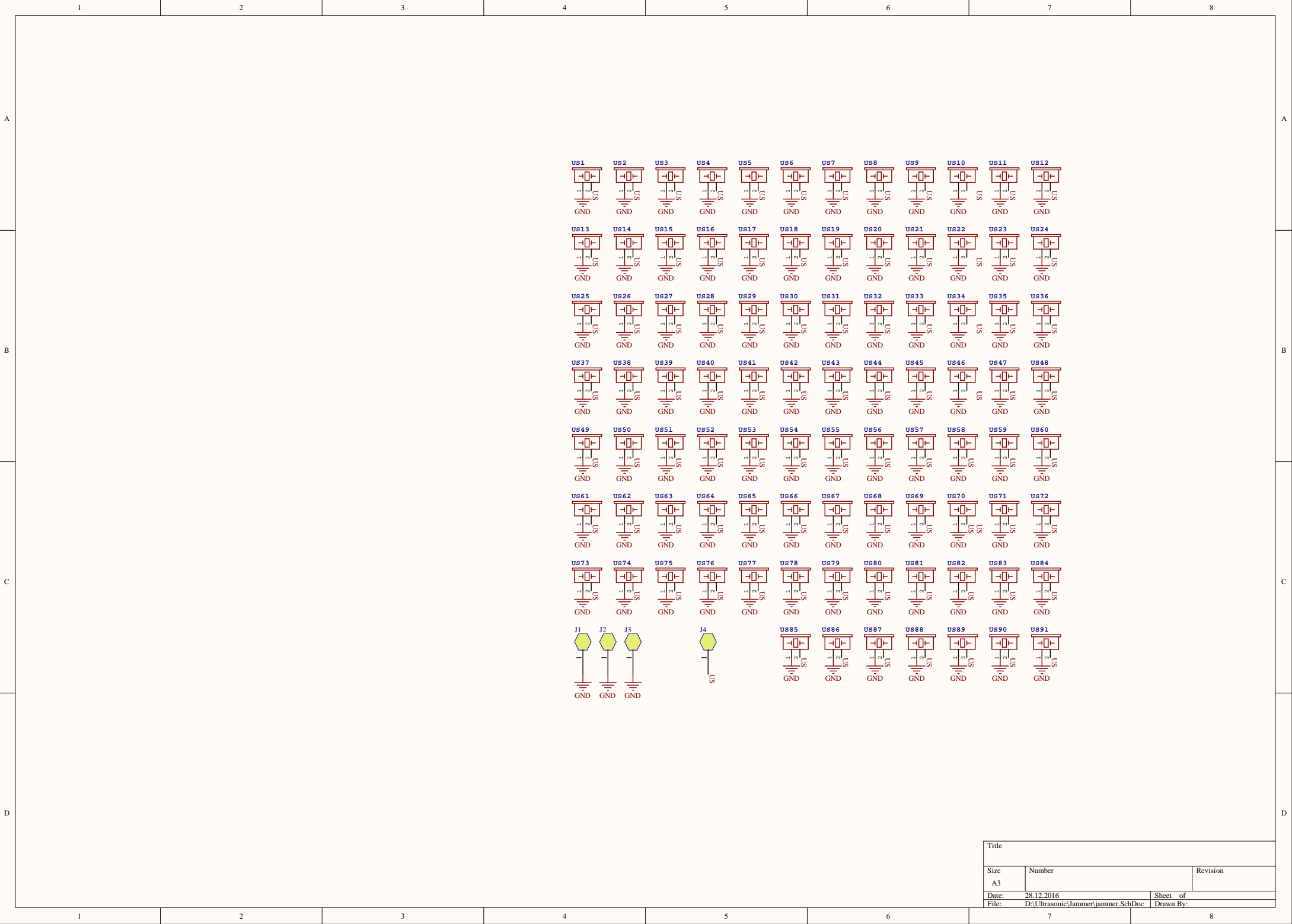
Altium



Title <i>H Bridge</i>			
Size: <i>A4</i>	Number:	Revision: <i>V1.0</i>	
Date: <i>28.12.2016</i>	Time: <i>01:35:33</i>	Sheet <i>4</i> of <i>5</i>	
File: <i>D:\Ultrasonic\Jammer\fet.SchDoc</i>			
			<i>Niklas Fauth</i>

Niklas Fauth





Title		
Size	Number	Revision
A3		
Date:	28.12.2016	Sheet of
File:	D:\Ultrasonic\Jammer\jammer.SchDoc	Drawn By:

Niklas Fauth 2016

Order Side

Micro Line

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