Security of BIOS/UEFI System Firmware from Attacker and Defender Perspectives

Miscellaneous Training Materials

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Derived from "Security of BIOS/UEFI System Firmware from Attacker and Defender Perspective" training by Yuriy Bulygin, Alex Bazhaniuk, Andrew Furtak and John Loucaides available at https://github.com/advanced-threat-research/firmware-security-training

MinnowMax Platform and EDKII

MinnowMax

Open hardware platform

Baytrail single or dual core

From http://firmware.intel.com/projects

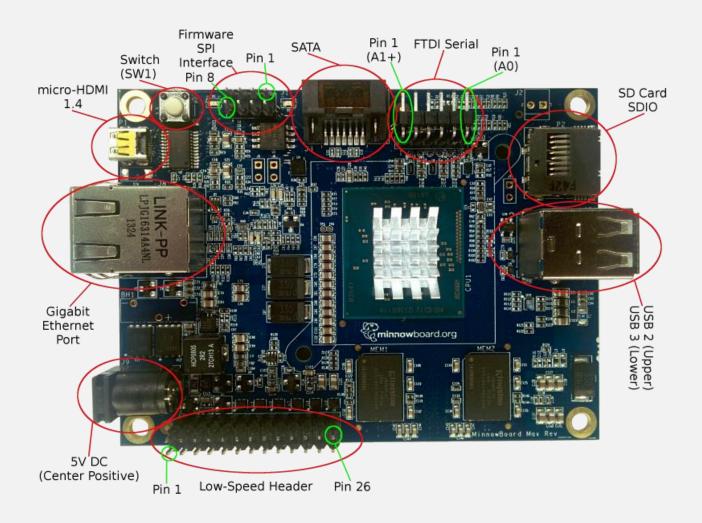


This project focus in on the firmware source code (and binary modules) required to create the boot firmware image for the MinnowBoard MAX. The UEFI Open Source (EDKII project) packages for MinnowBoard MAX are available at http://tianocore.sourceforge.net/wiki/EDK2. To learn more about getting involved in the UEFI EDKII project visit the How to Contribute page.

The source code builds using Microsoft Visual Studios and GNU C Compiler (for both 32 and 64 bit images) - production and debug execution environments. The source code builds the same UEFI firmware image shipping on MinnowBoard MAX.

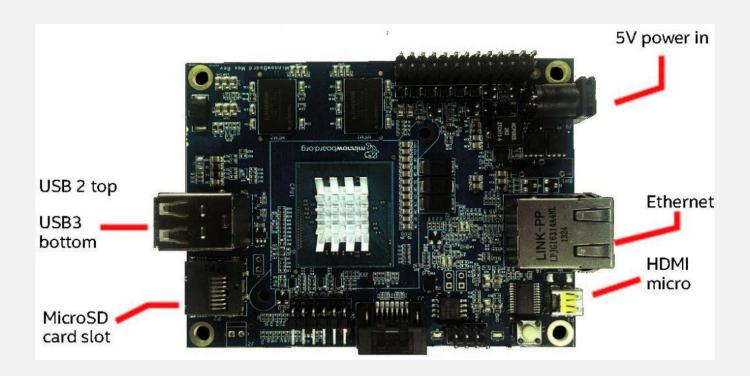
- See more at: http://firmware.intel.com/projects#sthash.1oOc8srY.dpuf

MinnowBoard Interfaces

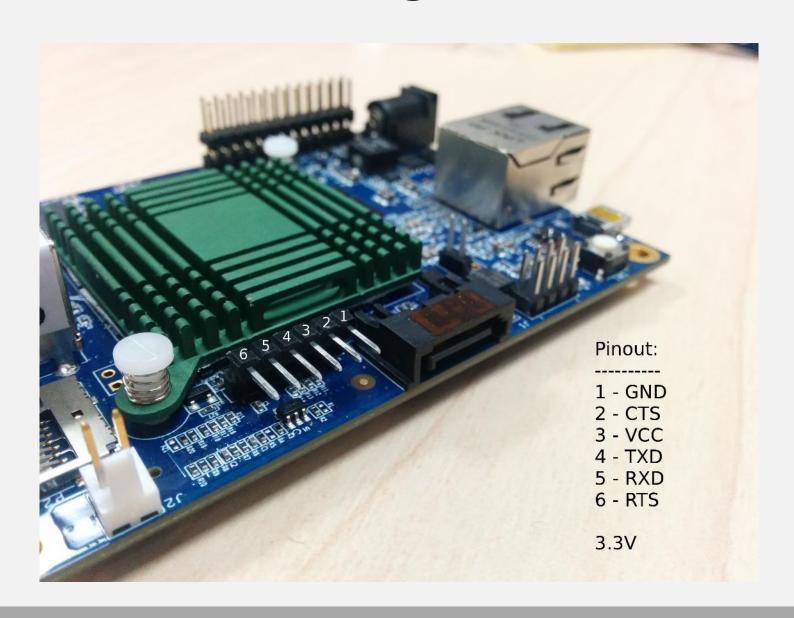


USB Ports in MinnowBoard

2 USB ports: USB3 on bottom and USB2 on top



UART Pinout Configuration



Connect to UART port



UART configuration

Baud rate - 115200

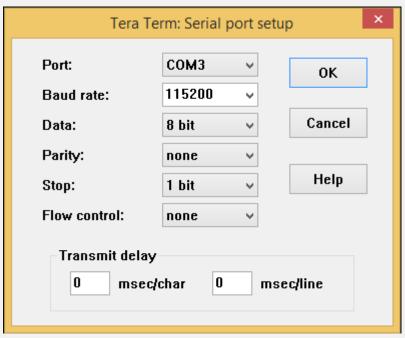
Flow control - 0

To read UART output

On Windows use: PUTTY or Tera Term

On Linux run minicom:

\$minicom -D /dev/ttyUSB0



Successfully launch Linux

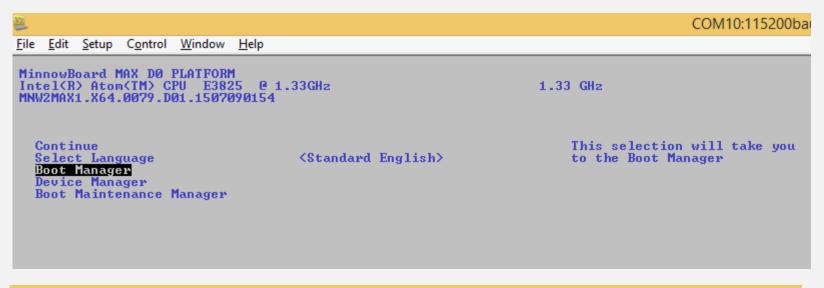
```
COM10:
File Edit Setup Control Window
                             Help
PhysicalStart= E00F8000
NumberOfPages= 1
Type= B
PhysicalStart= FED01000
NumberOfPages= 1
'vpe = B
EFI Memory Map End.
ConfigSccBootableDevicesAtExitBootService() Start
Jpdate Device 0 12 0
Devices 8A 2B 0 0 0 20 90831000 FFF 0 1000
Devices 8A 2B 0 0 0 20 90830000 FFF 0 1000
Jpdate Device 0 17 0
Devices 8A 2B 0 0 0 20 9082E000 FFF 0 1000
Devices 8A 2B 0 0 0 20 9082D000 FFF 0 1000
Switching SCC SD Host into ACPI Mode.
Switching eMMC Host into ACPI Mode.
ConfigSccBootableDevicesAtExitBootService() End
```

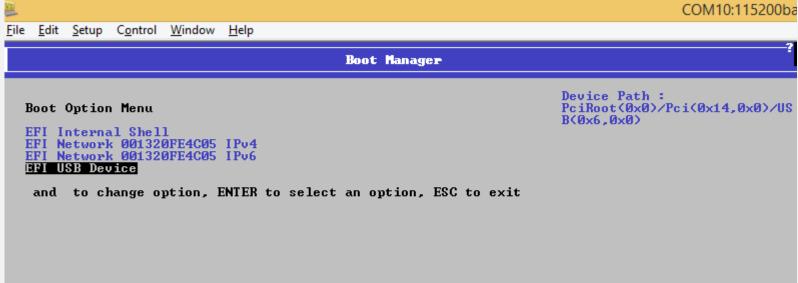
UEFI shell

For come to Setup Screen type exit & enter in the UEFI shell:

```
COM10:115200baud - Tera Term VT
   Edit Setup Control Window Help
EFI Shell version 2.40 [1.0]
Current running mode 1.1.2
 evice mapping table
fs0 :Removable HardDisk - Alias hd14a0h blk0
           PciRoot(0x0)/Pci(0x14,0x0)/USB(0x0,0x0)/HD(1,GPT,E41D19B7-EAD8-43D9-87F4-99344BF07A30,0x800,0x100000)
 hilde
          :Removable HardDisk - Alias hd14a0b fs0
            PciRoot(0x0)/Pci(0x14.0x0)/USB(0x0.0x0)/HD(1.GPT.E41D19B7-EAD8-43D9-87F4-99344BF07A30.0x800.0x100000)
          :Removable HardDisk - Alias (null)
PciRoot(0x0)/Pci(0x14,0x0)/USB(0x0,0x0)/HD(2,GPT,CCFA9FE2-F98C-4B86-BBCB-F2373759BF13,0x100800,0x182D000)
 hlkt
 h1k2
           :Removable HardDisk - Alias (null)
           PciRoot(0x0)/Pci(0x14,0x0)/USB(0x0,0x0)/HD(3,GPT,DC3527E6-751F-4FA2-8D64-DBA5182CB8EF,0x192D800,0x3CA000)
          Removable BlockDevice - Alias (null)
PciRoot(0x0)/Pci(0x14,0x0)/USB(0x0,0x0)
 h1k3
Press ESC in 5 seconds to skip startup.nsh, any other key to continue.
Shell> exit
```

Boot from USB Device





MinnowBoard Configuration

Students need to configure Ethernet card in laptops with 192.168.1.1/24 IP address

Access to MinnowBoard board through SSH (22 port).

Recommended clients: PUTTY, MobaXterm

MinnowBoard Network configuration:

IP address: 192.168.1.2

Gateway: 192.168.1.1

MinnowBoard File System

- ~/Desktop/bios
- ~/Desktop/chipsec
- ~/Desktop/image
- ~/Desktop/udk-debugger
- ~/Desktop/patches
- ~/Desktop/tools
- ~/Desktop/exercises

- MinnowBoard EDK2 FW sources
- CHIPSEC framework
- binary BIOS images
- udk-debugger installer & config
- BIOS patches
- misc useful BIOS/UEFI utilities
- materials for exercises

Useful UEFI Setup Options

Changing Boot Order On MinnowMax



Training materials are available on Github https://github.com/advanced-threat-research/firmware-security-training

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