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# 高通协议技术期刊 – 2015/12/16

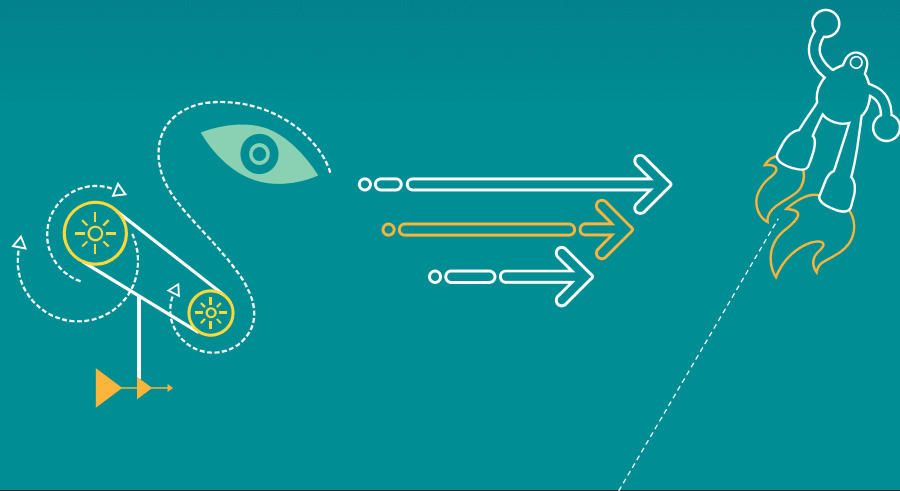
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Qualcomm Technologies, Inc.

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# Revision History

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Revision	Date	Description
A	Dec 2015	Initial release

**Note:** There is no Rev. I, O, Q, S, X, or Z per Mil. standards.

# Contents

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- ❑ MDM9x07 SAW design
- ❑ RF document update
- ❑ RJIL feature / QC compliance
- ❑ ODM device support model for RJIL
- ❑ RJIL Pre-Cert – Execution Process
- ❑ DC-HSUPA document 80-NK352-1 and 80-P3514-1

# MDM9x07 SAW design

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- OEMs shall use only SAW designs. GSM and TDS SAW less design support is non-PORed.
- 80-P1511-700 (AB) MDM9X07/MDM8207/MDM9628 CHIPSET ANNOUNCEMENT is released for schematic changes in MDM9x07 based audio supported designs.

# RF document update

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- **MSM8937**
  - 80-P2485-3\_B\_MSM89x7\_RF\_Software\_Overview.pdf
  - 80-NT112-100 [OMPREENHENSIVE MSM8909 FAMILY RF NV ITEMS](#)
- **MDM9x07**
  - 80-P2200-12 [PRESENTATION: MDM9X07 RF SOFTWARE OVERVIEW](#)
- **MSM8952**
  - 0-NV610-12
  - [MSM8X52 RF SOFTWARE OVERVIEW](#)
  - VD80-NV610-12C
  - [MSM8X52 RF SOFTWARE OVERVIEW VIDEO/CHINESE](#)

# RJIL feature / QC compliance

- RJIL operator includes features like IMS/VoLTE, VT, VoWiFi, eMBMS
- Current QC chipsets compliant to RJIL specification (V3.0)
  - 8916, 8939, 8909, 8992, 8994
  - Currently QCOM software support Reliance spec version 3.5
  - Scoping of new Reliance spec v4.0 is ongoing
- **Recommended list of CPLs for RJIL commercialization**

Chipset	SP	Latest Mainline release
8916 / 8939 (WTR1605)	LA.1.0.2.1	M8939AAAAANLYD102120.1
8916 / 8939 (WTR4905)	LA.2.1	M8939AAAAANLYD2149.1
8909	LA.1.1.C7	MSM8909.LA.1.1.c7-011337114-STD.PROD-2
8909	LA.1.2	MSM8909.LA.1.2-01234-ST.PROD-2
8994	LA1.3	M8994AAAAANLYD1359.1

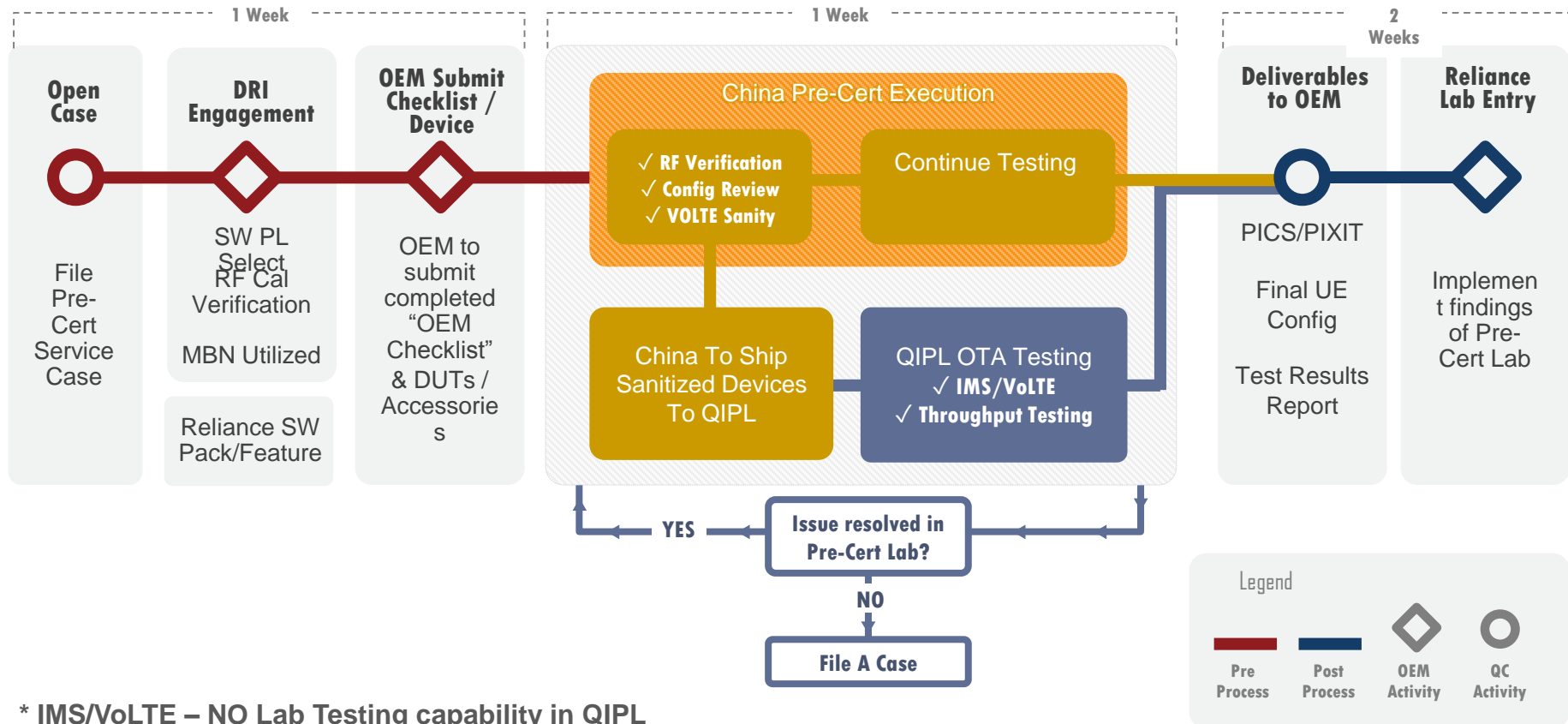
# ODM device support model for RJIL

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- ⑩ QCOM recommend OEMs to go through RJIL device pre-certification process conducted at Beijing pre-cert lab and India field test before entering RJIL certification lab. Request to contact your TAM for this.
- ⑩ Recommend to go over following docs once you decide to start a RJIL design :
  - ⑩ RJIL Guidelines preparation note for ODMs (80-P4064-1)
  - ⑩ RJIL Customization document for ODMs (80-P3336-1)
- ⑩ RJIL **M**odem **BiN**ary file (MBN) with complete RJIL specific NV configuration available part of build



# RJIL Pre-Cert – Execution Process



\* IMS/VoLTE – NO Lab Testing capability in QIPL  
 \* IMS/VoLTE testing in QIPL will be using RJIL live drops

# DC-HSUPA document 80-NK352-1 and 80-P3514-1

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- DC-HSUPA is a WCDMA feature that increases the maximum UL data rate from 5.76Mbps to 11.5Mbps. Details of this feature can be found in 80-NK352-1.
- Starting from TA.1.0 and TH.2.0, this feature is enabled by default in the code.
- This feature has been well-tested with various different infra vendors in both the lab and the field and have gone through rigorous testing.
- However, the customers do need to ensure that the device meets all of the RF compliance requirements, as described in **80-P3514-1**.
- For all OEMs planning on commercializing with DC-HSUPA, please make sure that you are aware of this document.

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## Questions?

<https://support.cdmatech.com>

