高通8996功耗温升优化技术期刊-20160315

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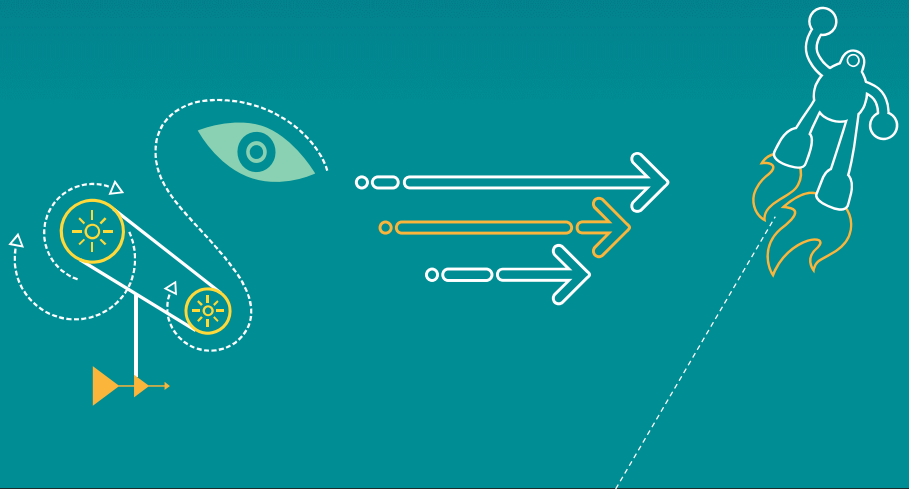

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Revision History

Revision	Date	Description
A	Jan 7, 2016	Initial version
B	Feb 5, 2016	Add more CRs
C	March 15, 2016	Add more CRs

Checklist

- Generic Power Debug Document
- Generic Power Measurement Checklist
- Generic Power Debug Tips
- MSM8996 Power Document
- MSM8996 CR/Patches Checklist
- MSM8996 Tips

Generic Power Debug Document

Document # (文档编号)	Document Description (文档描述)
80-P0955-1SC (中文版) 80-P0955-1 (英文版)	Power Debug Guide with Simplified Chinese 很详细的功耗debug中文手册，里面有各种case debug的步骤，以及如何来抓取各种log。功耗优化的必读宝典
80-NA157-246	LA Power Optimization 很详细的调试功耗如何抓log，用工具的文档。有各种场景如何抓log的介绍
80-NT616-1	Multimedia Power Debugging Case 有各种多媒体case的功耗调试手段介绍
80-NP961-1	Camera Power Debug Guide Camera功耗调试手册
80-NT384-1	PerfLock API Overview PerfLock接口的介绍
80-P1818-1EC	QUALCOMM ONDEVICE POWER DASHBOARD TEST GUIDE 客户机功耗测试指导手册
80-NT614-1	Android_Power_Basics_Power_Feature_Overview
80-P0956-1	Android_Power_Overview
80-P0958-1	Master_Reference_Document_Power

Generic Power Measurement Checklist

- 在测试功耗之前，请检查下面的这些基本项
- Use perf_defconfig instead defconfig
  - Remove “Debug” features
  - Double check if “CORESIGHT” config is removed
  - Double check if “CONFIG_MSM_DEBUG_LAR_UNLOCK” config is removed
- Disable UART console
  - Especially for smart panel
- Remove unnecessary debug log with high frequency
- Remove on-device logging tools

Generic Power Debug Tips --- How To Check RBSC Power

- Use IR camera to scan board's hot spot if RBSC current is high ;

  当RBSC高的时候可以使用IR Camera看看热点在哪里，这样可以确定是哪个硬件模块没有进入休眠。这个对某个大模块没有进入休眠很有帮助。比如Audio Codec

- Check if some clocks prevent system from going to VDD_MIN from AP side

  运行下面的命令，然后从kernel的dmesg中能看到AP休眠的时候，还有那些clock是enabled的。例如最常见NFC配置错误的时候，bb_clk2_pin在suspend之前没有被disable，导致系统进入不了VDD_MIN

  adb shell "echo 1 > /sys/kernel/debug/clk/debug_suspend

- Hardware break down

  硬件break down对于调试RBSC非常有用。通过焊掉不同器件能够知道到底是哪个器件有漏电。比如拔屏，焊掉NFC模块，Audio Codec模块，各种sensors等等。

- Compare HW difference between OEM device and Qualcomm reference board

  比如LCD, Touch Screen, Finger Print, NFC, Audio Codec, Sensors。最经常有问题的比如NFC, Finger Print.

- 对于有Finger Print的设备，如果Finger Print驱动request了CXO,那么系统就不能进入VDD_MIN. 这样RBSC就会高些

- Dump only clock via JTAG

  如果不希望通过JTAG传递太多数据，可以只dump clock，这样可以看出哪个clock阻止系统进入休眠。

- For more details, pls refer to doc 80-P0955-1SC, chapter 3.1.

  更多细节，请参考文档80-P0955-1SC的"3.1 底电流"

Generic Power Debug Tips --- How To Check MP3 Power

- Identify the MP3 playback mode

  Compress offload /Tunnel mode: Decoding on ADSP，下面是命令

  adb shell setprop audio.offload.disable 0

  Non-Offload/Nontunnel mode: Decoding on CPU，下面是命令

  adb shell setprop audio.offload.disable 1

- Check if there is 3rd party sound effect lib such as Dolby

  如果有第三方的音效处理算法，系统功耗肯定会比高通参考数据高。为了方便比较，可以先disable第三方算法，然后和参考平台进行比较

- Check is there any Hi-Fi function enabled

  可以重点关注Hi-Fi PA的功耗情况。同时看看有没有Hi-Fi bypass mode用来方便做对比测试

- For more details, pls refer to doc 80-P0955-1SC, chapter 3.4.

  更多细节，请参考文档80-P0955-1SC的“3.4 MP3播放 ”

Generic Power Debug Tips --- How To Check Static Display Power

- Check Static Display with Android Native UI

  这样可以排除由于产品本身的UI引起的功耗增加。Native UI case功耗正常以后再切换到产品定制UI上来。很多时候我们可以根据波形来进行对比。比如看看定制化UI的功耗波形是否有周期性的peak之类。

- Check if Touch Screen consumes more power

  当Touch Screen产生了更多中断的时候，Static Display这种case会有更好的功耗。Touch Screen本身firmware是否有优化空间也是考虑之一。需要和Touch Screen厂家沟通以确认。

- Check if auto backlight adjustment feature is enabled

  为了能更好对比功耗数据，做测试的时候需要disable自动背光调整功能

- For more details, pls refer to doc 80-P0955-1SC, chapter 3.3.

  更多细节，请参考文档80-P0955-1SC的“3.3 静态显示 ”

Generic Power Debug Tips --- How To Check Camera Power

- Disable all unnecessary logs

  Camera应用场景的时候因为log过多会导致系统功耗上升很多。要减少不必要log输出，或者直接disable LogD。

- Balance Performance and Power

  比如对于fps,可以低于30fps以取得更低功耗。需要找Camera Vendor提供更低帧率的设置

  对于sensor output，可以采用最低的sensor output resolution来满足实际场景需要，比如1080P video record的时候sensor就不用输出Full Size，而是最接近1080P的sensor output.

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- Get basic power data with all features disabled, such as OIS, ASD

  第一步先disable各种feature来得到一个最低功耗。因为各种feature功耗多少是可以采用叠加方式的。把最简单的case调好了后面就好调了。调试简单case的时候最好避免其他因素的干扰。

- For more details, pls refer to doc 80-P0955-1SC, chapter 4.17, 4.18 and 4.19.

  更多细节，请参考文档80-P0955-1SC的4.17 摄像头预览调试, 4.18 摄像头功率优化技术 4.19 视频录制功率　优化技术 ”

Generic Power Debug Tips --- How To Check Trex Power

- Disable thermal engine before power data collection

- Optimize Display Porch Values

  这样可以优化功耗

- Get ftrace log for analysis

  可以重点focus在DDR和CPU的频率和利用率上

Generic Power Debug Tips --- DoU (Days of Usage)

- Brightness should be adjust to a reasonable value when do apple to apple comparing with the target HW;
  不同硬件的同一亮度实际的背光功耗也可能有区别，这部分在做DoU比较的时候要弄清楚

- Thermal facts should be taken into account
  不同的thermal config会对DoU结果有影响

MSM8996 Power Document

Document # (文档编号)	Document Description (文档描述)
80-NT204-7	MSM8996_LA_Current_Consumption_Data 包括各种应用场景下的功耗breakdown数据
80-P0202-10	Power Data for MSM8996.LA.1.0 Release 01600

MSM8996 Checklist --- CR/Patches

N means No Impact - means Side Effect + means Good Impact

CR	Fix	Patches(Internal)	Perf	Power	Stability	Comment
CR941069	Fix "CPU DCVS governor wrongly changed into performance mode	https://review-android.quicinc.com/#/c/1462552/2	N	+	N	
CR869281	On Isatri we are observing that DIAG_WS wake_lock in not being released when we remove USB.	https://review-android.quicinc.com/#/c/1355991/	N	+	N	
CR948872	15ms peaks on static display	https://review-android.quicinc.com/#/c/1483407/	N	+	N	
NA	CPU floor frequency settings	https://review-android.quicinc.com/#/c/1356625/9	N	N	N	
N/A	Fps low for RR3 or other games	*AU_LINUX_ANDROID_LA.HB.1.1.1_RB4.06.00.00.176.020*	+	N	N	
CR946561(C)	LOGD verbose removal for VoLTE	https://review-android.quicinc.com/#/c/1476833/ https://review-android.quicinc.com/#/c/1476832/ https://review-android.quicinc.com/#/c/1484835/	N	+	N	

MSM8996 Checklist --- CR/Patches

N means No Impact - means Side Effect + means Good Impact

CR	Fix	Patches	Perf	Power	Stability	Comment
*CR*933416(C)	Camera capture XO shutdown block	https://review-android.quicinc.com/#/c/1453869/	N	+	N	
CR961377	IPA_WS will be hold for a long time , more than 60min	https://www.codeaurora.org/cgit/quic/la/kernel/msm-3.18/commit/?id=7609c58699585154c40eff145f35e872e82baba6	N	+	N	
CR916022	Fix to system suspend due to missing wakelock	https://www.codeaurora.org/cgit/quic/la/kernel/msm-3.18/commit/?id=94627c9a9a0e474a42c305727848630a66437f65	N	N	+	
CR 957221	The MODEM will not go to sleep when connection with remote SIM is established.	uimdrv.c uimdrv_hal_iso.c Will be changed	N	+	N	

MSM8996 Checklist --- CR/Patches (NEW)

N means No Impact - means Side Effect + means Good Impact

CR	Fix	Patches	Perf	Power	Stability	Comment
CR 977820	*Unexpected tsens min/max violations in low temperature testing*	*Disable MIN and MAX threshold for sensor0(both controllers) in* *XBL code* *QcomPkg/Msm8996Pkg/Library/DALConfigLib/TsensConfig/TsensBsp.c*	N	N	+	*the root cause seems to be an HW issue happens on sensor0 ADC conversion and only happens when exit from VDDmin.* *so only SW WA provided as below.*
CR 971875	If debug fuses are blown, qdss_fuse_trace_access() ends up leaving the vote to "/clk/qdss" enabled. This has a power impact on production devices, since fuse check is done on boot up.	/adsp_proc/core/debugtrace/common/src/qdss_fuse_check_dal.c /modem_proc/core/debugtrace/common/src/qdss_fuse_check_dal.c	N	+	N	All MP devices should include this fix
			N	N	+	
			N	+	N	
			N	+	N	

MSM8996 Tips (1)

- RAM dump can be collected by connneting PS_HOLD to GND less than 80ms when VDDmin with SW version later than Meta MSM8996.LA.1.0-01716-STD.INT-1

MSM8996 Tips (2)

- Check HW design to make sure the following block is correct
  The original design may lead 3-10mA leakage on RBSC
  Refer to 80-NT204-41 **MSM8996 Baseband** Reference Schematic and 80-NT204-5B
  If HW can`t be changed as above, you may implement SW workaround like this.

SW WA1- "Modify retention settings

- The change is one-bit change in RPM:
- rpm_proc\core\power\mpm\hal\bsp\source\<target>\BSPmpm.c
- BSP_mpm_ConfigDataType MPM_BSP_DATA =
- {
- ...
- Original code:
- /* IO Cfg */
- {
- /* Freeze Clamp SW Ebi1 Warm Boot Warm Boot VREF_PWRSAVE
- * IOs IOs Ctl Enable Freeze EBI1 Freeze EBI2 */
- TRUE, TRUE, TRUE, TRUE, TRUE, TRUE
- },

- Change to:
- /* IO Cfg */
- {
- /* Freeze Clamp SW Ebi1 Warm Boot Warm Boot VREF_PWRSAVE
- * IOs IOs Ctl Enable Freeze EBI1 Freeze EBI2 */
- FALSE, TRUE, TRUE, TRUE, TRUE, TRUE
- \