Packers

(5th April 2010)

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Changelog

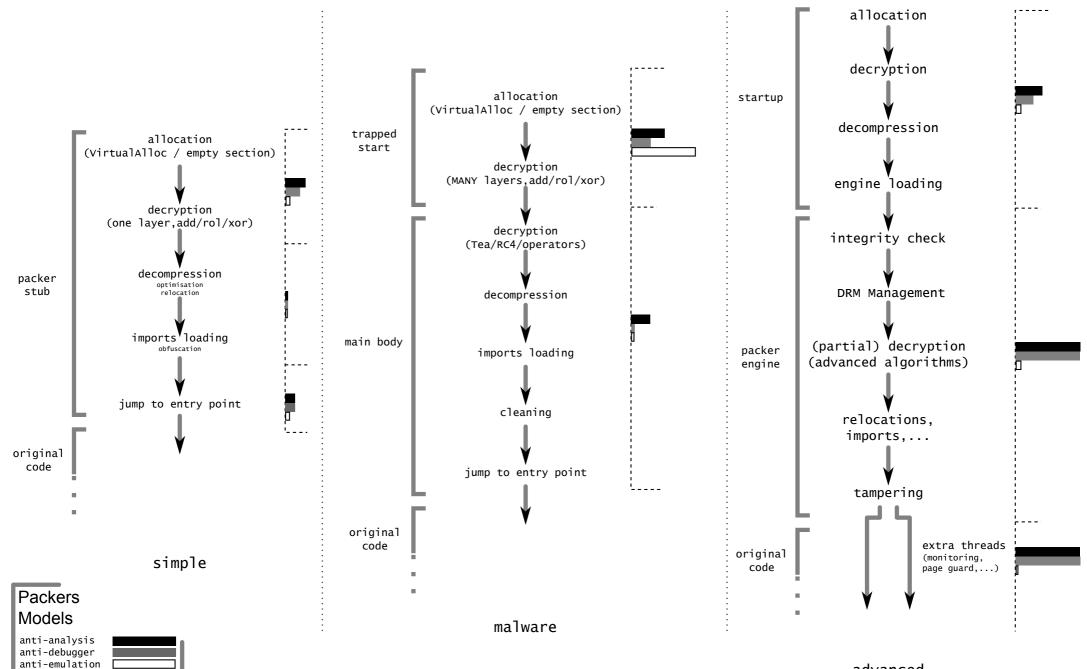
2010/04/05 +algorithms

2010/04/04 + models

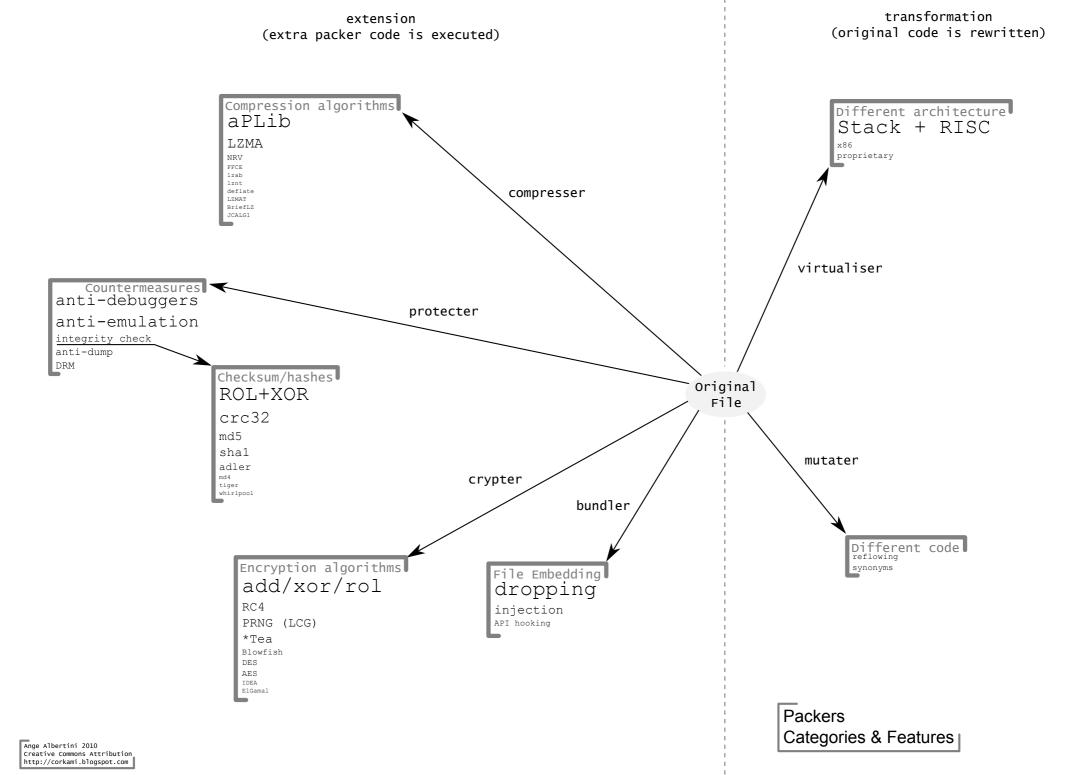
2010/03/29 +entrypoints

2010/03/24 +categories and features, detailed features

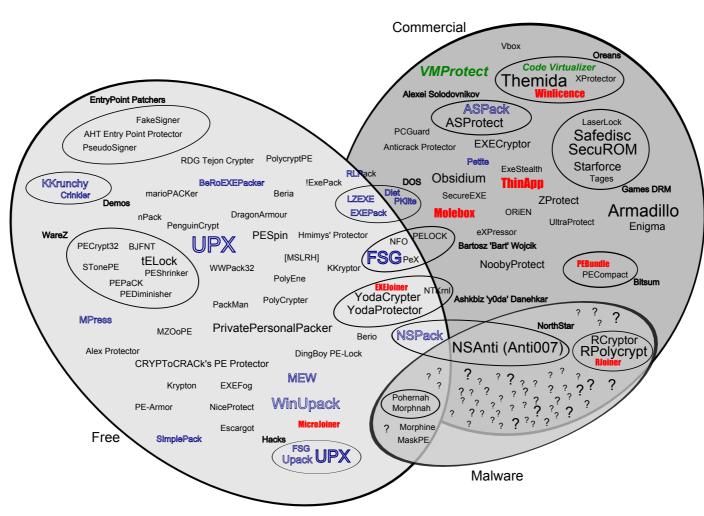
2010/02/23 +landscape (first graphic)



advanced



Packers
Landscape
Bundlers
Virtualisers
Compressors



	-
compression	(used on top of compression algorithms)
section merging	merge all sections (just one entry in the section table)
imports	imports are stored and loaded with a more compact import table format
imports by hash	exports are parsed until it matches a specific hash, instead of a $GetProcAddress$ call
call optimisation	turn relative operands of jumps and calls into absolute \rightarrow better compression
resources	compresses resources, avoiding critical ones (main icon, manifest,)
protection	
token check	presence check to allow the program to run: dongle, CD/DVD, key, file, network
fingerprinting	token is specific to a hardware element: disk/OS/CPU/MAC/
demo mode	inclusion of a demo binary/mode that is executed when token is absent or not enough privileged check the contents are unmodified with checksum or hash
integrity	check the contents are unmodified with checksum of hash
anti-analysis	
overlap	jumping after the first byte of an instruction
illusion junk	makes the analyst the something incorrect happened insertion of dummy code between relevant opcodes
jumps	insertion of dummy code between relevant opcodes insertion of jumps to makes analysis visually harder
polymorphism	different but equivalent code \rightarrow 2 packed files of the same source are different
self generation	packer stub generates polymorphic code on the fly \rightarrow same file executes differently
virtualization	virtualizes (part of) packer stub code \rightarrow harder analysis
stack	strings are built and decrypted before use, then discarded \rightarrow to avoid obvious references
faking	add fake code similar to known packers to fool identification
thread	use several parallel threads to make analysis harder
timing	comparing time between two points to detect unusual execution
anti-debugging	(and anti-tools, by extension)
detect	detect the presence of an attached debugger: IsDebuggerPresent
prevent	prevent a debugger to attach to the target itself or stay attached make debugger session difficult: BlockInput, slow down
nuisance thread	spawn a monitoring thread to detect tampering, breakpoints,
artifacts	detects a debugger by its artifact: window title, device driver, exports,
limitation	prevent the use of a tool via a specific limitation
exploit	prevent the use of a tool via a specific vulnerability
backdoor	detect or crash a debugger via a specific backdoor
self-debugging	debug itself to prevent another debugger to be attached
int1	block interruption $1 \to \text{debuggers stop working}$
fake	add code of known packer to fool identification
anti-dumping	(prevent making a working executable from a memory image)
tampering	erase or corrupt specific file parts to prevent rebuilding (header, packer stub,)
imports	add obfuscation between imports calls and APIs (obfuscation, virtualization, stealing,) API address is resolved before each use to prevent complete dumping
on the fly API hooking	alter API behavior: redirect benign API to a critical one → dump not working
inlining	copy locally the whole content of API code → no more 'import calls'
relocate	relocate API code in separate buffer \rightarrow calls don't lead to imported DLLs
byte stealing	move the first bytes of the original code elsewhere \rightarrow harder rebuilding and bypasses breakpoints
page guard	blocks of code are encrypted individually, and decrypted temporarily only upon execution
flow	flow opcodes are removed and emulated (or decrypted) by the packer during execution \rightarrow incorrect dump
virtualization	virtualizes (part of) original code, API start \rightarrow dump not working without VM code
anti-emulation	
opcodes	using different opcodes sets (FPU, MMX, SSE) to block emulators
undoc	use of rare or undocumented opcodes to block non-exhaustive emulators
API	unusual APIs are called to block non-exhaustive emulators (anti-virus)
loop	extra loops are added to make time-constraint emulators give up
bundlers	
drop	original file is written to disk then executed
injection hooking	original file is injected in existing process \rightarrow no new file on disk + higher privileges file handling APIs are modified to make embedded files usable like external ones
пооктид	me nandring AI is are incumed to make embedded mes usable like external ones

```
PECOMPACT
```

```
EntryPoint:
   mov eax, _1
   push eax
   push dword ptr fs:[0]
   mov fs:[0], esp
         eax. eax
    xor
   mov
        [eax], ecx
[...]
_1:
    mov
         eax, <random1>
         ecx, [eax + <random2>]
    lea
         [ecx + 1], eax
    mov
         edx, [esp + 4]
   mov
         edx, [edx + c]
   mov
         byte ptr [edx], 0e9
    add
         edx, 5
    sub
         ecx, edx
         [edx - 4], ecx
   mov
         eax, eax
    xor
    retn
   mov eax, 12345678
        dword ptr fs:[0]
   pop
    add esp, 4
    push ebp
    push ebx
```

```
MEW
```

```
_1:
    mov
        esi, <address>
        ebx, esi
   mov
    lodsd
    lodsd
   push eax
    lodsd
    xchq eax, edi
    mov dl, 80
_2:
   movsb
   mov dh, 80
    call [ebx]
    jnb 2
1...1
EntryPoint:
    jmp _1
```

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```
EntryPoint:
```

```
xchg [ 1], esp
    popad
    xchg eax, esp
    push ebp
_1:
    movsb
          dh, 80
    mov
    call
          [ebx]
    jnb
           1
    xor
          ecx. ecx
    call [ebx]
```

FSG

UPX (LZMA)

```
EntryPoint:
```

```
pushad
    mov
           esi, <address>
           edi, [esi + <negative>]
    1ea
    push
           edi
    mov
           ebp, esp
           ebx, [esp - 3E80]
    lea
    xor
           eax, eax
_1:
    push
          eax
    cmp
           esp. ebx
    jnz
           1
    inc
           esi
    inc
           esi
    push
          ebx
          0C478
    push
    push
           edi
    add
           ebx, 4
```

```
push
      ebx
push
      534E
push
      esi
add
      ebx, 4
      ebx
push
push
      dword ptr [ebx], 20003
mov
nop
nop
nop
nop
nop
push
      ebp
push
push
      esi
push
      ebx
sub
```

esp. 7C

edx, [esp + 90]

```
UPX
```

```
EntryPoint:
    pushad
            esi, <address>
    mov
            edi, [esi + <negative>]
    lea
    push
            ebp, fffffffff; * Not in UPX >3
    or
            $ + 12
    dmir
    nop
    nop
    nop
    nop
    nop
    nop
    mov
            al, [esi]
            esi
    inc
            [edi], al
```

```
ASPack
```

```
EntryPoint:
    pusha
    call
db 0E9h
              ; E9 EB045D45 CALL ...
    jmp
          _2
1:
          ebp
    pop
                          Packers
    inc
          ebp
                          EntryPoints
    push
          ebp
    retn
_2:
    call
db 0EBh
              ; EB54 JMP <garbage>
3:
          ebp
    pop
```

```
Upack
```

```
EntryPoint:
   mov esi, <address>
    lodsd
    push eax
    push dword ptr [esi+34]
    jmp short _1
[...]
_1:
    push dword ptr [esi+38]
    lodsd
    push eax
    mov edi, [esi]
    mov esi, <address2>
```

APLIB

```
start:
     start:
                                                        push ebp
         pushad
                                                        mov ebp, esp
         mov esi, [esp + 24]
                                                        add esp, -54
         mov edi, [esp + 28]
                                                        push ebx
         cld
                                                        push esi
         mov dl, 80
                                                        push edi
         xor ebx, ebx
                                                        mov [ebp - c], ecx
  . → copy literal:
                                                    [...]
         movsb
         mov bl, 2
                                                    $+84:
 >next:
                                                        add ecx, [ebp - 34]
       -call getbit
                                                        mov eax, 300
                                                        shl eax, cl
                                                        add eax, 736
         jnb short copy literal
                                                        dec eax
         xor ecx, ecx
                                                         test eax, eax
       -call getbit
                                                         jb no init
                                                         inc eax
     [...]
                                                        mov [ebp - 2c], 0
         sub esi, eax
                                                    init buffer:
         rep movsb
                                                        mov edx, [ebp - 10]
         pop esi
                                                        mov ecx, [ebp - 2c]
         jmp next
                                                        mov [edx + ecx * 4], 400
                                                        inc [ebp - 2c]
   →aetbit:
                                                        dec eax
         add dl, dl
                                                         jnz init buffer
        jnz skip
                                                    no init:
         mov dl, [esi]
         inc esi
                                                    [...]
         adc dl, dl
                                                        mov al, 1
   →skip:
                                                        pop edi
         retn
                                                        pop esi
     [...]
                                                        pop ebx
                                                        mov esp, ebp
    end:
                                                        pop ebp
         sub edi, [esp + 28]
                                                        retn 10
                                         CRC32
         mov [esp + 1c], edi
         popad
                                 crcloop:
         retn Oc
                                     test eax, 1
                                     jz
                                          no xor
                                     shr eax, 1
                                     xor eax, 0EDB88320h
      Packers
                                     jmp
                                          loop
       Algorithms
                                 no xor:
                                     shr eax, 1
                                 loop:
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                                     loop crcloop
```