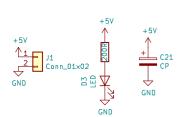
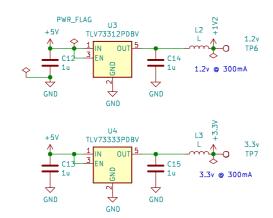
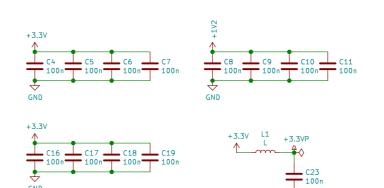
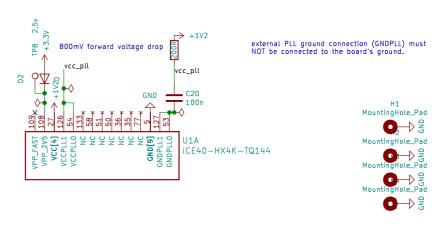
### Power Supply

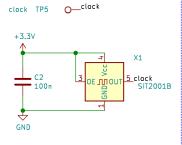




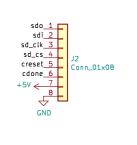




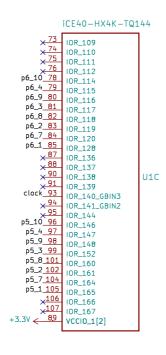
## Clock

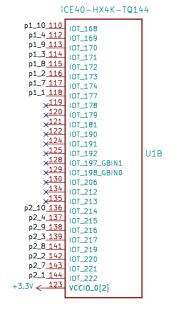


# Programming

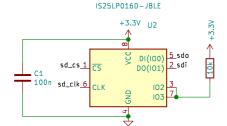


#### **FPGA**

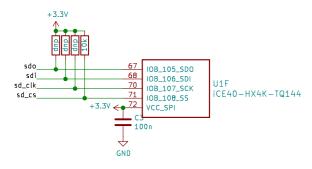




# **FLASH**

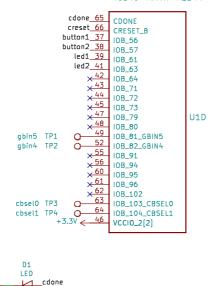


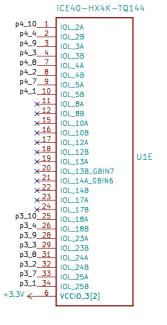
16Mb FLASH supports 0Bh fast read



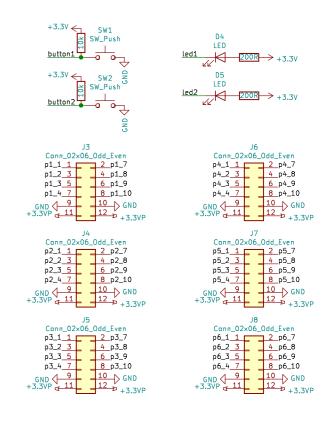
pullup on ss means configure as SPI master to read configuration from FLASH at boot

#### iCE40-HX4K-TQ144





## **Peripherals**



things to check: clock and reset gbin, will pll be ok? flash — pullups needed? check fpga pinout against lattice docs

