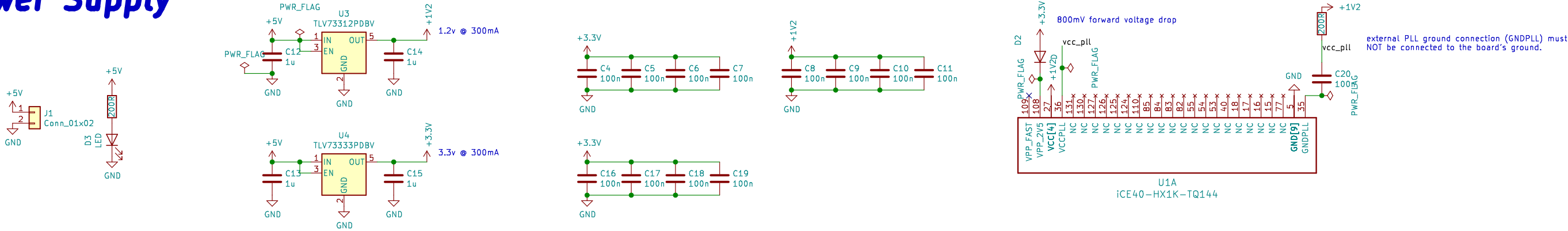
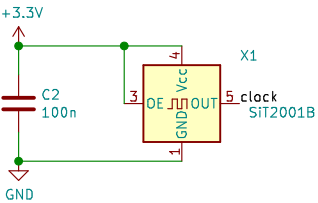


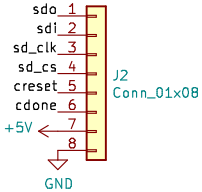
Power Supply



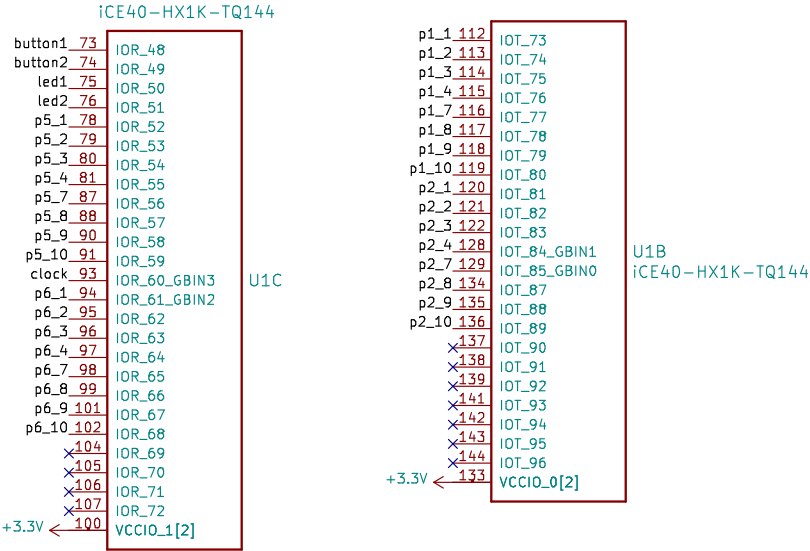
Clock



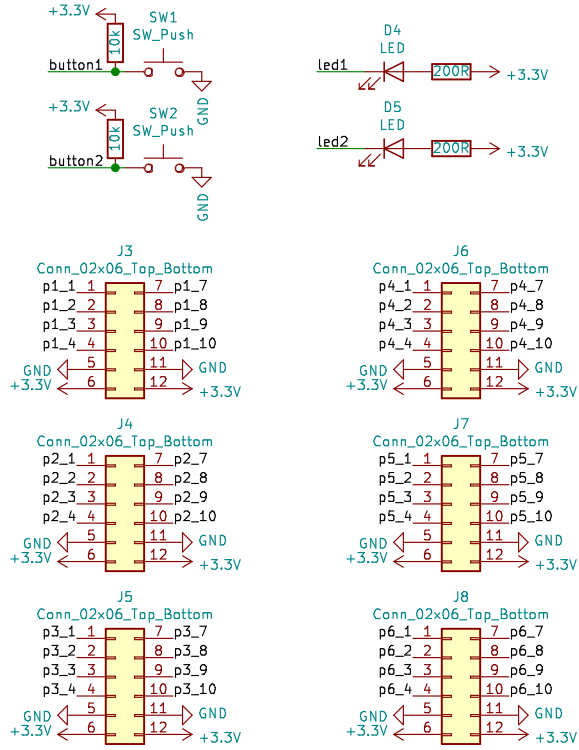
Programming



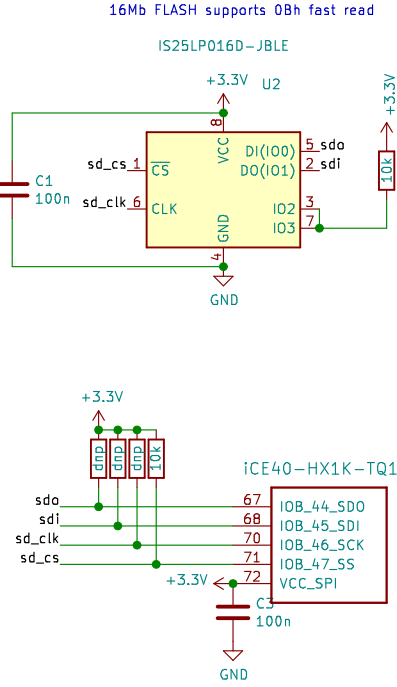
FPGA



Peripherals



FLASH



pullup on ss means configure as SPI master to read configuration from FLASH at boot

things to check:
clock and reset gbin, will pll be ok?
flash - pullups needed?
check fpga pinout against lattice docs