

[illegible]

external PLL ground connection (GNDPLL) must NOT be connected to the board's ground.

- things to check:
 - clock and reset gbin, will pll be ok?
 - flash – pullups needed?
 - check fpga pinout against lattice docs

Sheet: /		
File: first-fpga-pcb.sch		
Title: Matt-FPGA		
Size: A3	Date: 2019-12-30	Rev: 0.1
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