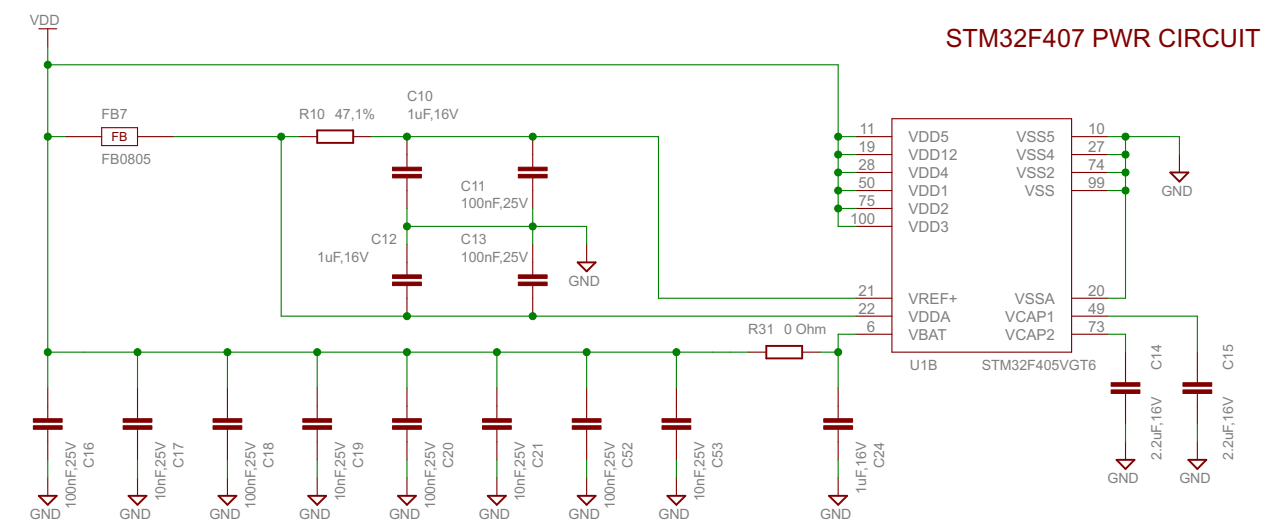


The schematic diagram illustrates the LED driver circuit for the QTLINK module. It features four parallel LED channels, each consisting of an LED, a resistor, and a diode connected to a common ground (GND).

- Channel 1 (LED1):** LED1 (LTST-S220KGKT, LED\_GREEN, RA) is connected in series with resistor R12 (680.1%) and diode Power.
- Channel 2 (LED2):** LED2 (LTST-S220KRKT, LED\_RED, RA) is connected in series with resistor R13 (680.1%) and diode PTT.
- Channel 3 (LED3):** LED3 (LTST-S220KGKT, LED\_GREEN, RA) is connected in series with resistor R15 (680.1%) and diode Sync.
- Channel 4 (LED4):** LED4 (LTST-S220KRKT, LED\_RED, RA) is connected in series with resistor R16 (680.1%) and diode Clip/Error.

The diodes (Power, PTT, Sync, Clip/Error) are connected to a common ground (GND). The LEDs are connected to the positive supply rails (LED-PWR, LED-PTT, LED-RT, LED-ERR).



VDD

P13 P14 P16 P18 P19 P20

GND GND GND

+5V +VIN

AIN1

AIN2

1nF 25V/0402

C55

GND

DAC1\_OUT

R55 0 Ohm

1nF 25V/0402

C56

GND

DAC2\_OUT

R54 0 Ohm

VBUS

OTG ID

OTG DM

OTG DP

SWDIO

SWCLK

NSS

BOOT1

USART3\_TX

USART3\_RX

OTG\_PWR\_ON

SCK

MISO

MOSI

STM32F405 CPU

U1A

STM32F405VGT6

PA0 PA1 PA2 PA3 PA4 PA5 PA6 PA7 PA8 PA9 PA10 PA11 PA12 PA13 PA14 PA15

PB0 PB1 PB2 PB3 PB4 PB5 PB6 PB7 PB8 PB9 PB10 PB11 PB12 PB13 PB14 PB15

PC0 PC1 PC2 PC3 PC4 PC5 PC6 PC7 PC8 PC9 PC10 PC11 PC12 PC13

PE0 PE1 PE2 PE3 PE4 PE5 PE6 PE7 PE8 PE9

PD0 PD1 PD2 PD3 PD4 PD5 PD6 PD7 PD8 PD9 PD10 PD11 PD12 PD13 PD14 PD15

PH0 PH1

PC14 PC15

NRST

BOOT0

97 PE0

98 PE1

1 PE2

2 PE3

3

4

5

38

39

40

41

42

43

44

45

46

81 SELECT

82 BACK

83

84

85

86 OTG\_OVR\_CUR

87

88 BOOT0

55 EXT\_PTT

56 TEST

57 CPPT

58

59 LED-PWR

60 LED-PTT

61 LED-RT

62 LED-ERR

12 OSC\_IN

13 OSC\_OUT

8

9

14 NRST

94 BOOT0

#7

SDA3

SCL3

The schematic diagram illustrates the electrical connection for the TS-026 module. A green line representing the SELECT signal line runs vertically. At the top, it is connected to VDD through a resistor labeled R7 (10K, 1%). Below the resistor, the line passes through a green dot. Further down, it passes through a switch symbol labeled SW2 TS-026. The switch is shown in a closed position, connecting the line to GND. The switch is controlled by a signal labeled SELECT, which is shown as a green line entering the switch from the right. The bottom of the switch is connected to GND through another green dot.

3.3V TTL Levels

CN11 HDR100-3

USART3 TX

USART3 RX

1

2

3

GND

The schematic diagram illustrates the internal circuitry of the EMIF02-USB03F2 module. It features two integrated circuits: a USB-to-serial converter (U3, STMP2141) and a USB-to-serial converter (U4, EMIF02-USB03F2). The module is powered by a +5V supply (VDD) and a ground (GND). The USB-to-serial converter (U3) is connected to the host's USB lines (VBUS, DM, DP, ID) and the module's serial lines (OTG\_PWR\_ON, OTG\_OVR\_CUR, OTG\_DM, OTG\_DP, OTG\_ID). The USB-to-serial converter (U4) is connected to the module's serial lines (VBUS, DM, DP, ID) and the module's USB lines (VBUS, DM, DP, ID). The module's serial lines are connected to the host's serial lines (VBUS, DM, DP, ID) and the module's USB lines (VBUS, DM, DP, ID). The module's USB lines are connected to the host's USB lines (VBUS, DM, DP, ID) and the module's serial lines (VBUS, DM, DP, ID). The module's serial lines are connected to the host's serial lines (VBUS, DM, DP, ID) and the module's USB lines (VBUS, DM, DP, ID). The module's USB lines are connected to the host's USB lines (VBUS, DM, DP, ID) and the module's serial lines (VBUS, DM, DP, ID).

## #7

The diagram shows the connection of a 24LC256-I/M5 EEPROM (U10) to an Arduino Uno. The chip's pins are connected as follows:

- Pins 1, 2, 3, and 4 are connected to GND.
- Pins 8 and 9 are connected to VCC.
- Pin 10 is connected to WP (Write Protect).
- Pin 11 is connected to SCL (Serial Clock Line).
- Pin 12 is connected to SDA (Serial Data Line).

The address is set to ADDR: 0xA0. Pull-up resistors R61 and R62 are connected to SDA and SCL respectively, with one end to the signal line and the other to VCC.

SPKR1  
SPKR-HY4004M-001

PCB1  
SM1000D  
**PRINTED CIRCUIT BOARD**

# ROWETEL

Document  
Number: SM1000-F

REV:  
F

Sheet: 1/2

