

The diagram illustrates three pull-up resistor configurations:

- R2 (4.7K, 1%):** Connected between VDD and USART3_RX.
- R5 (4.7K, 1%):** Connected between VDD and USART3_TX.
- R6 (1K, 1%):** Connected between BOOT1 and GND.

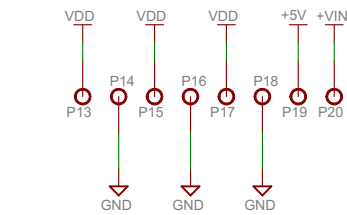
LED-PWR R12 680,1% Power LED1 LED-RED,RA

LED-PTT R13 680,1% PTT LED2 LED-RED,RA

LED-RT R15 680,1% Sync LED3 LED-GRN,RA

LED-ERR R16 680,1% Clip/Error LED4 LED-YEL,RA

GND



Pin connection diagram for the STM32F405 microcontroller. The diagram shows the U1A component with pins 1 through 46 connected to various signals. Power pins (PE0-PE3) are connected to PE0-PE3. Analog pins (PA0-PA15) are connected to PA0-PA15. Digital pins (PB0-PB15, PC0-PC15) are connected to PB0-PB15, PC0-PC15. I/O pins (PD0-PD15, NRST, BOOT0) are connected to PD0-PD15, NRST, BOOT0. The diagram also shows the connection of the DAC1_OUT and DAC2_OUT pins to the DAC1_OUT and DAC2_OUT pins. The diagram includes a table of pin numbers and their corresponding signals.

Pin	Signal
1	PE2
2	PE3
3	PE4
4	PE5
5	PE6
6	PE7
7	PC13
8	PC14
9	PC15
10	NRST
11	PC11
12	PC12
13	PC10
14	NRST
15	OTG_PWR_ON
16	PC1
17	PC2
18	PC3
19	PC4
20	PC5
21	PC6
22	PC7
23	PC8
24	PC9
25	PC10
26	PC11
27	PC12
28	PC13
29	PC14
30	PC15
31	PC16
32	PC17
33	PC18
34	PC19
35	PC20
36	PC21
37	PC22
38	PC23
39	PC24
40	PC25
41	PC26
42	PC27
43	PC28
44	PC29
45	PC30
46	PC31

**NOTE: SW1 HELD ON POWER UP
FORCES BOOT TO SYSTEM
MEMORY (STLINK)**

3.3V TTL Levels

CN11 HDR100-3

USART3_TX

USART3_RX

1

2

3

GND

The schematic diagram illustrates the internal circuitry of a USB-C to USB-A adapter. It features two main integrated circuits: the STMP2141 (U3) and the EMIF02-USB03F2 (U4). The USB-C connector (CN1) provides power (VDD, GND) and data lines (IN, EN, OUT, FAULT). The STMP2141 is configured with a 4.7uF, 16V tantalum capacitor (C9) and a 47K, 1% resistor (R24). It is connected to the EMIF02-USB03F2 via a 22.1% resistor (R27) and a 22.1% resistor (R28). The EMIF02-USB03F2 is connected to the USB-A connector (CN2) via a 0 Ohm resistor (R30). The circuit also includes a 0 Ohm resistor (R26) and a 10K, 1% resistor (R25). The USB-A connector (CN2) provides power (VBUS, DM, DP, ID, GND) and data lines (SHIELD1, SHIELD2, SHIELD3, SHIELD4). The circuit is powered by a 5V supply and includes a GND connection.

TODO:

1. Enlarge pads on R46 POT
2. AVDD has no source.
3. Check polarity & source of LMV341 pin 5.
4. Reposition 3.5mm jacks for 11.3mm plug spacing

Note: All non-polarized capacitors packages are 0603 unless otherwise noted.
Note: All resistor packages are 0603 unless otherwise noted.

PCB1
SM1000A
PRINTED CIRCUIT BOARD

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ROWETEL

TITLE: SmartMic Circuit Board

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Number: SM1000-C

REV:
C

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DATE	REVISION	ECN	REV	BY
04/21/14	Added to SourceForge Repository			RGB
04/25/14	Pre-Release to Prototype		A	RGB
05/28/14	Release for Review		B	RGB
06/13/14	Release for Prototype		B	RGB