

The diagram shows the NRST pin connected to VDD via a 4.7K resistor (R3) and to ground via a jumper (J5).

		U1A		STM32F407	
	AIN1	23	PA0	PE0	97
		24	PA1	PE1	98
	AIN2	25	PA2	PE2	1
		26	PA3	PE3	2
	DAC1_OUT	29	PA4	PE4	3
	DAC2_OUT	30	PA5	PE5	4
		31	PA6	PE6	5
		32	PA7	PE7	38
		67	PA8	PE8	39
	VBUS	68	PA9	PE9	40
	OTG_ID	69	PA10	PE10	41
	OTG_DM	70	PA11	PE11	42
	OTG_DP	71	PA12	PE12	43
	SWDIO	72	PA13	PE13	44
	SWCLK	76	PA14	PE14	45
		77	PA15	PE15	46
		35	PB0	PD0	81 SELECT
		36	PB1	PD1	82 BACK
	BOOT1	37	PB2	PD2	83
		89	PB3	PD3	84
		90	PB4	PD4	85
	SCL	91	PB5	PD5	86 OTG_OVR_CUR
		92	PB6	PD6	87
		93	PB7	PD7	88 BOOT0
		95	PB8	PD8	55 EXT-PTT
	SDA	96	PB9	PD9	56 TEST
	USART3_TX	47	PB10	PD10	57 CPPT
	USART3_RX	48	PB11	PD11	58
		51	PB12	PD12	59 LED-PWR
		52	PB13	PD13	60 LED-PTT
		53	PB14	PD14	61 LED-RTT
		54	PB15	PD15	62 LED-ERR
	OTG_PWR_ON	15	PC0		
		16	PC1		
		17	PC2		
		18	PC3	PH0	12 OSC_IN
		33	PC4	PH1	13 OSC_OUT
		34	PC5		
		63	PC6		
		64	PC7	PC14	8
		65	PC8	PC15	9
		66	PC9		
		78	PC10		
		79	PC11	NRST	14 NRST
		80	PC12		
		7	PC13	BOOT0	94 BOOT0

[illegible]

3.3V TTL Levels

CN11 HDR100-3

USART3 TX

USART3 RX

1

2

3

GND

The schematic diagram illustrates the internal wiring of the USB3F2 module. It features two main integrated circuits: the EMIF02-USB03F2 (U4) and the STMP2141 (U3). The EMIF02-USB03F2 is connected to the USB3F2 connector (CN2) and provides power and data signals to the STMP2141. The STMP2141 is configured as a USB-to-serial converter, with its IN pin connected to the VBUS line and its OUT pin connected to the DM line. The EN pin of the STMP2141 is connected to the DP line, and the FAULT pin is connected to the ID line. The module also includes a 4.7uF, 16V tantalum capacitor (C9) for power filtering and several resistors (R24, R25, R26, R27, R28, R30) for signal conditioning and current limiting. The power supply is provided by a +5V source, and the ground is connected to the GND pin of the connector.

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