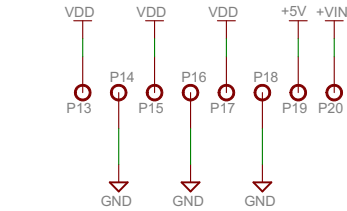


The diagram shows three pull-up resistor configurations:

- USART3_RX:** A green line labeled "USART3_RX" is connected to "VDD" on the left and has a pull-up resistor "R2" (4.7K, 1%) connected to "GND" on the right.
- USART3_TX:** A green line labeled "USART3_TX" is connected to "VDD" on the left and has a pull-up resistor "R5" (4.7K, 1%) connected to "GND" on the right.
- BOOT1:** A green line labeled "BOOT1" is connected to "VDD" on the left and has a pull-up resistor "R6" (1K, 1%) connected to "GND" on the right.



Pin	Signal	Pin	Signal	Pin	Signal
AIN1		23	PA0	97	PE0
AIN2		24	PA1	98	PE1
		25	PA2	1	PE2
		26	PA3	2	PE3
		29	PA4	3	
		30	PA5	4	
		31	PA6	5	
		32	PA7	38	
		67	PA8	39	
		68	PA9	40	
		69	PA10	41	
		70	PA11	42	
		71	PA12	43	
		72	PA13	44	
		76	PA14	45	
		77	PA15	46	
		35	PB0	81	SELECT
		36	PB1	82	BACK
		37	PB2	83	
		89	PB3	84	
		90	PB4	85	
		91	PB5	86	OTG_OVR_CUR
		92	PB6	87	
		93	PB7	88	BOOT0
		95	PB8	55	EXT-PTT
		96	PB9	D8	TEST
		47	PB10	D9	CPTT
		48	PB11	D10	
		51	PB12	D11	58
		52	PB13	D12	59
		53	PB14	D13	60
		54	PB15	D14	61
				D15	62
		15	PC0		
		16	PC1	12	OSC_IN
		17	PC2	13	OSC_OUT
		18	PC3		
		33	PC4		
		34	PC5		
		63	PC6	8	
		64	PC7	9	
		65	PC8		
		66	PC9		
		78	PC10		
		79	PC11		
		80	PC12		
		7	PC13		
				14	NRST
				94	BOOT0

VDD

3 4

SW1

SW-BOURNS-7914S

1 2

BOOT0

R1
10K,1%

GND

**NOTE: SW1 HELD ON POWER UP
FORCES BOOT TO SYSTEM
MEMORY (STLINK)**

3.3V TTL Levels

CN11 HDR100-3

UART3_TX

UART3_RX

GND

1

2

3

The schematic diagram illustrates the internal wiring of the USB3F2 module. It features two main integrated circuits: the **STMP2141** (U3) and the **EMIF02-USB03F2** (U4). The **STMP2141** is configured as a USB-to-serial bridge, with its **IN** pin connected to the **OTG PWR_ON** signal through a 0 Ohm resistor (R26), its **EN** pin connected to a +5V supply through a 10K resistor (R25), and its **FAULT** pin connected to ground. Its **OUT** pin is connected to the **VBUS** line through a 47K resistor (R24). A 4.7uF tantalum capacitor (C9) is used for decoupling on the **VBUS** line. The **EMIF02-USB03F2** is connected to the **STMP2141** via a series of resistors: 22.1K (R27) for **DM**, 22.1K (R28) for **DP**, and 0 Ohm (R30) for **ID**. The **EMIF02-USB03F2** also has its own **VBUS** pin connected to the **VBUS** line and its **D+OUT** pin connected to the **DM** line. The **EMIF02-USB03F2** is connected to a **MOLEX-MINI-B-SMT** connector (CN2) which provides **VBUS**, **DM**, **DP**, **ID**, and four shield pins (**SHIELD1** to **SHIELD4**). The **EMIF02-USB03F2** is also connected to ground through its **GND** pin and the **D2** pin of the connector. The **OTG OVR_CUR** signal is connected to the **OTG PWR_ON** line. The **OTG DM**, **OTG DP**, and **OTG ID** signals are connected to the **DM**, **DP**, and **ID** pins of the **EMIF02-USB03F2** respectively. The **VBUS** signal is connected to the **VBUS** pin of the **EMIF02-USB03F2** and the **VBUS** pin of the connector. The **DM** signal is connected to the **DM** pin of the **EMIF02-USB03F2** and the **DM** pin of the connector. The **DP** signal is connected to the **DP** pin of the **EMIF02-USB03F2** and the **DP** pin of the connector. The **ID** signal is connected to the **ID** pin of the **EMIF02-USB03F2** and the **ID** pin of the connector. The **SHIELD1** to **SHIELD4** signals are connected to the shield pins of the connector. The **VBUS** signal is connected to the **VBUS** pin of the connector. The **DM** signal is connected to the **DM** pin of the connector. The **DP** signal is connected to the **DP** pin of the connector. The **ID** signal is connected to the **ID** pin of the connector. The **SHIELD1** to **SHIELD4** signals are connected to the shield pins of the connector. The **VBUS** signal is connected to the **VBUS** pin of the connector. The **DM** signal is connected to the **DM** pin of the connector. The **DP** signal is connected to the **DP** pin of the connector. The **ID** signal is connected to the **ID** pin of the connector. The **SHIELD1** to **SHIELD4** signals are connected to the shield pins of the connector.

Sheet: 1/2

