



VDD

3 4

SW1

SW-BOURNS-7914S

1 2

BOOT0

R1

10K,1%

GND

**NOTE: SW1 HELD ON POWER UP
FORCES BOOT TO SYSTEM
MEMORY (STLINK)**

[illegible]

TODO:

1. Enlarge pads on R46 POT
2. AVDD has no source.
3. SW1 to NC contacts.
4. Check polarity & source of LHM341 pin 5.
5. Add 480 Ohm pullup to PCC circuit.

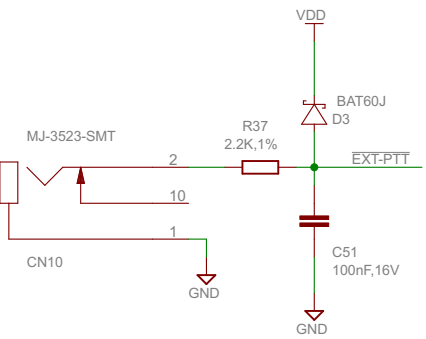
Note: All non-polarized capacitors packages are 0603 unless otherwise noted.
Note: All resistor packages are 0603 unless otherwise noted.

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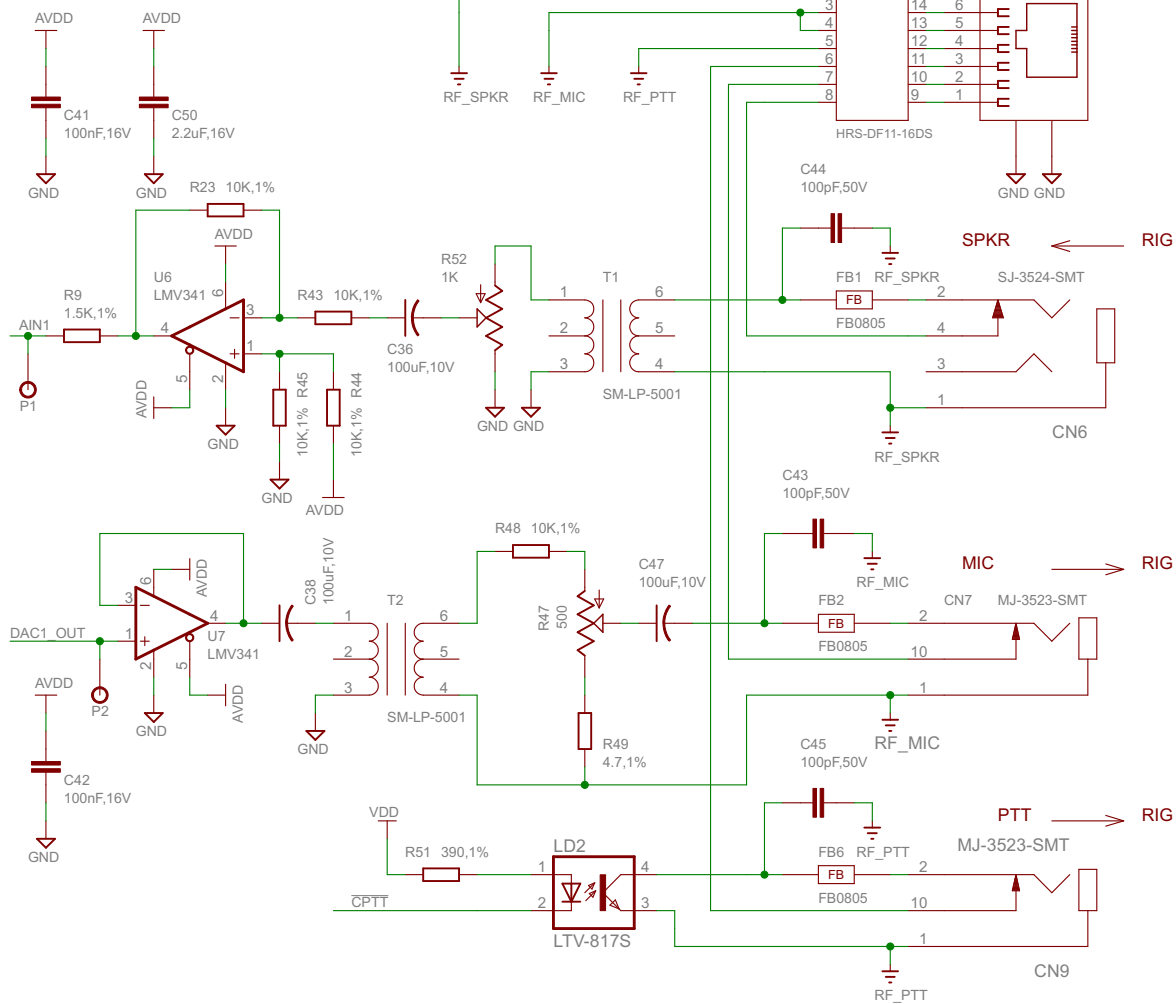
TITLE: SmartMic Circuit Board

REV: C

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DATE	REVISION	ECN	REV	BY
04/21/14	Added to SourceForge Repository			RGB
04/25/14	Pre-Release to Prototype		A	RGB
05/28/14	Release for Review		B	RGB
06/13/14	Release for Prototype		B	RGB



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ROWETEL

TITLE: Smart Mic Circuit Board

Document
Number: SM1000-C

REV:
C

Date: 10/2/2014 8:38:19 PM

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