

06/16/2014

- 40. Add test point P20 (+VIN).
- 43. Add test point P19 (+5V).
- 42. Add test points P13,p15,p17 (VDD).
- 41. Add test points P14,P16,P18 (GND).
- 40. Add test points at pins 24,25,29,30.
AIN1,AIN2,DAC1,DAC2
- 39. Add test points at pins 97,98,1,2.
PE0,PE1,PE2,PE3
- 38. Add test points at pins 77,78,79,80.
PA15,PC10,PC11,PC12

06/15/2014

- 37. Change GND on CN12-12,13 to RF
- 36. Change C39 to 220uF,25V
- 35. Move Pot R52 to T1 output & change to 1K.
- 34. Change C40 to 25V.
- 33. Change C27 to 22pF.
- 32. Change C2 & C3 to 20pF.
- 31. Correct pinout for LD3985.
- 30. Change R1, R7 & R8 to 10K
- 29. Change R12, R13, R15, R16 & R17 to 680 Ohms.
- 28. Change C8 to 10nF.
- 27. Change C10 & C12 to Ceramic 0603 caps.
- 26. Remove signals from pins 9 & 10 CN12.

06/12/2014

- 25. Change SW3 back to PBH2UUENAGX
- 24. Change D3 to BAT60

06/05/2014

- 23. Change 0603 caps to IPC packages & symbols.
- 22. Change 0603 resistors to IPC packages & symbols.

06/02/2014

- 21. Add SELECT & BACK switches.

06/01/2014

- 20. Delete VUSB Led & circuitry.
- 19. Delete USB fault LED & circuitry.
- 18. Delete STLINK LED & components.
- 17. Change SCL, SDA pulldowns to pullups.

05/29/2014

- 16. Change CN1 to 3-pin
- 15. Power electret mic from VDD
- 14. Change Reset switch to header.

05/28/2014

- 13 Added PTT SW network to permit DFU mode on power up.
- 12. Change C23 to 25V.
- 11. Change C43, C44, C45 to 100pF
- 10. Add hdr for speaker.
- 9. Change bypass caps to mix of 100nF and 10nF.
- 8. Change processor from STM32F407 to STM32F405.
- 7. Change PCB from 4 layer to 2 layer.
- 6. Bring GND, RF(GND) & +VEXT to IC1 connector.
- 5. Add ext. PTT connector and cpu DI.
- 4. Change mic preamp circuit to op-amp per DR.
- 3. Change rig mic amp circuit to pot on transformer output.
- 2. Move LED8 to 5V
- 1. Change U5 TPS54329E to RT8250

Change Log for SM1000 PCB