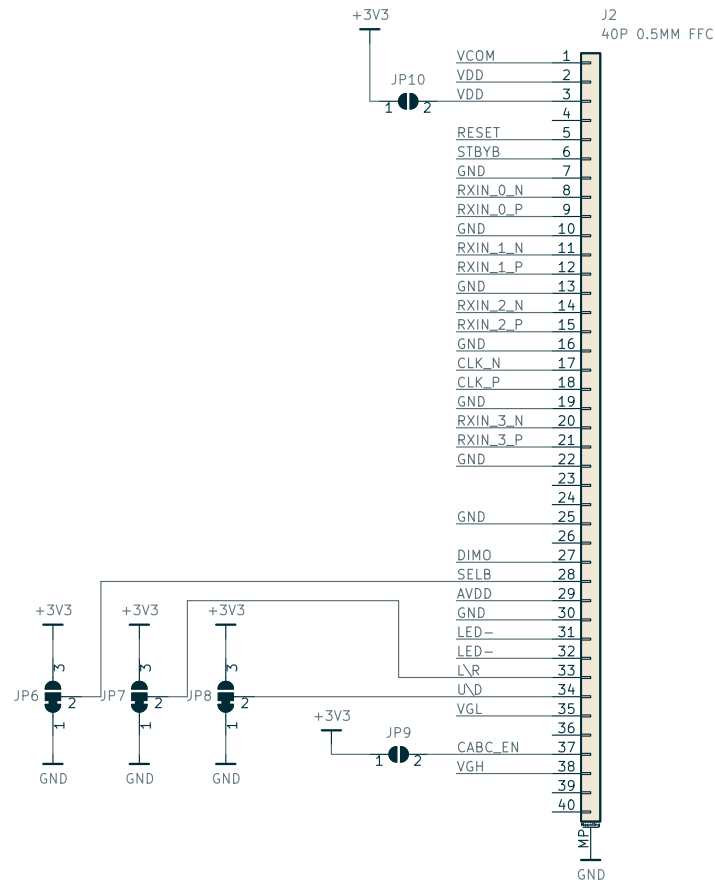


Power Supply
Analog Power Supply
Power On Gate Voltage
Power Off Gate Voltage
LED Backlight

VDD
AVDD
VG
VGL
VL

10.1" panel			7" panel		
Typ	2.5V	95mA	Typ	3.3V	33mA
Max	8.2V	45mA	Max	9.5V	18mA
Typ	22V	705uA	Typ	18V	310uA
Max	-7V	705uA	Max	-10V	1000uA
Typ	14.3V	370mA	Typ	12.8V	420mA
Max			Max		

Supply sequencing: STBYB > VDD > AVDD > VGL > VG > DATA > B/L



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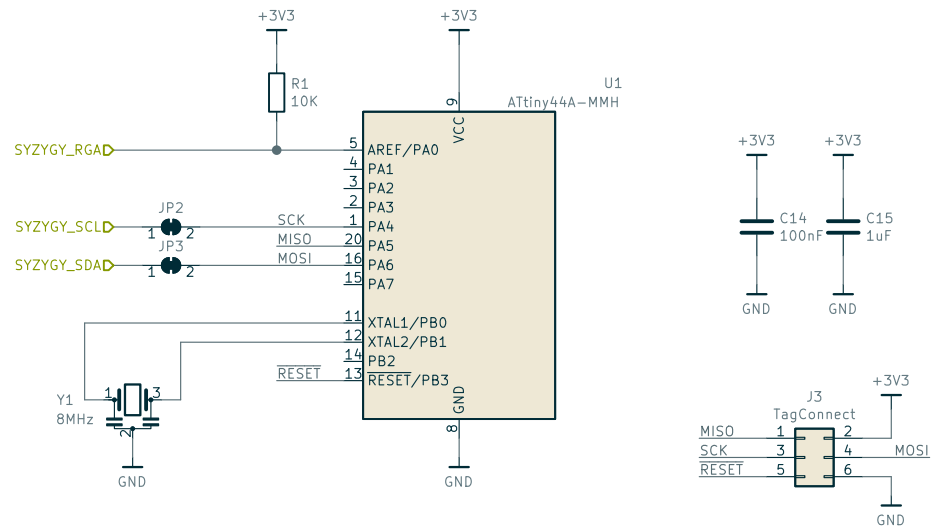
Title: SYZYGY LVDS LCD

Size: A4 Date: 2019-03-20

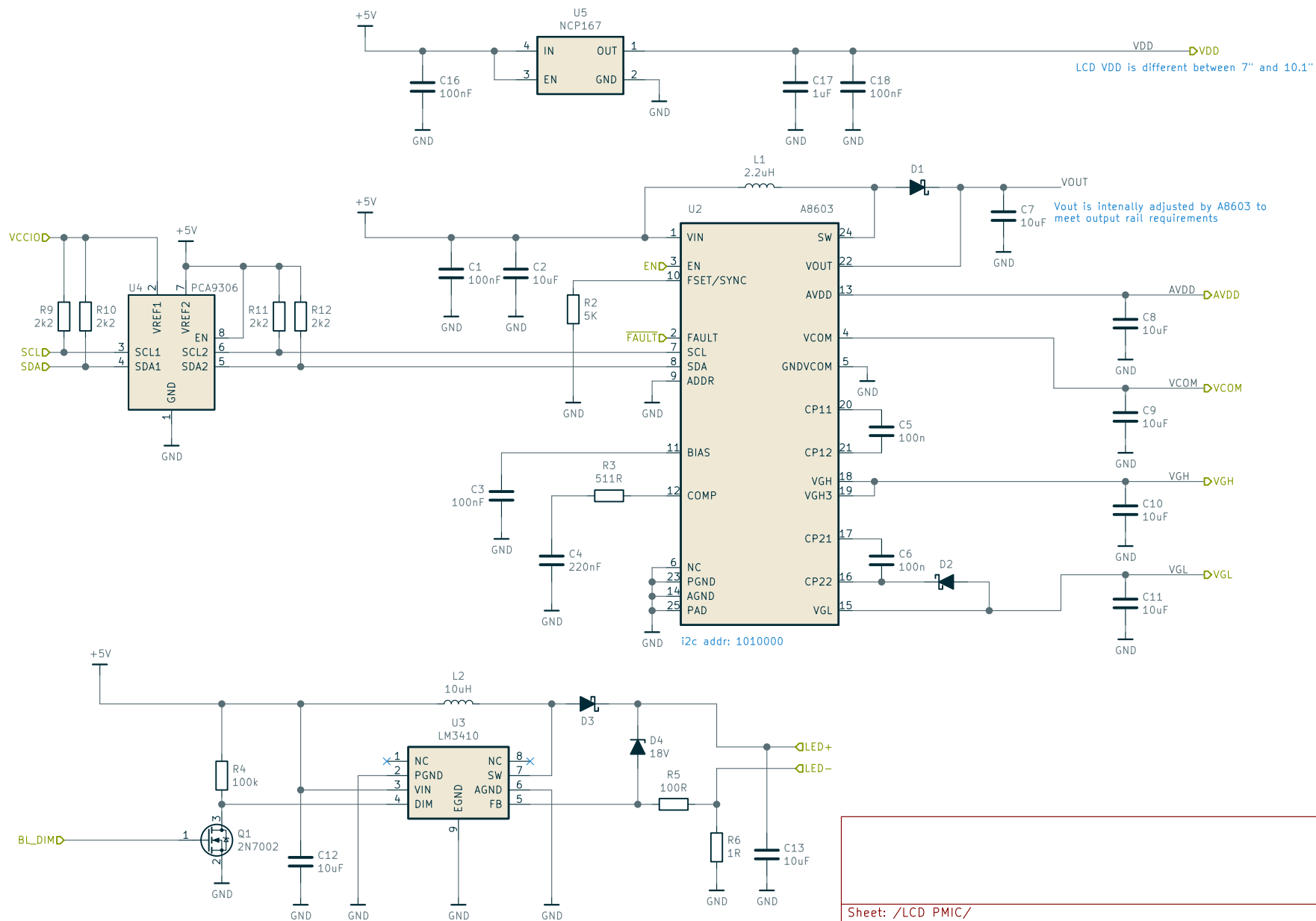
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Rev: r1.0

Id: 1/3



Sheet: /Peripheral MCU/ File: pMCU.sch		
Title: SYZGY LVDS LCD		
Size: A4	Date: 2019-03-20	Rev: r1.0
KiCad E.D.A. kicad 5.1.6-c6e7f7d86ubuntu18.04.1		Id: 2/3



Sheet: /LCD PMIC/
File: pmic.sch

Title: SYZYGY LVDS LCD

Size: A4 Date: 2019-03-20

KiCad E.D.A. kicad 5.1.6-c6e7f7d86ubuntu18.04.1

Rev: r1.0

Id: 3/3