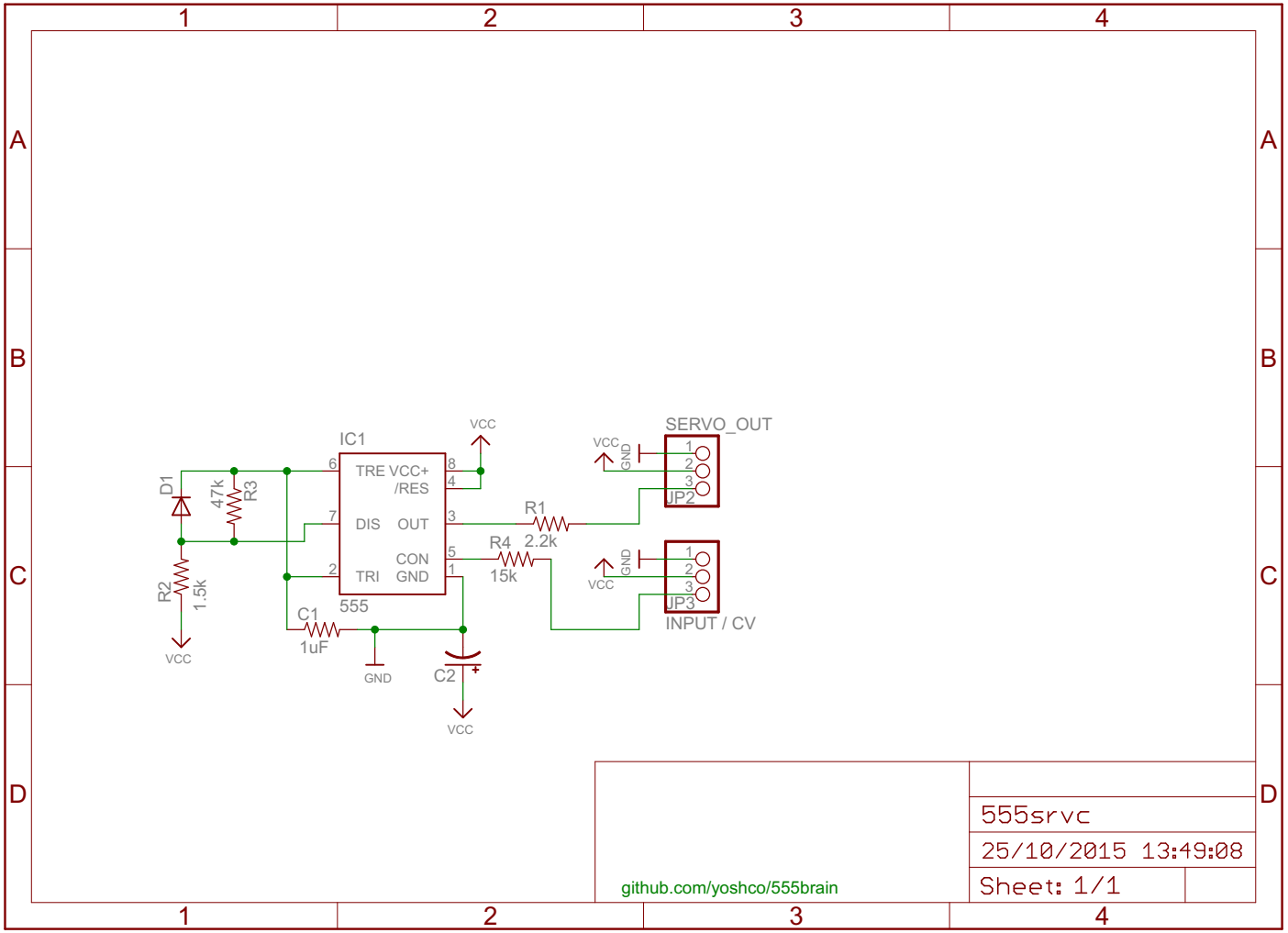
IC1
555
TRE
VCC+
/RES
DIS
OUT
CON
TRI
GND
6
7
2
8
4
3
5
1
VCC
D1
47k
R3
R2
1.5k
VCC
C1
1uF
GND
C2
VCC
R1
2.2k
R4
15k
SERVO_OUT
VCC
GND
JP2
INPUT / CV
JP3
555srvc
25/10/2015 13:49:08
Sheet: 1/1
github.com/yoshco/555brain