**Module Design Document**

**For**

**MotAgArbn**

**Nov 16, 2016**

**Prepared For:**

**Software Engineering**

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| **Description** | **Author** | **Version** | **Date** |
| Initial Version | ML | 1.0 | 16-Nov-2016 |

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# Introduction

## Purpose

## Scope

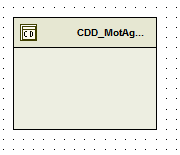
# MotAgArbn & High-Level Description

*Refer FDD.*

# Design details of software module

*<The Data Flow Diagrams should be created in the absence of this representation with the FDD.>*

## Graphical representation of MotAgArbn



## Data Flow Diagram

Refer FDD

### Component level DFD

Refer FDD

### Function level DFD

Refer FDD

# Constant Data Dictionary

## Program (fixed) Constants

### Embedded Constants

#### Local Constants

|  |  |  |  |
| --- | --- | --- | --- |
| Constant Name | Resolution | Units | Value |
| Refer .m file |  |  |  |
| CORRLNSTSMASKSIGA\_CNT\_U08 | 1 | Cnt | 0x01 |
| MAXSTALLCNTR\_CNT\_U08 | 1 | Cnt | 255 |

# Software Component Implementation

## Sub-Module Functions

## Init: MotAgArbnInit1

## Design Rationale

Init1 function is created so that it will allow a RTE model to be created in the AUTOSAR tools which allows Per-Instance Memory and calibration definition needs. The initialization function is doing nothing

## Module Outputs

*None*

## Per: MotAgArbnPer1

## Design Rationale

*None*

## Processing of function)……

*Refer FDD*

## Server Runnables

None

## Interrupt Functions

*None*

## Module Internal (Local) Functions

## Local Function #1

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | SigAvlChkRev | Type | Min | Max |
| **Arguments Passed** | SigCorrChk\_Cnt\_T\_u08 | uint8 | 0 | 1 |
|  | SigRollgCnt\_Cnt\_T\_u08 | uint8 | 0 | 255 |
|  | SigQlfr\_Cnt\_T\_enum | SigQlfr1 | SIGQLFR\_NORES | SIGQLFR\_FAILD |
|  | \* LstRollgCnt\_Cnt\_T\_u08 | uint8 | 0 | 255 |
|  | \* StallCnt\_Cnt\_T\_u08 | uint8 | 0 | 255 |
| **Return Value** | SigAvl\_Cnt\_T\_lgc | boolean | FALSE | TRUE |

## Design Rationale

None

## Processing

Refer FDD SigAvlChkRev2 State flow Chart

## GLOBAL Function/Macro Definitions

None

# Known Limitations with Design

None

# UNIT TEST CONSIDERATION

None

Abbreviations and Acronyms

| **Abbreviation or Acronym** | **Description** |
| --- | --- |
|  |  |
|  |  |

Glossary

**Note**: Terms and definitions from the source “Nexteer Automotive” take precedence over all other definitions of the same term. Terms and definitions from the source “Nexteer Automotive” are formulated from multiple sources, including the following:

* ISO 9000
* ISO/IEC 12207
* ISO/IEC 15504
* Automotive SPICE® Process Reference Model (PRM)
* Automotive SPICE® Process Assessment Model (PAM)
* ISO/IEC 15288
* ISO 26262
* IEEE Standards
* SWEBOK
* PMBOK
* Existing Nexteer Automotive documentation

| **Term** | **Definition** | **Source** |
| --- | --- | --- |
| MDD | Module Design Document |  |
| DFD | Data Flow Diagram |  |

References

| **Ref. #** | **Title** | **Version** |
| --- | --- | --- |
| 1 | AUTOSAR Specification of Memory Mapping (Link:[AUTOSAR\_SWS\_MemoryMapping.pdf](http://www.autosar.org/download/R4.0/AUTOSAR_SWS_MemoryMapping.pdf)) | v1.3.0 R4.0 Rev 2 |
| 2 | MDD Guideline | Process 4.02.01 |
| 3 | [Software Naming Conventions.doc](http://misagweb01.nexteer.com/eRoomReq/Files/erooms8/NextGeneration/0_fc55f/Software%20Naming%20Conventions%2003x(In%20Work).doc) | Process 4.02.01 |
| 4 | [Software Design and Coding Standards.doc](http://eroom1.nexteer.com/eRoomReq/Files/erooms8/NextGeneration/0_1a67a9/Software%20Design%20and%20Coding%20Standards.doc) | Process 4.02.01 |
| 5 | FDD – ES248B\_MotAgArbn\_Design | See Synergy Subproject verison |