**Module Design Document**

**For**

**TqOscn**

**Feb 05, 2016**

**Prepared For:**

**Software Engineering**

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**Saginaw, MI, USA**

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# Introduction

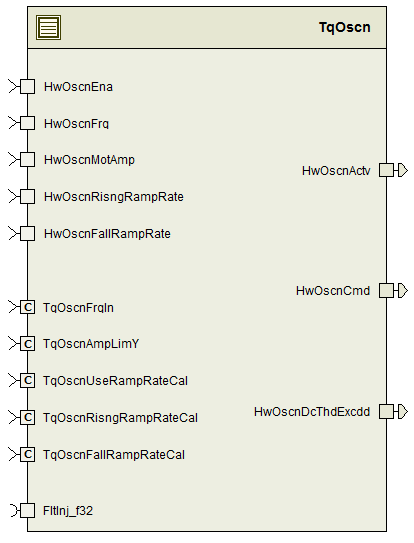
## Purpose

# TqOscn & High-Level Description

Please refer FDD.

# Design details of software module

## Graphical representation of TqOscn

**

## Data Flow Diagram

Please refer FDD

### Component level DFD

Please refer FDD

### Function level DFD

Please refer FDD

# Constant Data Dictionary

## Program (fixed) Constants

### Embedded Constants

#### Local Constants

|  |  |  |  |
| --- | --- | --- | --- |
| Constant Name | Resolution | Units | Value |
| Please refer .m file |  |  |  |

# Software Component Implementation

## Sub-Module Functions

## Init: TqOscnInit1

## Design Rationale

None

## Module Outputs

## None

## Per: TqOscnPer1

## Design Rationale

None

## Store Module Inputs to Local copies

None

## (Processing of function)………

Please refer FDD

## Store Local copy of outputs into Module Outputs

Please refer FDD

## Server Runnables

None

## Interrupt Functions

None

## Module Internal (Local) Functions

## Local Function #1

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | AmpRateLim | Type | Min | Max |
| **Arguments Passed** | LimdAmp\_MotNwtMtr\_T\_f32 | Float32 | 0.0F | 1.2F |
|  | HwOscnRisngRampRate\_MotNwtMtrPerSec\_T\_f32 | Float32 | 0.1F | 4400.0F |
|  | HwOscnFallRampRate\_MotNwtMtrPerSec\_T\_f32 | Float32 | -4400.0F | -0.1F |
|  | HwOscnEna\_Cnt\_T\_logl | Boolean | FALSE | TRUE |
|  | \*NonZeroAmpFlg\_Cnt\_T\_logl | Boolean | FALSE | TRUE |
| **Return Value** | RateLimdAmp\_MotNwtMtr\_T\_f32 | Float32 | 0.0002F | -8.8F |

## Local Function #2

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Function Name** | ChkFlg | Type | Min | Max |
| **Arguments Passed** | PhaAg\_MatRad\_T\_f32 | Float32 | 0.125F | 0.628F |
| **Return Value** | TqOscnPhaAg\_MatRad\_T\_f32 | Float32 | 0.0F | 0.628F |

## GLOBAL Function/Macro Definitions

None

# Known Limitations with Design

None

# UNIT TEST CONSIDERATION

None.

Abbreviations and Acronyms

| **Abbreviation or Acronym** | **Description** |
| --- | --- |
|  |  |
|  |  |

Glossary

**Note**: Terms and definitions from the source “Nexteer Automotive” take precedence over all other definitions of the same term. Terms and definitions from the source “Nexteer Automotive” are formulated from multiple sources, including the following:

* ISO 9000
* ISO/IEC 12207
* ISO/IEC 15504
* Automotive SPICE® Process Reference Model (PRM)
* Automotive SPICE® Process Assessment Model (PAM)
* ISO/IEC 15288
* ISO 26262
* IEEE Standards
* SWEBOK
* PMBOK
* Existing Nexteer Automotive documentation

| **Term** | **Definition** | **Source** |
| --- | --- | --- |
| MDD | Module Design Document |  |
| DFD | Data Flow Diagram |  |

References

| **Ref. #** | **Title** | **Version** |
| --- | --- | --- |
| 1 | AUTOSAR Specification of Memory Mapping (Link:[AUTOSAR\_SWS\_MemoryMapping.pdf](http://www.autosar.org/download/R4.0/AUTOSAR_SWS_MemoryMapping.pdf)) | v1.3.0 R4.0 Rev 2 |
| 2 | MDD Guideline | EA4 01.00.00 |
| 3 | [Software Naming Conventions.doc](http://misagweb01.nexteer.com/eRoomReq/Files/erooms8/NextGeneration/0_fc55f/Software%20Naming%20Conventions%2003x(In%20Work).doc) | 1.0 |
| 4 | [Software Design and Coding Standards.doc](http://eroom1.nexteer.com/eRoomReq/Files/erooms8/NextGeneration/0_1a67a9/Software%20Design%20and%20Coding%20Standards.doc) | 2.0 |
| 5 | FDD: SF043A\_ TqOscn\_Design | See Synergy sub project version |