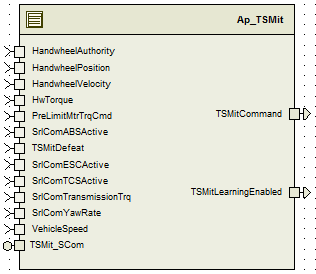
# Module --

# High-Level Description

This function provides a motor torque command to mitigate linear rack force disturbances caused by unequal driveline torques.

# Figures



# Variable Data Dictionary

For details on module input / output variable, refer to the Data Dictionary for the application. Input / output variable names are listed here for reference.

|  |  |  |
| --- | --- | --- |
| Module Inputs | Module Outputs | |
| HandwheelAuthority\_Uls\_f32 | | TSMitCommand\_MtrNm\_f32 |
| HandwheelPosition\_HwDeg\_f32 | | TSMitLearningEnabled\_Cnt\_lgc |
| HandwheelVelocity\_HwRadpS\_f32 | |  |
| HwTorque\_HwNm\_f32 | |  |
| PreLimitMtrTrqCmd\_MtrNm\_f32 | |  |
| SrlComABSActive\_Cnt\_lgc | |  |
| SrlComESCActive\_Cnt\_lgc | |  |
| SrlComTCSActive\_Cnt\_lgc | |  |
| SrlComTransmissionTrq\_TransNm\_f32 | |  |
| SrlComYawRate\_DegpS\_f32 | |  |
| VehicleSpeed\_Kph\_f32 | |  |
| TSMitDefeat\_Cnt\_lgc | |  |

## Module Internal Variables

This section identifies the name, range and resolutions for module specific data created by this module. If there are no range restrictions on the variable, the term “FULL” is placed into the table for legal range.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Variable Name | Resolution | Legal Range  (min) | Legal Range  (max) | Software Segment |
| TSMit\_PosTrsmTqGainLongTerm\_Uls\_M\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_32 |
| TSMit\_NegTrsmTqGainLongTerm\_Uls\_M\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_32 |
| TSMit\_GainLearningEnableTimer\_mS\_M\_u32 | 1 | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_32 |
| TSMit\_PrevTSMitCommand\_HwNm\_M\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_32 |
| TSMit\_HwTrqFilterKSV\_Cnt\_M\_str | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| TSMit\_HwTrqFilterKSV\_Cnt\_M\_str .SV\_Uls\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| TSMit\_HwTrqFilterKSV\_Cnt\_M\_str .K\_Uls\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| TSMit\_YawRateFilterKSV\_Cnt\_M\_str | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| TSMit\_YawRateFilterKSV\_Cnt\_M\_str .SV\_Uls\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| TSMit\_YawRateFilterKSV\_Cnt\_M\_str .K\_Uls\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| TSMit\_STPosTransGainFilterKSV\_Cnt\_M\_str | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| TSMit\_STPosTransGainFilterKSV\_Cnt\_M\_str .SV\_Uls\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| TSMit\_STPosTransGainFilterKSV\_Cnt\_M\_str .K\_Uls\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| TSMit\_STNegTransGainFilterKSV\_Cnt\_M\_str | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| TSMit\_STNegTransGainFilterKSV\_Cnt\_M\_str .SV\_Uls\_f32 |  | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| TSMit\_STNegTransGainFilterKSV\_Cnt\_M\_str .K\_Uls\_f32 |  | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |
| TSMit\_HwTqFiltered\_HwNm\_D\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_32 |
| TSMit\_YawRateFiltered\_DegpS\_D\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_32 |
| TSMit\_PinionTrqEst\_HwNm\_D\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_32 |
| TSMit\_PosTransTrqGainShortTerm\_HwNmpTransNm\_D\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_32 |
| TSMit\_NegTransTrqGainShortTerm\_HwNmpTransNm\_D\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_32 |
| TSMit\_PosTransTrqGainLongTerm\_HwNmpTransNm\_D\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_32 |
| TSMit\_NegTransTrqGainLongTerm\_HwNmpTransNm\_D\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_32 |
| TSMit\_CmdScaleFactor\_Uls\_D\_f32 | Single Precision Float | See DD | See DD | TSMIT\_START\_SEC\_VAR\_CLEARED\_32 |
| TSMit\_CommandRaw\_HwNm\_D\_f32 | Single Precision Float |  |  | TSMIT\_START\_SEC\_VAR\_CLEARED\_32 |

### User defined typedef definition/declaration

This section documents any user types uniquely used for the module.

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| Typedef Name | Element Name | User Defined Type | Legal Range  (min) | Legal Range  (max) |
| (Name given for the user defined typdef of type struct/union)  (Variable name qualified similar to all other variables) | (Variable name qualified similar to all other variables) | as other variables |  |  |
|  | (Variable name qualified similar to all other variables) |  |  |  |

# Constant Data Dictionary

## Calibration Constants

This section lists the calibrations used by the module. For details on calibration constants, refer to the Data Dictionary for the application.

|  |
| --- |
| Constant Name |
| k\_TSMitHwTrqLPFiltFc\_Hz\_f32 |
| k\_TSMitYawRateLPFiltFc\_Hz\_f32 |
| k\_TSMitGainLearningFreqFc\_Hz\_f32 |
| k\_TSMitUseABSActiveFlag\_Cnt\_lgc |
| k\_TSMitUseTCSActiveFlag\_Cnt\_lgc |
| k\_TSMitUseESCActiveFlag\_Cnt\_lgc |
| k\_TSMitMinHwTrqEnable\_HwNm\_f32 |
| k\_TSMitMaxHwPosEnable\_HwDeg\_f32 |
| k\_TSMitMaxYawRateEnable\_DegpS\_f32 |
| k\_TSMitMinHwAuthEnable\_Uls\_f32 |
| k\_TSMitMinVehSpdEnable\_Kph\_f32 |
| k\_TSMitMaxVehSpdEnable\_Kph\_f32 |
| k\_TSMitMaxHwVelEnable\_HwRadpS\_f32 |
| k\_TSMitMaxNegTransTrqEnable\_TransNm\_f32 |
| k\_TSMitMinPosTransTrqEnable\_TransNm\_f32 |
| k\_TSMitTimeOnEnable\_Sec\_f32 |
| k\_TSMitPosTransGainLimit\_HwNmpTransNm\_f32 |
| k\_TSMitDisableNegTransTrqSTGainLearning\_Cnt\_f32 |
| k\_TSMitNegTransGainLimit\_HwNmpTransNm\_f32 |
| t\_TSMitCmdSclVelocityTbl\_Kph\_u8p8[5] |
| t\_TSMitCmdSclHwPosTbl\_HwDeg\_s10p5[11] |
| t2\_TSMitCmdSclScaleFactor\_Uls\_u2p14[5][11] |
| k\_TSMitPosTransTrqThresh\_TransNm\_f32 |
| k\_TSMitNegTransTrqThresh\_TransNm\_f32 |
| k\_TSMitCmdSlewRate\_HwNmpS\_f32 |
| k\_CmnSysTrqRatio\_HwNmpMtrNm\_f32 |

## Program(fixed) Constants

### Embedded Constants

All embedded constants whose values are provided in Eng units will be evaluated to the equivalent counts by using the FPM\_InitFixedPoint\_m() macro within the #define statement.

#### Local

|  |  |  |  |
| --- | --- | --- | --- |
| Constant Name | Resolution | Units | Value |
| D\_10MS\_SEC\_F32 | Single Precision Float | Sec | 0.01 |
| D\_ONESEC\_MS\_F32 | Single Precision Float | mS | 1000 |
| D\_OUTPUTCMDLIMIT\_HWNM\_F32 | Single Precision Float | HwNm | 3 |

#### Global

This section lists the global constants used by the module. For details on global constants, refer to the Data Dictionary for the application.

|  |
| --- |
| Constant Name |
| D\_ZERO\_ULS\_F32 |
|  |

### Module specific Lookup Tables Constants

(This is for lookup tables (arrays) with fixed values, same name as other tables)

|  |  |  |  |
| --- | --- | --- | --- |
| Constant Name | Resolution | Value | Software Segment |
| None |  |  |  |

# Functions/Macros used by the Sub-Modules

## Library Functions / Macros

The library and functions / Macros that are called by the various sub modules are identified below,

1. Abs\_f32\_m
2. FPM\_FixedToFloat\_m
3. FPM\_FloatToFixed\_m
4. BilinearXYM\_u16\_s16Xu16YM\_Cnt
5. TableSize\_m

## Data Hiding Functions

1. Rte\_Pim\_TSMitGainLrn()
2. Rte\_Pim\_TSMitDisableEOL()

## Global Functions/Macros Defined by this Module

None

## Local Functions/Macros Used by this MDD only

None

# Software Module Implementation

## Runtime Environment (RTE) Initial Values

This section lists the initial values of data written by this module but controlled by the RTE. After RTE initialization, the data in this table will contain these values.

|  |  |
| --- | --- |
| Data | Value |
| Rte\_InitValue\_HandwheelAuthority\_Uls\_f32 | 0 |
| Rte\_InitValue\_HandwheelPosition\_HwDeg\_f32 | 0 |
| Rte\_InitValue\_HandwheelVelocity\_HwRadpS\_f32 | 0 |
| Rte\_InitValue\_HwTorque\_HwNm\_f32 | 0 |
| Rte\_InitValue\_PreLimitMtrTrqCmd\_MtrNm\_f32 | 0 |
| Rte\_InitValue\_SrlComABSActive\_Cnt\_lgc | FALSE |
| Rte\_InitValue\_SrlComESCActive\_Cnt\_lgc | FALSE |
| Rte\_InitValue\_SrlComTCSActive\_Cnt\_lgc | FALSE |
| Rte\_InitValue\_SrlComTransmissionTrq\_TransNm\_f32 | 0 |
| Rte\_InitValue\_SrlComYawRate\_DegpS\_f32 | 0 |
| Rte\_InitValue\_TSMitCommand\_MtrNm\_f32 | 0 |
| Rte\_InitValue\_TSMitLearningEnabled\_Cnt\_lgc | FALSE |
| Rte\_InitValue\_VehicleSpeed\_Kph\_f32 | 0 |
| Rte\_InitValue\_TSMitDefeat\_Cnt\_lgc | FALSE |

## Initialization Functions

### Init: \_Init1

#### Design Rationale

None

#### Design

See unambiguous design at SF47\_TqStrMtgtn/Ap\_TorqueSteerMitigation/TSMit\_Init.

## Periodic Functions

### Per: \_Per1

#### Design Rationale

None

#### Program Flow Start

Rte\_Call\_TSMit\_Per1\_CP0\_CheckpointReached()

#### Store Module Inputs to Local copies

HandwheelAuthority\_Uls\_T\_f32 = Rte\_IRead\_TSMit\_Per1\_HandwheelAuthority\_Uls\_f32()

HandwheelPosition\_HwDeg\_T\_f32 = Rte\_IRead\_TSMit\_Per1\_HandwheelPosition\_HwDeg\_f32()

HandwheelVelocity\_HwRadpS\_T\_f32 = Rte\_IRead\_TSMit\_Per1\_HandwheelVelocity\_HwRadpS\_f32()

HwTorque\_HwNm\_T\_f32 = Rte\_IRead\_TSMit\_Per1\_HwTorque\_HwNm\_f32()

PreLimitMtrTrqCmd\_MtrNm\_T\_f32 = Rte\_IRead\_TSMit\_Per1\_PreLimitMtrTrqCmd\_MtrNm\_f32()

SrlComABSActive\_Cnt\_T\_lgc = Rte\_IRead\_TSMit\_Per1\_SrlComABSActive\_Cnt\_lgc()

SrlComTCSActive\_Cnt\_T\_lgc = Rte\_IRead\_TSMit\_Per1\_SrlComTCSActive\_Cnt\_lgc()

SrlComESCActive\_Cnt\_T\_lgc = Rte\_IRead\_TSMit\_Per1\_SrlComESCActive\_Cnt\_lgc()

SrlComTransmissionTrq\_TransNm\_T\_f32 = Rte\_IRead\_TSMit\_Per1\_SrlComTransmissionTrq\_TransNm\_f32()

SrlComYawRate\_DegpS\_T\_f32 = Rte\_IRead\_TSMit\_Per1\_SrlComYawRate\_DegpS\_f32()

TSMitDefeat\_Cnt\_T\_lgc = Rte\_IRead\_TSMit\_Per1\_TSMitDefeat\_Cnt\_lgc()

VehicleSpeed\_Kph\_T\_f32 = Rte\_IRead\_TSMit\_Per1\_VehicleSpeed\_Kph\_f32()

#### (Processing of function)………

See unambiguous design at SF47\_TqStrMtgtn/Ap\_TorqueSteerMitigation/TSMit\_Per1.

#### Store Local copy of outputs into Module Outputs

Rte\_IWrite\_TSMit\_Per1\_TSMitLearningEnabled\_Cnt\_lgc(GainLearningEnableOutput\_Cnt\_T\_lgc)

Rte\_IWrite\_TSMit\_Per1\_TSMitCommand\_MtrNm\_f32(TSMitCommand\_MtrNm\_T\_f32)

#### Program Flow End

Rte\_Call\_TSMit\_Per1\_CP1\_CheckpointReached()

## Fault Recovery Functions

None

## Shutdown Functions

None

## Interrupt Functions

None

## Serial Communication Functions

### SComm: TSMit\_SCom\_GainReset

#### Design Rationale

None

#### Description

See unambiguous design at SF47\_TqStrMtgtn/Ap\_TorqueSteerMitigation/TSMit\_ResetSvc\_Manuf.

### SComm: TSMit\_SCom\_SetFcnDefeat

#### Design Rationale

None

#### Description

See unambiguous design at SF47\_TqStrMtgtn/Ap\_TorqueSteerMitigation/Set\_FunctionDefeat\_Service.

### SComm: TSMit\_SCom\_SetLrnDefeat

#### Design Rationale

None

#### Description

See unambiguous design at SF47\_TqStrMtgtn/Ap\_TorqueSteerMitigation/Set\_LearningDefeat\_Service.

### SComm: TSMit\_SCom\_SetLongTermGains

#### Design Rationale

None

#### Description

See unambiguous design at SF47\_TqStrMtgtn/Ap\_TorqueSteerMitigation/TSMit Set Long Term Gains Service.

# Execution Requirements

## Execution Sequence of the Module

(Describe in words relevant details about the execution sequence of the different sub modules.)

## Execution Rates for sub-modules called by the Scheduler

This table serves as reference for the Scheduler design

|  |  |  |
| --- | --- | --- |
| Function Name | Calling Frequency | System State(s) in which the function is called |
| TSMit\_Init1 | On Init | All |
| TSMit\_Per1 | 10 ms | All |

## Execution Requirements for Serial Communication Functions

|  |  |
| --- | --- |
| Function Name | Sub-Module called by (Serial Comm Function Name) |
| TSMit\_SCom\_GainReset | Common manufacturing services |
| TSMit\_SCom\_GetFcnDefeat | Common manufacturing services |
| TSMit\_SCom\_GetLongTermGains | Common manufacturing services |
| TSMit\_SCom\_GetLrnDefeat | Common manufacturing services |
| TSMit\_SCom\_SetFcnDefeat | Common manufacturing services |
| TSMit\_SCom\_SetLongTermGains | Common manufacturing services |
| TSMit\_SCom\_SetLrnDefeat | Common manufacturing services |

# Memory Map Definition Requirements

## Sub Modules (Functions)

This table identifies the software segments for functions identified in this module.

|  |  |
| --- | --- |
| Name of Sub Module | Software Segment |
| TSMit\_Init1 | RTE\_START\_SEC\_AP\_TSMIT\_APPL\_CODE |
| TSMit\_Per1 | RTE\_START\_SEC\_AP\_TSMIT\_APPL\_CODE |

## Local Functions

This table identifies the software segments for local functions identified in this module.

|  |  |
| --- | --- |
| Name of Sub Module | Software Segment |
| None |  |

# Known Issues / Limitations With Design

1. None

# Revision Control Log

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| **Item #** | **Rev #** | **Change Description** | **Date** | **Author Initials** |
| 1 | 1 | Initial creation | 25-Jul-14 | Jared |
| 2 | 2 | Added new Defeat input as part of SF-47 actual release | 3-Sept-14 | Jared |