Integration Manual – TrqOsc

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# Dependencies

## SWCs

|  |  |
| --- | --- |
| Module | Required Feature |
| <None> |  |

Note : Referencing the external components should be avoided in most cases. Only in unavoidable circumstance external components should be refered. Developer should track the references.

## Global Functions (Non RTE) to be provided to Integration Project

< None>

# Configuration

## Build Time Config

|  |  |  |
| --- | --- | --- |
| Modules | Notes |  |
| None |  |  |

## Configuration Files to be provided by Integration Project

Ap\_TrqOsc\_Cfg.h for checkpoint enables

### Da Vinci Parameter Configuration Changes

|  |  |  |
| --- | --- | --- |
| Parameter | Notes | SWC |
| TrqOscGeneral/TrqOscCPEnable | To enable checkpoints | TrqOsc |

### DaVinci Interrupt Configuration Changes

|  |  |  |  |
| --- | --- | --- | --- |
| ISR Name | VIM # | Priority Dependency | Notes |
| <None> |  |  |  |

### Manual Configuration Changes

|  |  |  |
| --- | --- | --- |
| Constant | Notes | SWC |
| <None> |  |  |

# Integration

## Required Global Data Inputs

1. TrqOscEnable\_lgc from CF-09 (GM Overlay State Handler)
2. TrqOscAmp\_MtrNm CF-09 (GM Overlay State Handler)
3. TrqOscFreq\_Hz CF-09 (GM Overlay State Handler)

## Required Global Data Outputs

1. TrqOscDCExceeded\_Cnt\_lgc
2. TrqOscCmd\_MtrNm\_f32

## Specific Include Path present

No

# Runnable Scheduling

This section specifies the required runnable scheduling.

|  |  |  |
| --- | --- | --- |
| Init | Scheduling Requirements | Trigger |
| TrqOsc\_Init | None | RTE/Init |

|  |  |  |
| --- | --- | --- |
| Runnable | Scheduling Requirements | Trigger |
| TrqOsc\_Per1() | None | RTE(2ms) |

**.**

# Memory Mapping

## Mapping

|  |  |  |
| --- | --- | --- |
| Memory Section | Contents | Notes |
| TRQOSC\_START\_SEC\_VAR\_CLEARED\_BOOLEAN |  |  |
| TRQOSC\_START\_SEC\_VAR\_CLEARED\_32 |  |  |
| TRQOSC\_START\_SEC\_VAR\_CLEARED\_UNSPECIFIED |  |  |
| RTE\_START\_SEC\_AP\_TRQOSC\_APPL\_CODE |  |  |

\* Each …START\_SEC… constant is terminated by a …STOP\_SEC… constant as specified in the AUTOSAR Memory Mapping requirements.

## Usage

|  |  |  |
| --- | --- | --- |
| Feature | RAM | ROM |
| None |  |  |

Table 1: ARM Cortex R4 Memory Usage

## Non RTE NvM Blocks

|  |
| --- |
| Block Name |
| None |

Note : Size of the NVM block if configured in developer

## RTE NvM Blocks

|  |
| --- |
| Block Name |
| None |

Note : Size of the NVM block if configured in developer

# Compiler Settings

## Preprocessor MACRO

None

## Optimization Settings

None

# Revision Control Log

|  |  |  |  |
| --- | --- | --- | --- |
| **Rev #** | **Change Description** | **Date** | **Author** |
| 1 | Initial version | 02-Apr-2014 | gz7pm0 |