```
5980626 #(`DWC USB3 NUM U3 ROOT PORTS)
5980627 U DWC usb3 debounce filter PowerPresent (
5980628 .mac clk
                             (mac clk),
5980629
        .mac hwr gswr rst n
                            (mac hwr gswr rst n),
                            (reset filt cntr),
5980630
        reset filt cntr
         .reload filt cntr
                             (reload filt cntr),
5980631
                             (pipe3 PowerPresent uf),
5980632
         .dat in
5980633
         .debounce time
                             (time 5ms),
5980634
         .posedge filter
                             (1'b1),
5980635
         .bypass filter
                             (bus filter bypass[1] | sb2ml ssic en),
5980636
        .master_bypass
                             (sb2md filter bypass),
5980637 .ssr restore
                             (ssr restore),
5980638 .restore value
                             (ssr filter cmd [3+`DWC USB3 NUM U3 ROOT PORTS+`DWC USB3 NUM U2 ROOT PORTS-1:3+`DWC USB3 NUM U2 ROOT PORTS]),
5980639 .dat filtered
                             (pipe3 PowerPresent w),
5980640 .filter done
                             (pwr present filter done)
5980641 );
5980642 end // block: i debounce filter PowerPresent
5980643 else begin : noti debounce filter PowerPresent
5980644 assign pipe3 PowerPresent w = 0;
5980645 assign pwr present filter done = {(`DWC USB3 NUM U3 ROOT PORTS){1'b1}};
5980646 end // else: !if(`DWC USB3 SSPHY INTERFACE == 1)
5980647 endgenerate
5980648
5980649 endmodule
5980650 MMMMMMMMMM file E MMMMMMMMMMMMMM
5980651 ./d ip usb ss thang/usb30drd/rtl/taihang usb3/src/bius/u3drd DWC usb3 bus gs.v
5980654 //
5980655 //
5980656 //
5980657 //
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                                 ALL RIGHTS RESERVED
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5980660 // proprietary to Synopsys, Inc. Your use or disclosure of this
5980661 // software is subject to the terms and conditions of a written
5980662 // license agreement between you, or your company, and Synopsys, Inc.
5980663 //
5980664 // The entire notice above must be reproduced on all authorized copies.
5980665 //
5980666 // Component Name : DWC usb3
5980667 // Component Version: 3.30b
5980668 // Release Type
5980669 //
5980670
5980671 //
5980672 // Filename
                       : DWC_usb3_bus_gs.v
5980673 // Author
                        : S.Arvind
5980674 // Release version : 3.30b
5980675 // Date
                        : $Date: 2018/04/23 $
5980676 // File Version : $Revision: #1 $
5980677 // Revision
                        : $Id: //dwh/usb3_iip/rel/usb3_br_3.30a_cust_BF/DWC_usb3/src/bius/DWC_usb3_bus_gs.v#1 $
5980678 // Description
                       : Slave BUS Interface Module. This conditionally instantiates
5980679 //
                          either AHB, AXI, or OCP Slave Interface Module
5980680 //
                      Defines
5980681 //
                        DWBB BUS GS AHB EN or DWBB BUS GS AXI EN or DWBB BUS GS OCP EN
5980682 // Modification History:
```

5980646 end // else: !if(`DWC USB3 SSPHY INTERFACE == 1)