

[illegible]

LDO 5V TO 3.3V

The diagram shows an LDO regulator circuit. The input is connected to a 5V source through capacitor C7 (22uF). The output is connected to a 3.3V source through capacitor C8 (22uF). A feedback resistor R6 (1K) and an LED D4 are connected to the output.

The diagram shows a circuit for USER I/O. It includes a switch SW1 (SW_SPST) connected to a +3.3V supply through a pull-up resistor R8 (10K). The switch is also connected to a capacitor C9 (100n) which is connected to GND. The switch is labeled with 1 and 2. The LED display D3 (LED_RGBA) is connected to the +3.3V supply through resistors R7, R9, and R10 (all 1K). The LED display is labeled with 1, 2, 3, 4, and 5. The LED display is connected to GND through a common connection point.

MOUNTING

The diagram illustrates the mounting of a component on a PCB. It shows four mounting holes labeled H1, H2, H3, and H4. Holes H1, H2, and H4 are connected to GND (Ground) via green lines. Hole H3 is marked with a red 'X' and is not connected to GND.

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