FROM 65[C]22 VIA

CBM IEC SERIAL PORT

Vcc

IC1 7406 1/6   R1   D1
PIN 20 Vcc   1K   1N914   FB1
PIN 39 CA2 CLK_OUT 1   14 2   CLK PIN 4
PIN 1   Vss   7   D2   ATN PIN 3
PIN 2   PA0 CLK_IN   1N914   DATA PIN 5
                                Vss   SRQ PIN 1

                                Vcc   RES PIN 6
                        R2   D3   GND PIN 2
                        1K   1N914   FB2
            IC1 7406 2/6   Vss
PIN 9   PA7 ATN_OUT 3   4   D4
PIN 4   PA2 ATN_IN   1N914
            (UNUSED BY SOFTWARE)   Vss

            Vcc
                        R3   D5
                        1K   1N914   FB3
            IC1 7406 3/6
PIN 19 CB2 DATA_OUT 5   6   D6
PIN 3   PA1 DATA_IN   1N914
                                Vss

                                Vcc
                                      D7
                                     1N914   FB4
PIN 18 CB1 SRQ_IN   D8
                                         1N914
                                         Vss

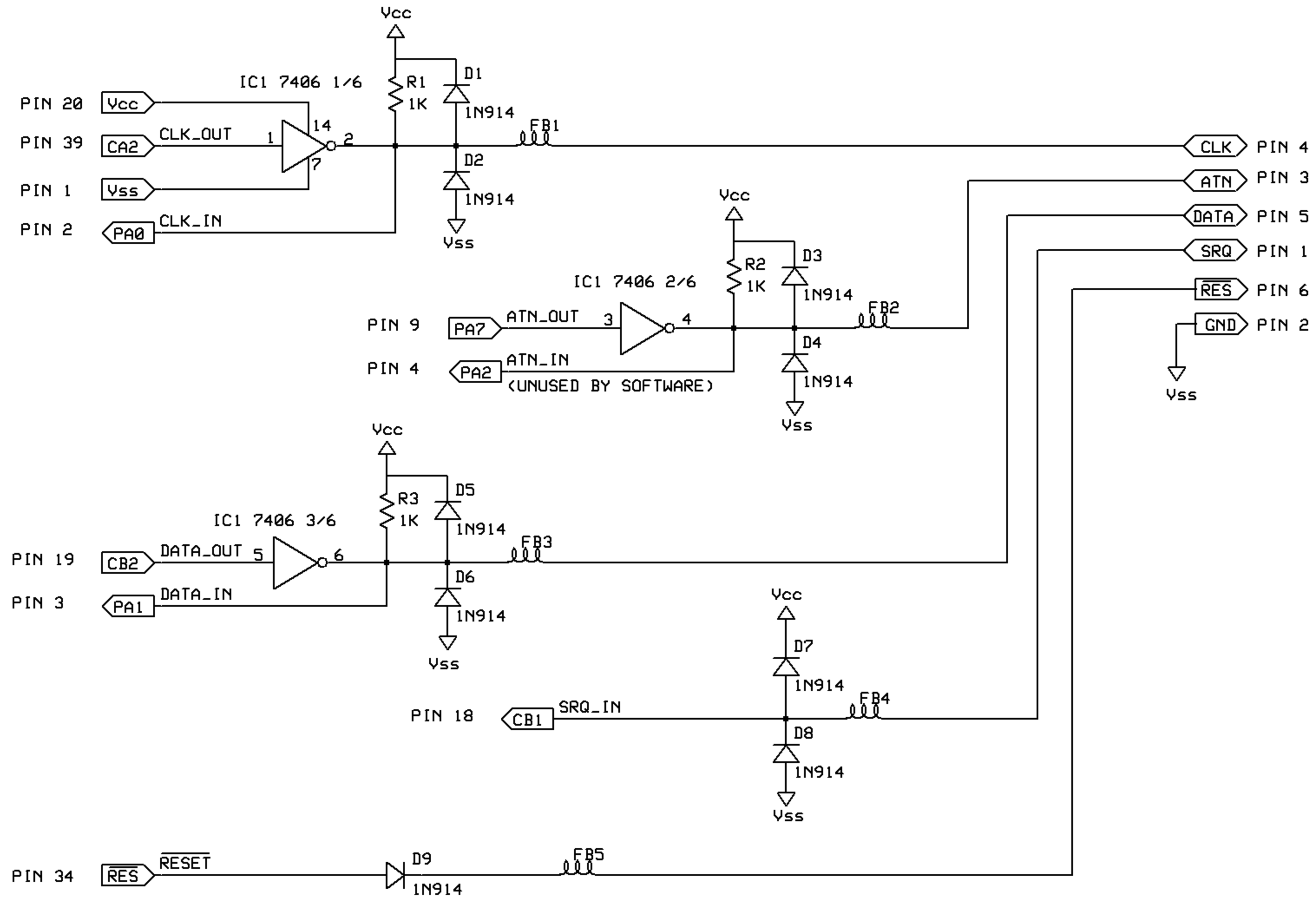   D9   FB5
PIN 34 RES RESET   1N914

NOTES: TIE ALL UNUSED IC1 (7406) INPUTS TO Vcc: PINS 9, 11, 13

       IF IC1 7406 IS NOT IN CLOSE PROXIMITY TO 6522 VIA, INSTALL
       A DECOUPLING CAP (0.1uF) BETWEEN PIN 14 (Vcc) AND PIN 7 (Vss)

       ALL RESISTORS 0.25W / 5%

Commodore IEC Interface		
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