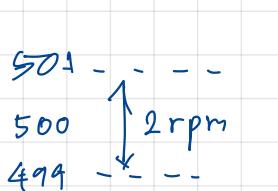


(8 marks) Q5B A DAC is used to control the speed of a motor. The 0 to 4 mA analog current from the DAC is amplified to produce motor speed from 0 to 2000 rpm. Explain how many bits should be used to produce a motor speed that is within 1 rpm of the desired speed?

$$\frac{2000}{1023} = k$$

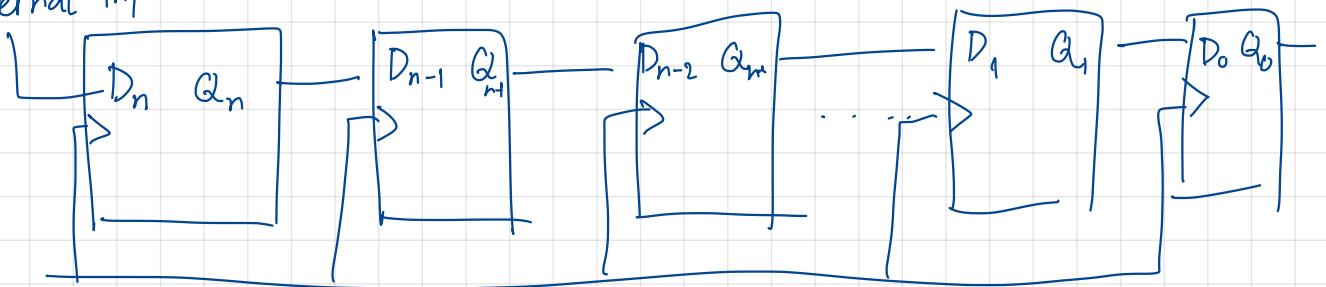


$$\frac{2000}{2} = 1000 \text{ steps} = 2^{10}$$

10 bits

n bits right shifters

external input



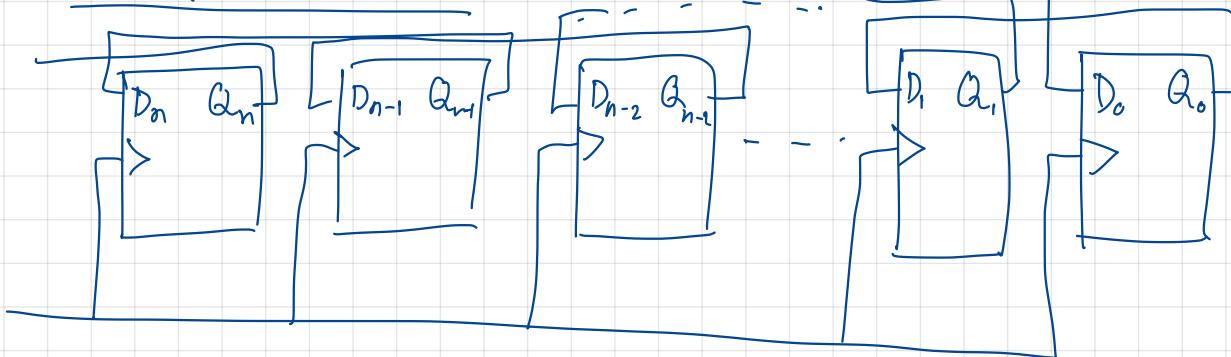
0 1 1 0

± 0 1 1

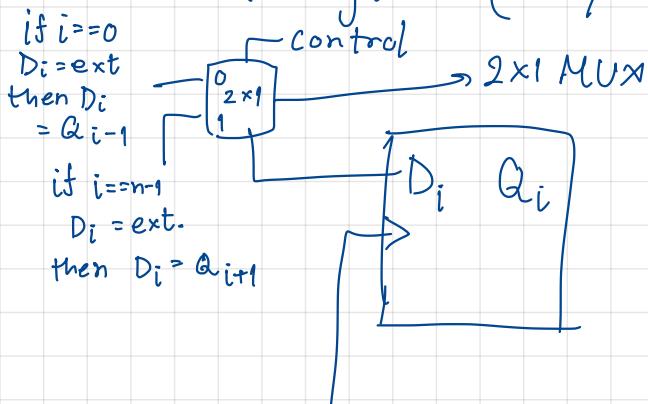
± 1 0 1

n bits left shifters

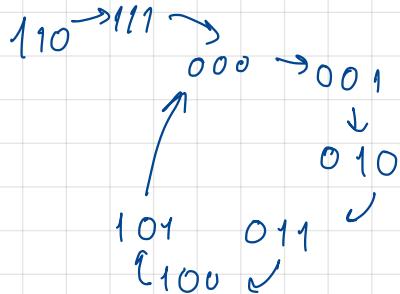
external input



Shift register ($\overline{\text{left/right}}$) $0 \leq i \leq n$



MOD 6 synchronous counter using JK



	J	k
0 → 0	0	x
0 → 1	1	x
1 → 0	x	1
1 → 1	x	0

PS			NS			J _C		k _C		J _B		k _B		J _A		k _A		
C	B	A	C	B	A	0	x	0	x	0	x	1	x	1	x	1	x	
0	0	0	0	0	1	0	x	1	x	1	x	x	1	x	x	1	x	
0	0	1	0	1	0	0	x	0	x	0	x	0	1	x	x	1	x	
0	1	0	0	1	1	0	x	1	x	1	x	1	x	1	x	1	x	
0	1	1	1	0	0	0	x	1	x	1	x	1	x	1	x	1	x	
1	0	0	1	0	1	0	x	0	x	0	x	1	x	1	x	1	x	
1	0	1	0	0	0	0	x	1	x	0	x	0	x	0	x	0	x	
	1	1	0	1	1	1	x	0	x	0	x	1	x	1	x	1	x	
	1	1	1	0	0	0	x	1	x	x	1	x	1	x	1	x	1	x

$$J_A = 1$$

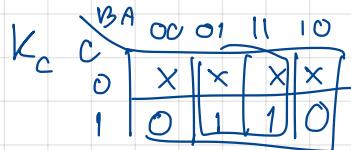
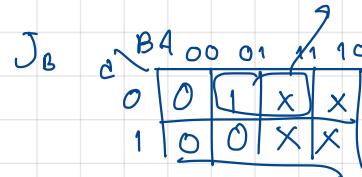
$$k_A = 1$$

$$J_B = \bar{C}A$$

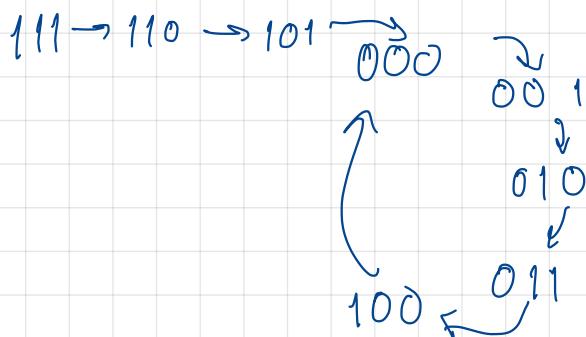
$$k_B = A$$

$$J_C = BA$$

$$k_C = A$$



MOD - 5 synchronous counter using JK



excitation table

	J	k
0 → 0	0	x
0 → 1	1	x
1 → 0	x	1
1 → 1	x	0

PS			NS			J _C		k _C		J _B		k _B		J _A		k _A	
C	B	A	C	B	A	0	x	0	x	0	x	1	x	1	x	1	x
0	0	0	0	0	1	0	x	1	x	1	x	0	1	x	x	1	x
0	0	1	0	1	0	0	x	0	x	0	x	0	1	x	x	1	x
0	1	0	0	1	1	1	x	1	x	1	x	1	x	1	x	1	x
0	1	1	1	0	0	0	x	1	x	1	x	1	x	1	x	1	x
1	0	0	0	0	0	0	x	1	x	1	x	0	0	x	x	1	x
1	0	1	0	0	0	0	x	1	x	1	x	0	0	x	x	1	x
1	1	0	1	0	1	1	x	0	x	0	x	1	x	1	x	1	x
1	1	1	1	1	0	0	x	0	x	0	x	0	0	x	x	0	x

$$k_A = 1$$

$$k_B = \bar{C}A + C\bar{A}$$

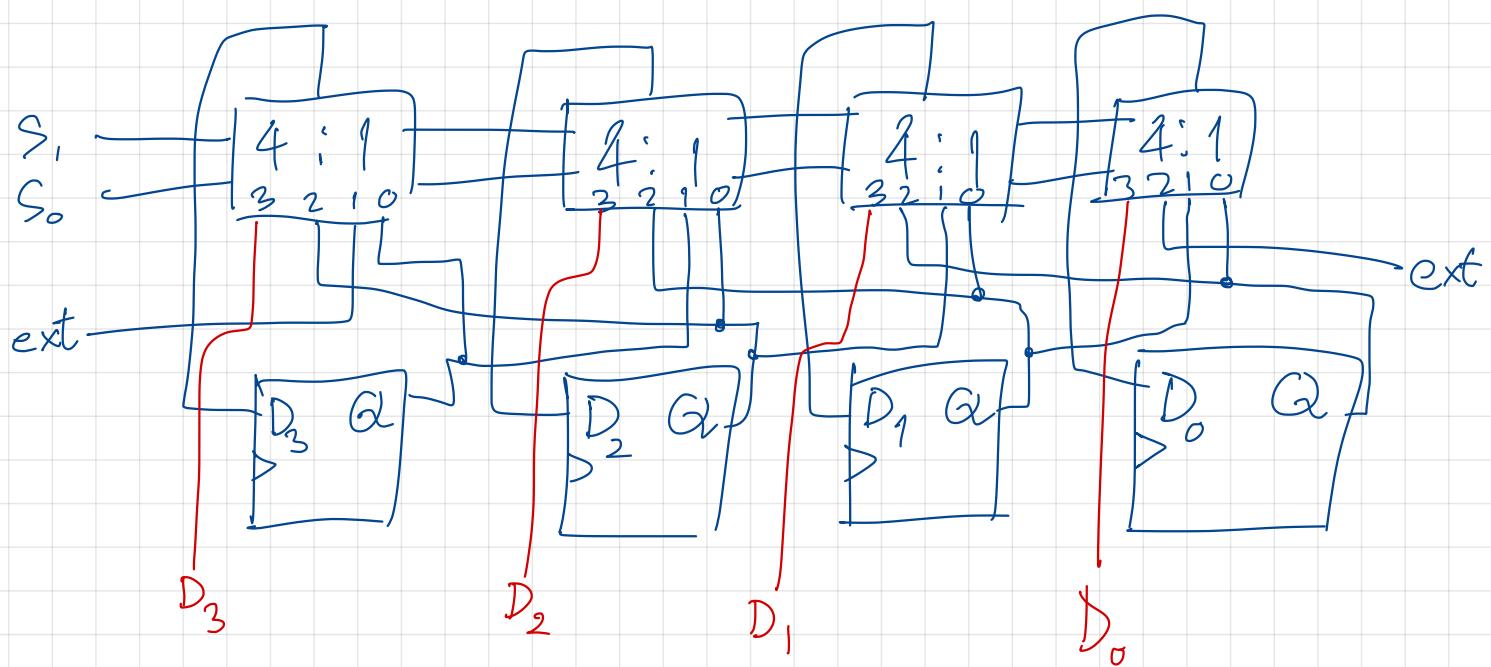
$$J_A = B + \bar{C}$$

$$J_B = \bar{C}A$$

$$k_C = \bar{B}$$

$$J_C = BA$$

S_1	S_0	
0	0	Hold
0	1	R
1	0	L
1	1	Parallel



A	B	B_{in}	Diff	Bout
0	0	0	0	0
0	0	1	1	1
0	1	0	1	1
0	1	1	0	1
1	0	0	1	0
1	0	1	0	0
1	1	0	0	0
1	1	1	1	1