

National Institute of Electronics & Information Technology

Ministry of Electronics and Information Technology,

GOVERNMENT OF INDIA



Certificate of Participation

This is to certify that Dr./Mr./ Ms./ ADITYA SHARMA has participated in '*IEP on FPGA Based Embedded Systems – Covering Swadeshi Microprocessors*' organized by NIELIT CALICUT during 05th to 16th October 2020, supported through C2SD (SMDP-III) Project.



IEP Coordinator



<https://www.nielit.gov.in/calicut>

Executive Director

Certificate No. IEP-OLC 1668

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