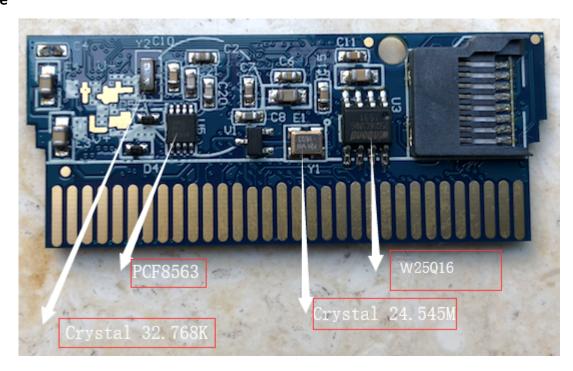
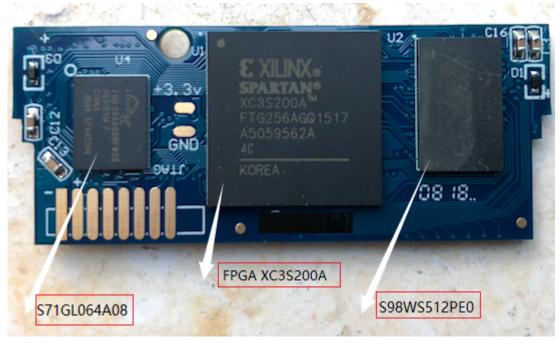
# **EZ-FLASH OMEGA Document**

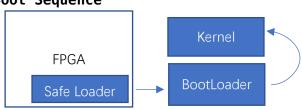
#### Hardware





S71GL064A08 MCP Chip: 64 Megabit NOR flash 8 Megabit SRAM S98WS512PE0 MCP Chip: 512 Megabit NOR flash 256Megabit PSRAM

### **Boot Sequence**



## S71GL064A08 NOR Allocation

Start Address 16bit	End Address 16bit	Size	Content
0×0	0×8000	0x10000 Byte	BootLoader
NUL			
0×20000	<0x120000	Max	Kernel
		0x200000Byte	
0×3D0000	0x3D7FFF	0x2000 Byte	NOR INFO
0x3D8000	0x3DFFFF	0x2000 Byte	System Setting

#### S71GL064A08 SRAM Allocation

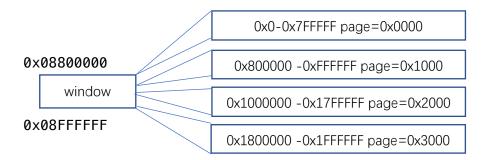
SRAM Start	Size	Content	SetRampage	RTS offset
Address 16bit	3120	content	Secrampage	KIS OTTSEC
0x0	0x10000 Byte	Save1 64K Byte	0×00	
0×8000	-	Save2 64K Byte	0×10	
0×10000	0×10000	0x12C00 VRAM buffer	0x20	
0×18000	0x2C00		0×30	
0×20000		02000000-0203FFFF	0×40	0×00000
0×28000		256K Byte	0×50	0×10000
0×30000		-	0×60	0×20000
0×38000			0×70	0×30000
0×40000	Size 0x8000	03000000-03007FFF	0×80	0×40000
		32K Byte		
0×44000	0×400	05000000-050003FF	0×80	0×48000
		1K Byte		
0×44200		NULL		0×48400
0×48000	0x10000 Byte	06000000-06017FFF	0×90	0×50000
0×50000	0x8000 Byte	(96K Byte)	0×A0	0×60000
0×54000	0x400	07000000-070003FF	0×A0	0×68000
		1K Byte		
0x54200		R4-R11	0xA0	0×68400
0x54800	0×400	04000000-040003FE	0xA0	0×69000
		10		
0×58000				
		FLAG	0×A0	0X6FFF0

## Like EZ3, OMEGA have OS MODE and GAME MODE

## OS Mode: The working mode of BootLoader and Kernel

Start	End	Content	
Address	Address		
0x08000000	0x08040000	BootLoader (ROMPGAE 0x8000)   NOR flash(S71GL064A08)	
0x08040000	0x087FFFFF	Kernel, (ROMPGAE 0x8002)	
0x08800000	0x08FFFFFF	64Mbit(8MByte)window,1Mbit/page, total 256Mbit(32Mbyte) PSRAM	
0×09000000	0x097FFFFF	64Mbit(8Mbyte)window,1Mbit/page, total 512Mbit(64Mbyte) NOR	
0×09E00000		MicroSD read and write buffer address	
0×0E000000	0x0E00FFFF	64 Kbytes SetRampage 0x0 SAVE LOAD	
0×0E010000	0x0E01FFFF	64 Kbytes SetRampage 0x10	
0×0E020000		1 page is 32Kbit, (4Kbyte/page), RTS file load	

SetPSRampage: Set the PSRAM window map address. Available value(0x0000,0x1000,0x2000,0x3000)



SetRampage: Set SRAM Saver offset start address.

Game mode: The working mode for games

PSRAM : SetRompage(0x200)

	. •
0×08000000	Total 32Mbyte Space
0×0A000000	

NOR: SetRompage(rompage) 1Mbit/page

1 3 · 1 3 · / 1 3	
GAME 1 offset 0x0, size 4MByte	SetRompage(0)
GAME 2 offset 0x400000, size 8MByte	SetRompage(0x40)
GAME 3 offset 0xC00000	SetRompage(0xC0)

#### Change Mode

We can toggle between **OS mode** and **Game mode** by set the bit 15 of Rompage to 1 or 0. Set rompage =  $0 \times 8000$ , system goes to **OS mode**. Set rompage(0), system goes to **Game mode**.

PSRAM only have one map address in Game mode, address 0 maps to 0x08000000, Read only.