Final Exam S1 Computer Architecture

Answer on the worksneet		Duration: 1 hr 30 m	ıin.
Last name:	First name:	Group:	

Exercise 1 (2 points)

Convert the following numbers from the source form into the destination form. Do not write down the result in a fraction or a power form (e.g. write down 0.25 and not $\frac{1}{4}$ or 2^{-2}). Write down the result only (do not show any calculation).

Number to Convert	Source Form	Destination Form	Result
10011101.01	Binary	Decimal	
B5.4	Hexadecimal	Decimal	
126	Decimal	Hexadecimal	
101011001.11101	Binary	Hexadecimal	

Exercise 2 (5 points)

Perform the following 8-bit binary operations (the two operands and the result are 8 bits wide). Then, convert the result into unsigned and signed decimal values. If an overflow occurs, write down 'ERROR' instead of the decimal value.

Operation	Binary Result	Decimal Value		
	Dinary Result	Unsigned	Signed	
11101101 + 11101110				
11110000 - 11001010				
01101110 - 11011110				
11111111 – 11111111				
11111111 + 11111111				

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Exercise 3 (4 points)

We want to design the following comparator:

The A and B inputs are 2-bit unsigned integers (A0 and B0 are the LSBs):

- If A > B, the 'A > B' output is set to 1 and the other outputs are set to 0.
- If A = B, the 'A = B' output is set to 1 and the other outputs are set to 0.
- If A < B, the 'A < B' output is set to 1 and the other outputs are set to 0.
- 1. Complete the following truth table:

A1	A0	B1	B0	A > B	A = B	A < B
0	0	0	0			
0	0	0	1	*		
0	0	1	0		******	
0	0	1	1			
0	1	0	0			
0	1	0	I			
0	1	1	0			
0	1	1	1			
1	0	0	0			
1	0	0	1			
1	0	1	0			
1	0	1	1		-	
1	1	0	0			
1	1	0	1			
1	1	1	0			
1	1	1	1			

2.	Without using Karnaugh maps, give the most simplified expression of the $'A = B'$ output. Use the
	EXCLUSIVE-OR operator to simplify the expression. Write down the result only (do not show
	any calculation).



3. Complete the Karnaugh maps below and give the most simplified expressions of the 'A > B' and 'A < B' outputs. No points will be given to an expression if its Karnaugh map is wrong.

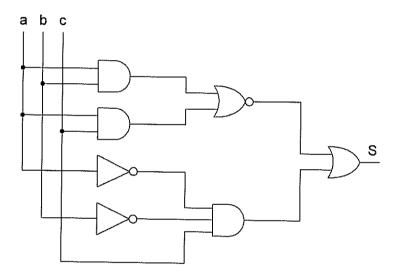
			Bl	B0	
	A < B	00	01	11	10
	00				
41.40	01				
A1 A0	11				
	10				

'A > B' =

'A < B' =

Exercise 4 (3 points)

We want to simplify the following circuit diagram:



1. Without any simplifications, give the S output in terms of a, b and c.

2. Simplify the expression of S by using the algebraic method. Show all calculations.

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3.	From the simplified expression, draw a new circuit diagram by using three NOT gates, one AND gate and one OR gate.

Exercise 5 (6 points) Let us consider the truth tables below. A, B, C and D are the inputs. U, V, W, X, Y and Z are the outputs.

A	В	C	U	V
0	0	0	0	1
0	0	1	1	1
0	1	0	0	1
0	1	1	1	1
1	0	0	0	1
1	0	1	0	0
1	1	0	0	0
1	1	1	0	1

A	В	С	W	X
0	0	0	0	0
0	0	1	1	1
0	1	0	1	1
0	1	1	0	1
1	0	0	0	1
1	0	1	1	0
1	1	0	1	0
1	1	1	0	0

A	В	C	D	Y	Z
0	0	0	0	0	1
0	0	0	1	0	0
0	0	1	0	0	1
0	0	1	1	Φ	0
0	1	0	0	0	0
0	1	0	1	0	Φ
0	1	1	0	0	0
0	1	1	1	Φ	Φ
1	0	0	0	Φ	1
1	0	0	1	Φ	0
<u> </u>	0	1	0	1	1
1	0	1	1	1	0
1	1	0	0	1	0
1	1	0	1	1	Φ
1	1	1	0	1	0
1	<u> </u>	1	1	1	Φ

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	Write do	wn the m	interm c	anonica	Architect I form of	U.				.,	
	Write dov	wn the m	axterm (canonica	l form of	V					
e	each outp	ut. No p	oints wi	ll be giv	ow (circle en to an ne EXCL	expressi	on if its I	Karnaug	most sir h map i	nplified s wrong	expre
			ī	3C					Γ	С	7
	W	00	01	11	10]		X	0	1	
	0	00	U L	11	10			00			_
	1						AB	01 11			-
	W =	<u></u>		1		İ		10			-
	VV						'	X =			_1
			C	D					C	D	
		00	01	11	10		Z	00	01	11	10
	Y	00			1 1		00				
	00	VV									1
3	00	00				AB	01				
3	00 01 11					AB	01 11				
3	00	00				АВ	01				

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